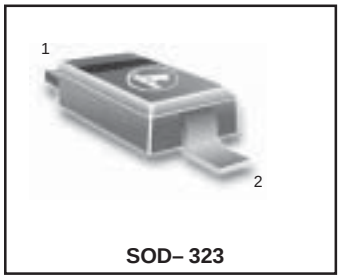
Transient Voltage Suppressors for ESD Protection

LESD3Z5.0CMT1G

General Description

The LESD3Z5.0CMT1G is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.



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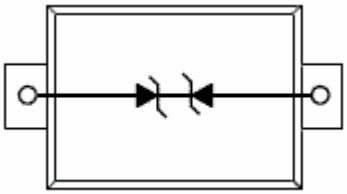


Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Features

- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 200 Watts @ 8 x 20 _s Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000−4−2 Level 4 ESD Protection
- IEC61000−4−4 Level 4 EFT Protection

ORDERING INFORMATION

Device	Marking	Shipping
LESD3Z5.0CMT1G	3M	3000/Tape & Reel

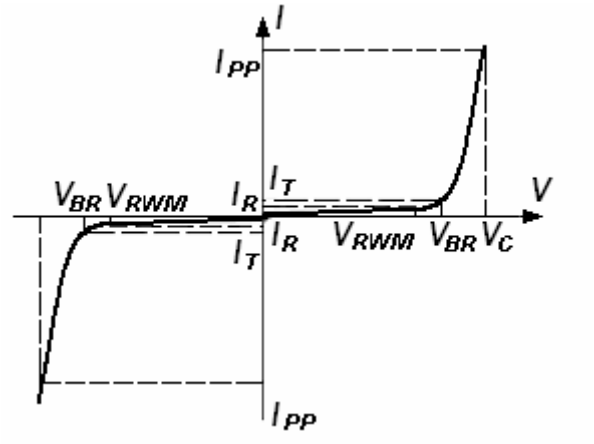
Absolute Ratings (T_{amb}=25°C)

Symbol	Parameter	Value	Units
P_{PP}	Peak Pulse Power (t_p = 8/20 μ s)	200	W
T_L	Maximum lead temperature for soldering during 10s	260	°C
T_{stg}	Storage Temperature Range	-55 to +155	°C
T_{op}	Operating Temperature Range	-40 to +125	°C
T_j	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge	±15 ±8	KV
	IEC61000-4-4 (EFT)	40	A
	ESD Voltage Per Human Body Model	16	KV

LESD3Z5.0CMT1G

Electrical Parameter

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
I_T	Test Current
V_{BR}	Breakdown Voltage @ I_T



Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

Device	V_{RWM} (V)	I_R(uA) @ V_{RWM}	V_{BR} (V)@ I_T (Note 1)	I_T	V_C (V) @ I_{PP}=5 A*	V_C (V) @ Max I_{PP}*	I_{PP} (A)*	P_{PK} (W)*	C (pF)
	Max	Max	Min	mA	Typ	Max	Max	Max	Typ
LESD3Z5.0CMT1G	5.0	1	5.6	1.0	11.6	18.6	9.4	174	25

*Surge current waveform per Figure 1.

1. V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25℃.

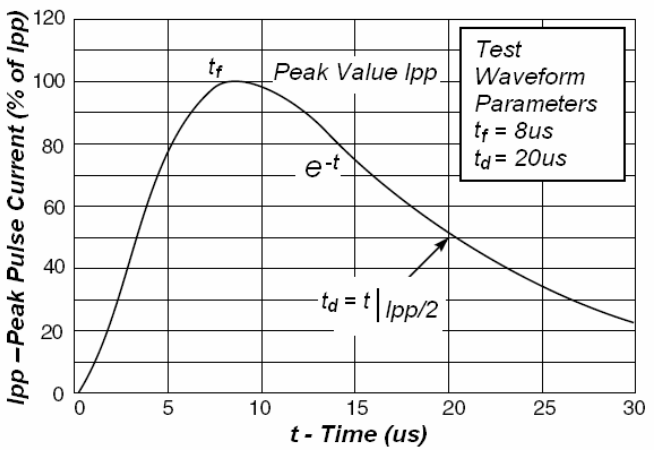


Fig1. Pulse Waveform

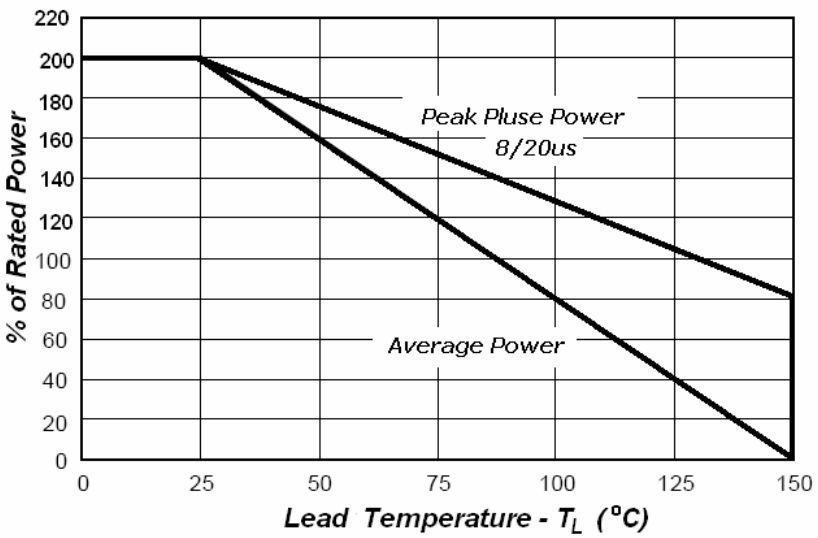


Fig2.Power Derating

Application Note

Electrostatic discharge (ESD) is a major cause of failure in electronic systems. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented.

Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rises above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The LESD3Z5.0CMT1G is the ideal board evel protection of ESD sensitive semiconductor components.

The tiny SOD-323 package allows design flexibility in the design of high density boards where the space saving is at a premium. This enables to shorten the routing and contributes to hardening against ESD.

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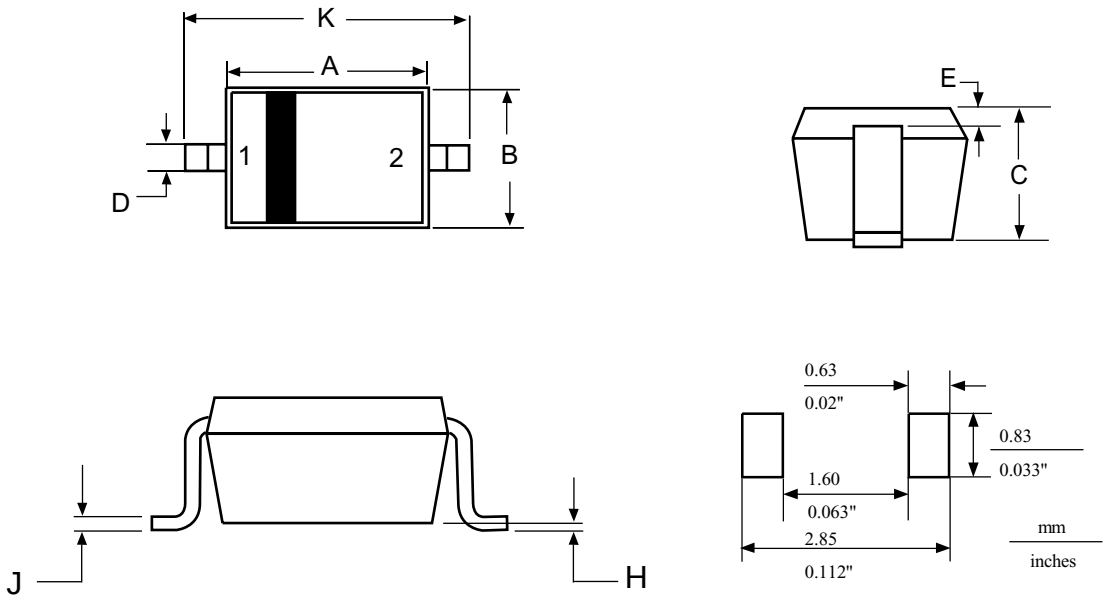
SOD−323



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.60	1.80	0.063	0.071
B	1.15	1.35	0.045	0.053
C	0.80	1.00	0.031	0.039
D	0.25	0.40	0.010	0.016
E	0.15 REF		0.006 REF	
H	0.00	0.10	0.000	0.004
J	0.089	0.177	0.0035	0.0070
K	2.30	2.70	0.091	0.106

PIN: 1. CATHODE
2. ANODE