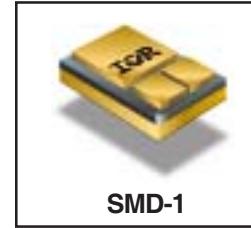


**RADIATION HARDENED  
POWER MOSFET  
SURFACE MOUNT (SMD-1)**

**IRHN57250SE**  
**200V, N-CHANNEL**  
**R5 TECHNOLOGY**

**Product Summary**

Part Number	Radiation Level	RDS(on)	ID
IRHN57250SE	100K Rads (Si)	0.06Ω	31A



International Rectifier's R5™ technology provides high performance power MOSFETs for space applications. These devices have been characterized for Single Event Effects (SEE) with useful performance up to an LET of 80 (MeV/(mg/cm<sup>2</sup>)). The combination of low RDS(on) and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

**Features:**

- Single Event Effect (SEE) Hardened
- Ultra Low RDS(on)
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Ceramic Package
- Light Weight

**Absolute Maximum Ratings**

**Pre-Irradiation**

	Parameter	Units	
ID @ VGS = 12V, TC = 25°C	Continuous Drain Current	A	31
ID @ VGS = 12V, TC = 100°C	Continuous Drain Current		19
IDM	Pulsed Drain Current ①	124	
PD @ TC = 25°C	Max. Power Dissipation	W	150
	Linear Derating Factor	W/°C	1.2
VGS	Gate-to-Source Voltage	V	±20
EAS	Single Pulse Avalanche Energy ②	mJ	300
IAR	Avalanche Current ①	A	31
EAR	Repetitive Avalanche Energy ①	mJ	15
dv/dt	Peak Diode Recovery dv/dt ③	V/ns	7.0
T <sub>J</sub>	Operating Junction	°C	-55 to 150
T <sub>STG</sub>	Storage Temperature Range		
	Pckg. Mounting Surface Temp.	g	300 (for 5s)
	Weight		2.6 (Typical)

For footnotes refer to the last page

**Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	200	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.23	—	$\text{V}/^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $\text{I}_D = 1.0\text{mA}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source On-State Resistance	—	—	0.06	$\Omega$	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 19\text{A}$ ④
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.5	—	4.5	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 1.0\text{mA}$
$g_{\text{fs}}$	Forward Transconductance	18	—	—	S	$\text{V}_{\text{DS}} \geq 15\text{V}, \text{I}_{\text{DS}} = 19\text{A}$ ④
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	—	10	$\mu\text{A}$	$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		—	—	25		$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
$Q_g$	Total Gate Charge	—	—	132	nC	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 31\text{A}$
$Q_{\text{gs}}$	Gate-to-Source Charge	—	—	45		$\text{V}_{\text{DS}} = 100\text{V}$
$Q_{\text{gd}}$	Gate-to-Drain ('Miller') Charge	—	—	60		
$t_{\text{d(on)}}$	Turn-On Delay Time	—	—	35	ns	$\text{V}_{\text{DD}} = 100\text{V}, \text{I}_D = 31\text{A}, \text{V}_{\text{GS}} = 12\text{V}, R_G = 2.35 \Omega$
$t_r$	Rise Time	—	—	125		
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	—	80		
$t_f$	Fall Time	—	—	80		
$L_S + L_D$	Total Inductance	—	4.0	—	nH	Measured from the center of drain pad to center of source pad
$C_{\text{iss}}$	Input Capacitance	—	3860	—	pF	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = 25\text{V}$ $f = 1.0\text{MHz}$
$C_{\text{oss}}$	Output Capacitance	—	628	—		
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	31	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	31	A	$T_J = 25^\circ\text{C}, I_S = 31\text{A}, \text{V}_{\text{GS}} = 0\text{V}$ ④
$I_{\text{SM}}$	Pulse Source Current (Body Diode) ①	—	—	124		
$V_{\text{SD}}$	Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}, I_S = 31\text{A}, \text{V}_{\text{GS}} = 0\text{V}$ ④
$t_{\text{rr}}$	Reverse Recovery Time	—	—	450	nS	$T_J = 25^\circ\text{C}, I_F = 31\text{A}, dI/dt \leq 100\text{A}/\mu\text{s}$ $\text{V}_{\text{DD}} \leq 25\text{V}$ ④
$Q_{\text{RR}}$	Reverse Recovery Charge	—	—	6.0	$\mu\text{C}$	
$t_{\text{on}}$	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ .				

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
$R_{\text{thJC}}$	Junction-to-Case	—	—	0.83	$^\circ\text{C}/\text{W}$	
$R_{\text{thJ-PCB}}$	Junction-to-PC board	—	6.6	—		soldered to a 2" square copper-clad board

Note: Corresponding Spice and Saber models are available on International Rectifier Website.

For footnotes refer to the last page

## Radiation Characteristics

IRHN57250SE

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

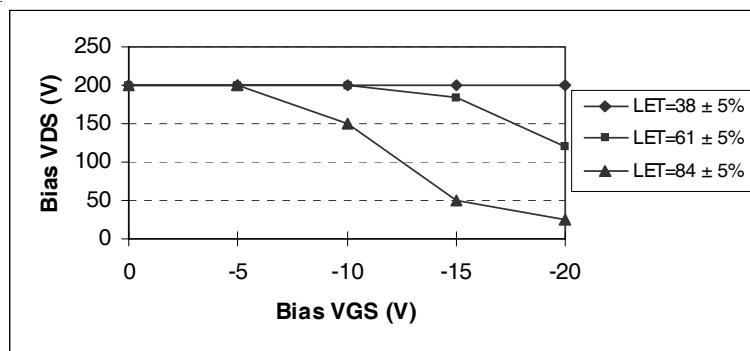
**Table 1. Electrical Characteristics @  $T_j = 25^\circ\text{C}$ , Post Total Dose Irradiation** <sup>⑤⑥</sup>

	Parameter	100K Rads (Si)		Units	Test Conditions
		Min	Max		
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	200	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	4.5		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = 1.0\text{mA}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	10	$\mu\text{A}$	$\text{V}_{\text{DS}}=160\text{V}, \text{V}_{\text{GS}}=0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-3)	—	0.06	$\Omega$	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 19\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (SMD-1)	—	0.06	$\Omega$	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 19\text{A}$
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>④</sup>	—	1.2	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 31\text{A}$

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

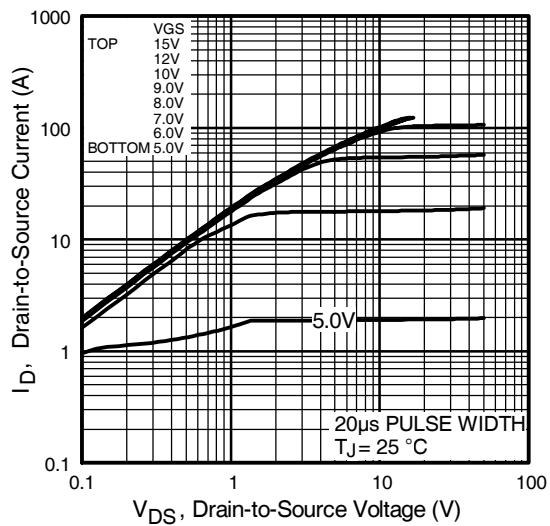
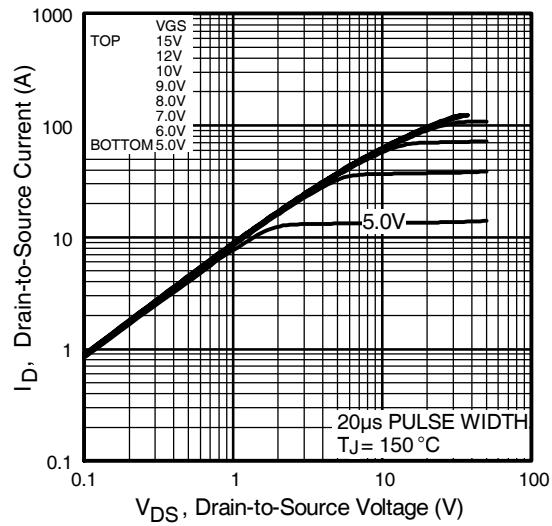
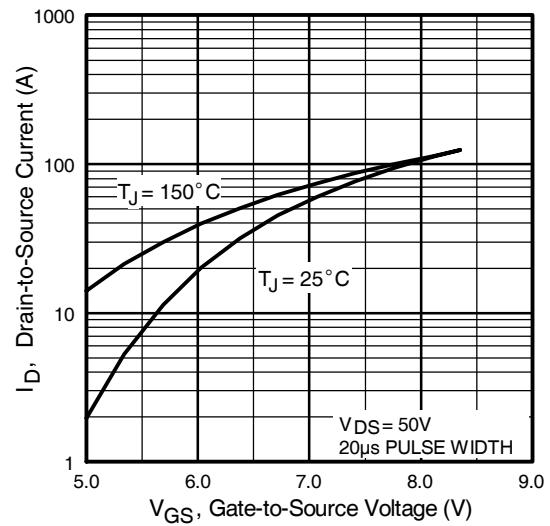
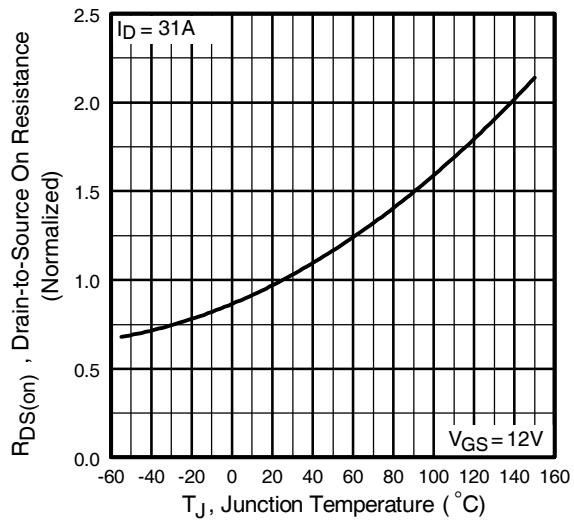
**Table 2. Typical Single Event Effect Safe Operating Area**

LET (MeV/(mg/cm <sup>2</sup> ))	Energy (MeV)	Range (μm)	VDS (V)				
			@ $\text{V}_{\text{GS}} = 0\text{V}$	@ $\text{V}_{\text{GS}} = -5\text{V}$	@ $\text{V}_{\text{GS}} = -10\text{V}$	@ $\text{V}_{\text{GS}} = -15\text{V}$	@ $\text{V}_{\text{GS}} = -20\text{V}$
$38 \pm 5\%$	$300 \pm 7.5\%$	$38 \pm 7.5\%$	200	200	200	200	200
$61 \pm 5\%$	$330 \pm 7.5\%$	$31 \pm 10\%$	200	200	200	185	120
$84 \pm 5\%$	$350 \pm 10\%$	$28 \pm 7.5\%$	200	200	150	50	25



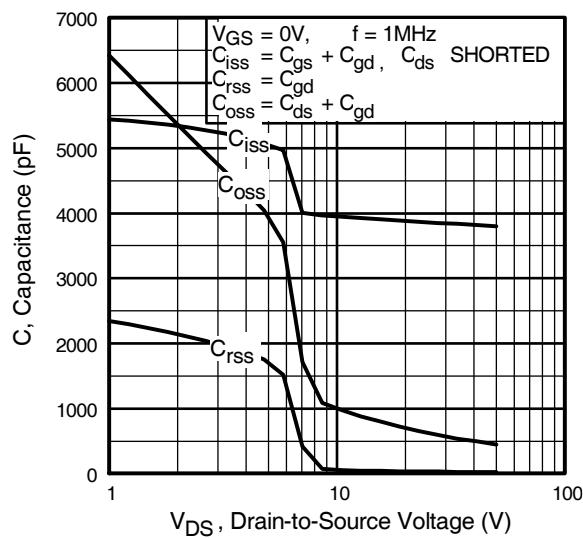
**Fig a.** Single Event Effect, Safe Operating Area

For footnotes refer to the last page

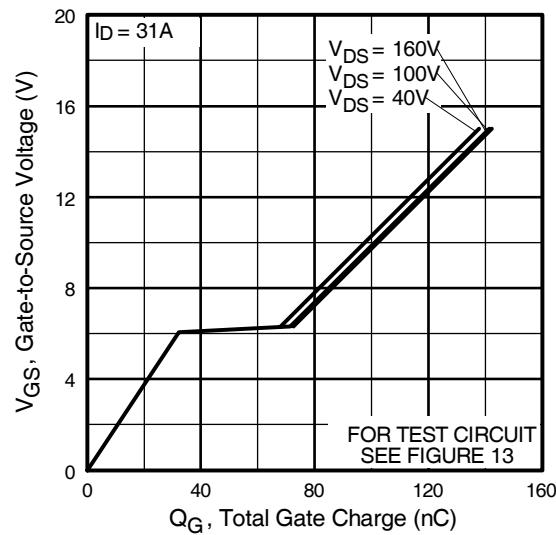
**IRHN57250SE****Pre-Irradiation****Fig 1.** Typical Output Characteristics**Fig 2.** Typical Output Characteristics**Fig 3.** Typical Transfer Characteristics**Fig 4.** Normalized On-Resistance Vs. Temperature

## Pre-Irradiation

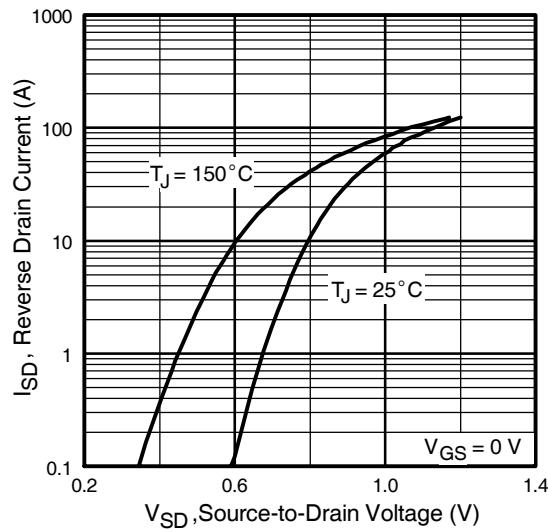
**IRHN57250SE**



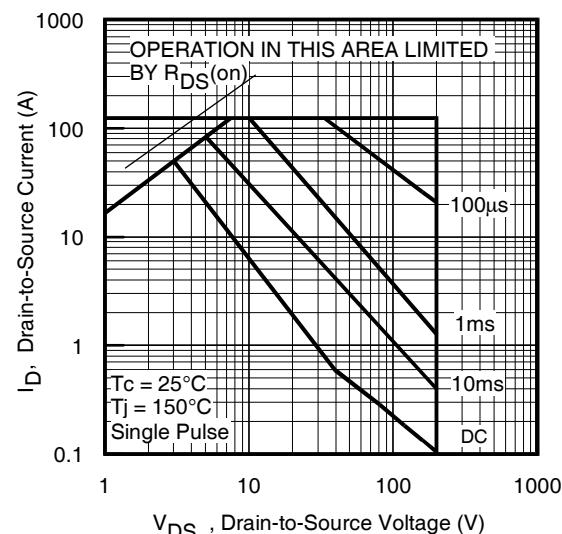
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



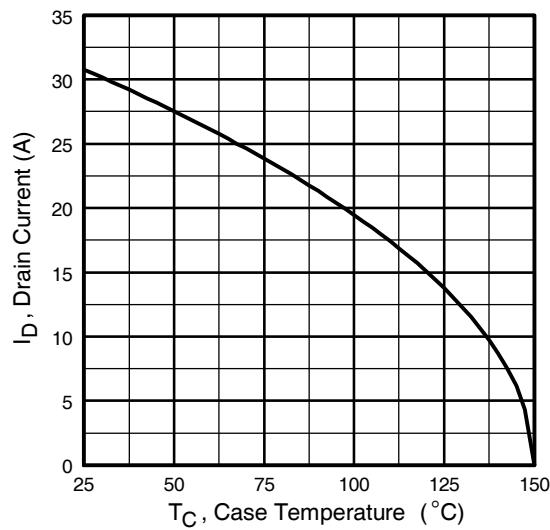
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



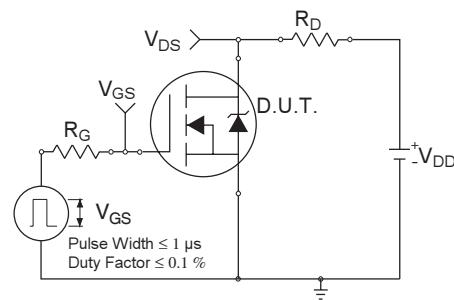
**Fig 8.** Maximum Safe Operating Area

## IRHN57250SE

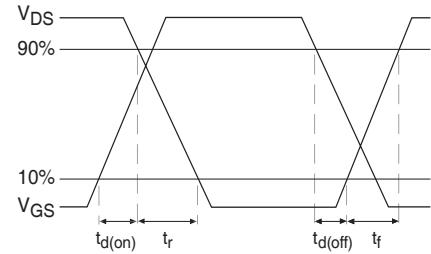
## Pre-Irradiation



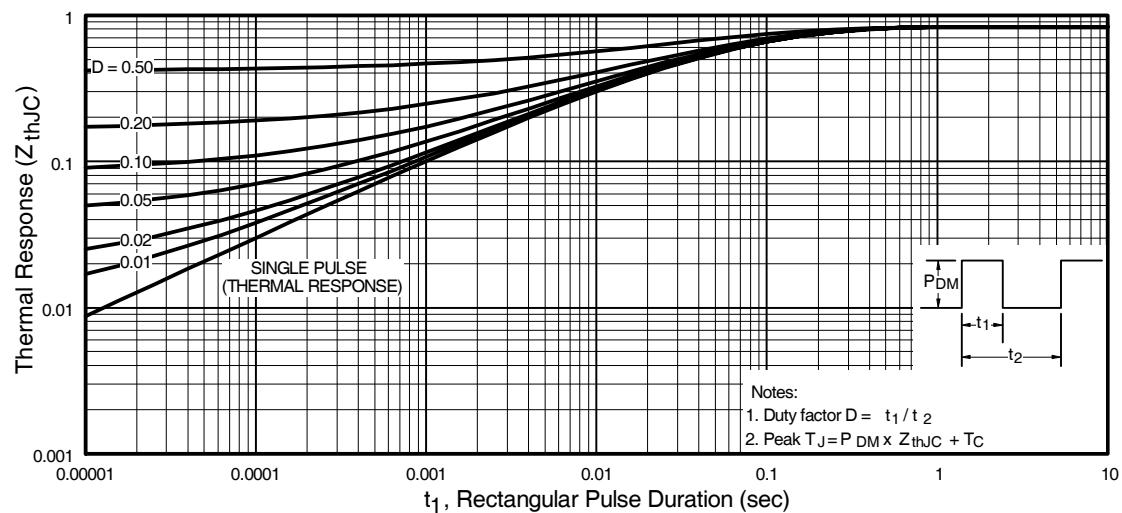
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



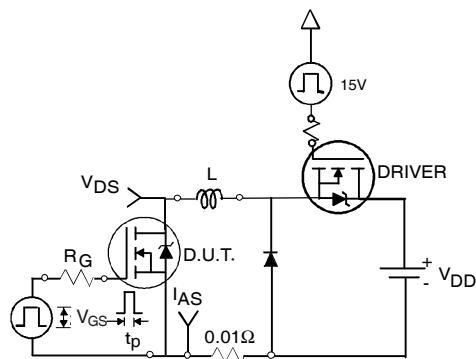
**Fig 10b.** Switching Time Waveforms



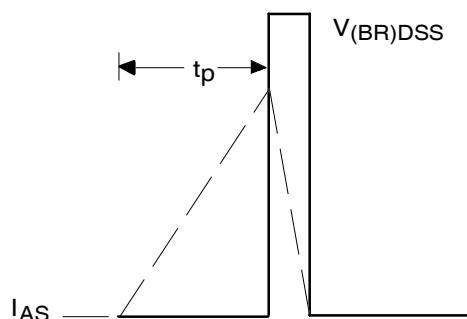
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

## Pre-Irradiation

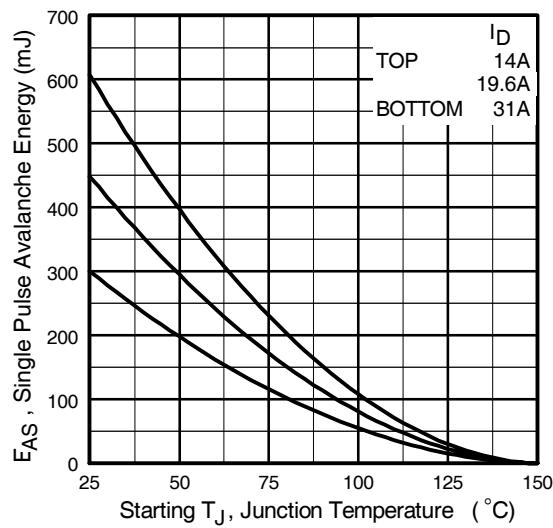
IRHN57250SE



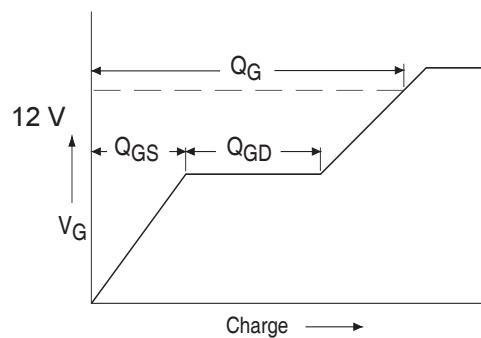
**Fig 12a.** Unclamped Inductive Test Circuit



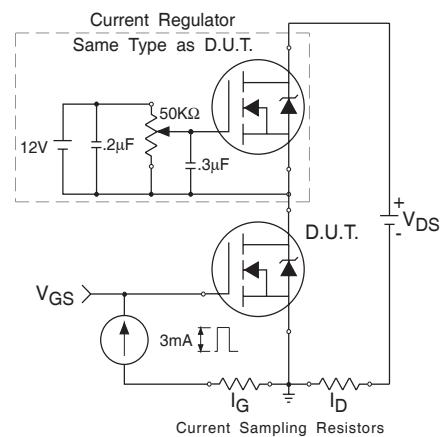
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



**Fig 13a.** Basic Gate Charge Waveform



**Fig 13b.** Gate Charge Test Circuit

