S29GL-S (Pb Package)

3 Volt-Only Flash Memory with Page Mode
featuring 65 nm MirrorBit® Process Technology Device Documentation

Supplement

General Description

This supplementary document provides information on a device designed for limited distribution. It describes how the features, operation, and ordering options of this device have been enhanced or changed from the standard device on which it is based. The information contained in this document modifies any information on the same topics established by the data sheets listed in the Affected Documents/Related Documents table and should be used in conjunction with those documents. This document may also contain information that was not previously covered by the S29GL-S data sheet. It is intended for hardware system designers and software developers of applications, operating systems, or tools.

Affected Documents/Related Documents

Title	Publication Number
S29GL-S MirrorBit Flash Family Data Sheet	S29GL_128S_01GS_00

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1. Device Description

None.

2. Device Bus Operation Changed

None.

3. Embedded Algorithm Performance

None.

4. Electrical Specifications

None.

5. Timing Specifications

None.

6. Ordering Information

Valid Combinations

The Recommended Combinations table lists configurations planned to be available in volume. The table below will be updated as new combinations are released. Consult your local sales representative to confirm availability of specific combinations and to check on newly released combinations.

S29GL-S Valid Combinations					
Base OPN	**Speed (ns)**	**Package and Temperature**	**Model Number**	**Packing Type**	**Ordering Part Number (yy = Model Number, x = Packing Type)**
S29GL01GS	100, 110	FAI (Note 1)	01, 02	0, 3 (Note 2)	S29GL01GS10FAIyyx
					S29GL01GS11FAIyyx
S29GL512S					S29GL512S10FAIyyx
					S29GL512S11FAIyyx
S29GL256S	90, 100				S29GL256S10FAIyyx
					S29GL256S90FAIyyx
S29GL128S					S29GL128S10FAIyyx
					S29GL128S90FAIyyx

Notes:

1. *Additional speed, package, and temperature options maybe offered in the future. Check with your local sales representative for availability.*
2. *Package Type 0 is standard option.*

The ordering part number for the device is formed by a valid combination of the following:

S29GL01GS 10 F A I 01 0

Packing Type
0 = Tray
3 = 13" Tape and Reel

Model Number (V_{IO} and V_{CC} Range)
01 = V_{IO} = V_{CC} = 2.7 to 3.6V, highest address sector protected
02 = V_{IO} = V_{CC} = 2.7 to 3.6V, lowest address sector protected
V1 = V_{IO} = 1.65 to V_{CC}, V_{CC} = 2.7 to 3.6V, highest address sector protected
V2 = V_{IO} = 1.65 to V_{CC}, V_{CC}= 2.7 to 3.6V, lowest address sector protected
S1 = V_{IO} = V_{CC} = 2.7 to 3.6V, highest address sector protected, DYB's power-up in unprotected state
S2 = V_{IO} = V_{CC} = 2.7 to 3.6V, lowest address sector protected, DYB's power-up in unprotected state
S3 = V_{IO} = V_{CC} = 2.7 to 3.6V, highest address sector protected, DYB's power-up in protected state
S4 = V_{IO} = V_{CC} = 2.7 to 3.6V, lowest address sector protected, DYB's power-up in protected state

Temperature Range
C = Extended Commercial (0°C to +85°C)
I = Industrial (–40°C to +85°C)

Package Materials Set
A = Standard

Package Type
F = Fortified Ball-Grid Array Package (LAA064) 11 mm x 13 mm

Speed Option
90 = 90 ns random access time
10 = 100 ns random access time
11 = 110 ns random access time
12 = 120 ns random access time

Device Number/Description
S29GL01GS, S29GL512S, S29GL256S, S29GL128S
3.0 Volt Core, with V_{IO} Option, 1024, 512, 256, 128 Megabit Page-Mode Flash Memory, Manufactured on 65 nm MirrorBit Eclipse Process Technology

7. Revision History

Section	Description
Revision 01 (October 21, 2011)	
	Initial release
Revision 02 (December 14, 2011)	
Ordering Information	Updated Valid Combinations table

Colophon

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