

**April 2015** 

# FGA50T65SHD 650 V, 50 A Field Stop Trench IGBT

#### **Features**

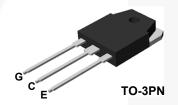
- Maximum Junction Temperature: T<sub>J</sub> =175°C
- · Positive Temperature Co-efficient for Easy Parallel Operating
- · High Current Capability
- Low Saturation Voltage:  $V_{CE(sat)} = 1.6 \text{ V(Typ.)} @ I_C = 50 \text{ A}$
- 100% of the Parts Tested for I<sub>LM</sub>(1)
- · High Input Impedance
- · Fast Switching
- · Tighten Parameter Distribution
- · RoHS Compliant

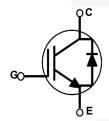
#### **General Description**

Using novel field stop IGBT technology, Fairchild's new series of field stop 3<sup>rd</sup> generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.

### **Applications**

· Solar Inverter, UPS, Welder, Telecom, ESS, PFC





#### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description		FGA50T65SHD	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		650	V
V	Gate to Emitter Voltage		± 20	V
$V_{GES}$	Transient Gate to Emitter Voltage		± 30	V
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 25°C	100	А
ıC	Collector Current	@ T <sub>C</sub> = 100°C	50	А
I <sub>LM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	150	А
I <sub>CM (2)</sub>	Pulsed Collector Current		150	Α
I <sub>F</sub>	Diode Forward Current	@ T <sub>C</sub> = 25°C	60	Α
'F	Diode Forward Current	@ T <sub>C</sub> = 100°C	30	Α
I <sub>FM (2)</sub>	Pulsed Diode Maximum Forward Current		150	Α
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	319	W
гD	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	160	W
TJ	Operating Junction Temperature		-55 to +175	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +175	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	°C	

#### Notes:

- 1.  $\rm V_{CC}$  = 400 V,  $\rm V_{GE}$  = 15 V,  $\rm I_{C}$  =150 A,  $\rm R_{G}$  = 30  $\Omega,$  Inductive Load
- 2. Repetitive rating: Pulse width limited by max. junction temperature

## **Thermal Characteristics**

Symbol	Parameter	FGA50T65SHD	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case, Max.	0.47	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case, Max.	1.25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	°C/W

# Package Marking and Ordering Information

Pare Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGA50T65SHD	FGA50T65SHD	TO-3PN	Tube	-	-	30

# Electrical Characteristics of the IGBT T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	eteristics					
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 1 mA	650	-	-	V
ΔBV <sub>CES</sub> / ΔTJ	Temperature Coefficient of Breakdown Voltage	I <sub>C</sub> = 1 mA, Reference to 25°C	-	0.6	-	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	-	-	250	μΑ
I <sub>GES</sub>	G-E Leakage Current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0 V	-	-	±400	nA
On Charac	teristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = V <sub>GE</sub>	4.0	5.5	7.5	V
()		I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	-	1.6	2.1	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 175°C	-	2.14	-	V
Dynamic C	Characteristics					
C <sub>ies</sub>	Input Capacitance		-	2516	-	pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1 MHz	-	100	-	pF
C <sub>res</sub>	Reverse Transfer Capacitance	- 1 - 11VII 12	-	31	-	pF
Switching	Characteristics		7			
t <sub>d(on)</sub>	Turn-On Delay Time		-	22.4	-	ns
t <sub>r</sub>	Rise Time		-	38.4	- /	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 50 A,	-	73.6	- /	ns
t <sub>f</sub>	Fall Time	$R_G = 6 \Omega$ , $V_{GE} = 15 V$ ,	-	12.8	-	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 25°C	-	1280	/ -	uJ
E <sub>off</sub>	Turn-Off Switching Loss		-	384	- //	uJ
E <sub>ts</sub>	Total Switching Loss		-	1664	-	uJ
t <sub>d(on)</sub>	Turn-On Delay Time		-	20.8	-	ns
t <sub>r</sub>	Rise Time		-	36.8	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 50 \text{ A},$	-	79.2	-	ns
t <sub>f</sub>	Fall Time	$R_G = 6 \Omega$ , $V_{GE} = 15 V$ ,	-	11.2	-	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 175°C	-	1920	-	uJ
E <sub>off</sub>	Turn-Off Switching Loss		-	556	-	uJ
E <sub>ts</sub>	Total Switching Loss		_	2476	_	uJ

# **Electrical Characteristics of the IGBT** (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
Qg	Total Gate Charge		-	87	-	nC
Q <sub>ge</sub>	Gate to Emitter Charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	-	15.7	-	nC
Q <sub>gc</sub>	Gate to Collector Charge	1 V GE = 13 V	-	33.6	-	nC

# Electrical Characteristics of the Diode T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditi	ons	Min.	Тур.	Max	Unit
V <sub>FM</sub>	Diode Forward Voltage	I <sub>E</sub> = 30 A	T <sub>C</sub> = 25°C	-	2.3	2.7	V
<b>*</b> FM	Blode i diward voltage	F = 30 A	T <sub>C</sub> = 175°C	-	1.9	-	1 1
E <sub>rec</sub>	Reverse Recovery Energy		T <sub>C</sub> = 175°C	-	50	-	uJ
t	Diode Reverse Recovery Time	   I <sub>F</sub> =30 A, dI <sub>F</sub> /dt = 200 A/μs	T <sub>C</sub> = 25°C	/ -	34.6	-	ns
<sup>l</sup> rr	Blodd Novolod Noddvoly Time	ης -30 A, αις/αι - 200 A/μ3	T <sub>C</sub> = 175°C	-	197	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge		$T_{\rm C} = 25^{\rm o}{\rm C}$	-	58.6	-	nC
≪II	Disas Noveles Noovely Ollarge		T <sub>C</sub> = 175°C	-	810	-	]

**Figure 1. Typical Output Characteristics** 

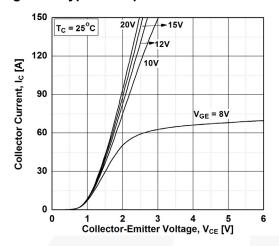


Figure 3. Typical Saturation Voltage Characteristics

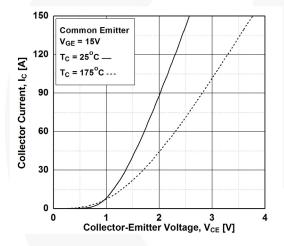
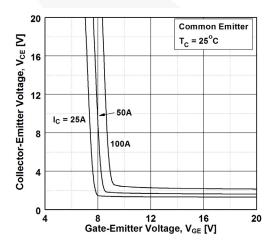


Figure 5. Saturation Voltage vs. V<sub>GE</sub>



**Figure 2. Typical Output Characteristics** 

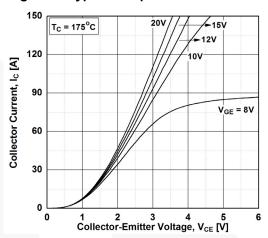


Figure 4. Saturation Voltage vs. Case
Temperature at Variant Current Level

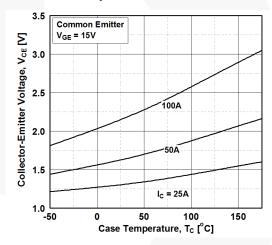


Figure 6. Saturation Voltage vs. V<sub>GE</sub>

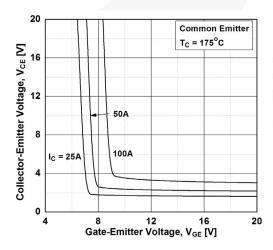


Figure 7. Capacitance Characteristics

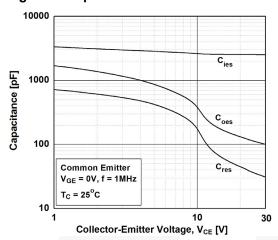


Figure 9. Turn-on Characteristics vs.
Gate Resistance

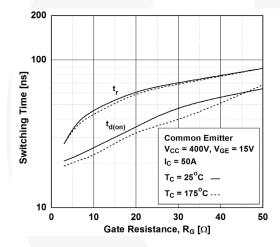


Figure 11. Switching Loss vs.
Gate Resistance

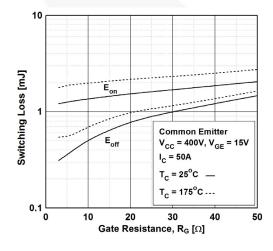


Figure 8. Gate charge Characteristics

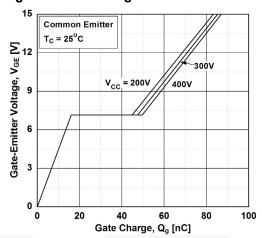


Figure 10. Turn-off Characteristics vs. Gate Resistance

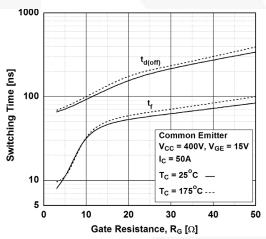


Figure 12. Turn-on Characteristics vs. Collector Current

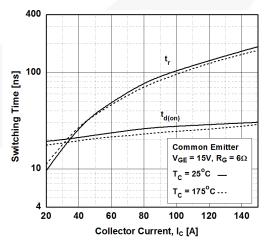


Figure 13. Turn-off Characteristics vs. Collector Current

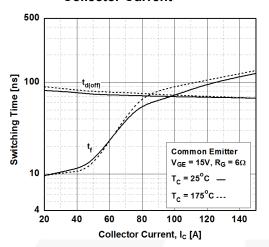


Figure 14. Switching Loss vs. Collector Current

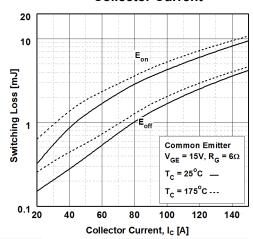


Figure 15. Load Current Vs. Frequency

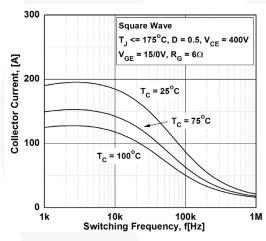


Figure 16. SOA Characteristics

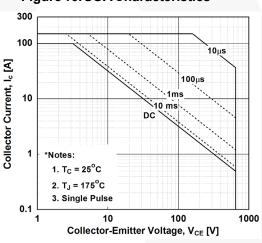


Figure 17. Forward Characteristics

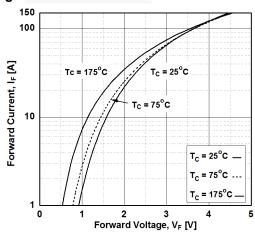


Figure 18. Reverse Recovery Current

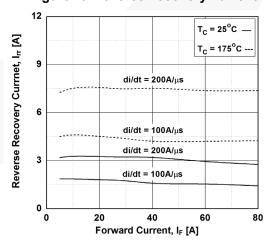


Figure 19. Reverse Recovery Time

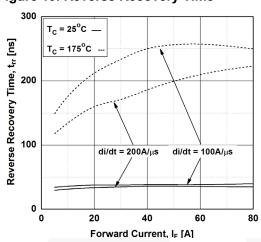


Figure 20. Stored Charge

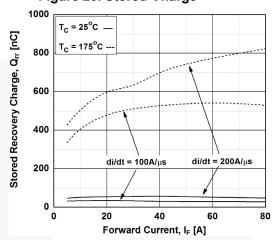


Figure 21. Transient Thermal Impedance of IGBT

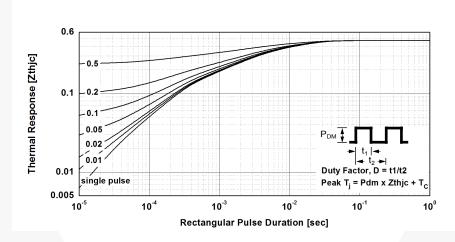
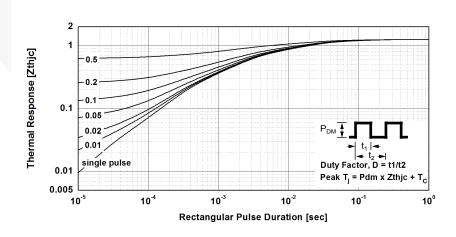
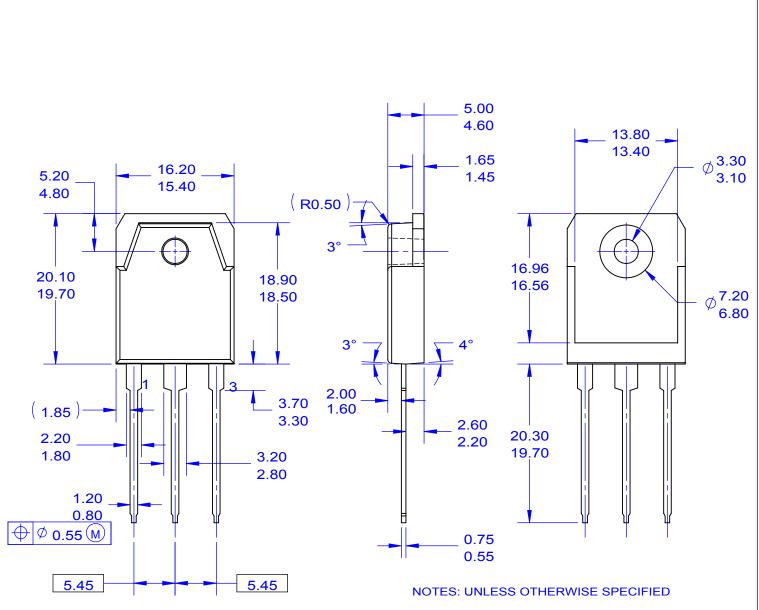
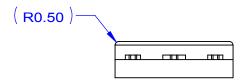


Figure 22.Transient Thermal Impedance of Diode







- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSION AND TOLERANCING PER ASME14.5-2009.
- D) DIMENSIONS ARE EXCLUSSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSSIONS.
- E) DRAWING FILE NAME: TO3PN03AREV2.
- F) FAIRCHILD SEMICONDUCTOR.







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Definition of Terms						
Datasheet Identification	Product Status	Definition				
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.				
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.				
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