



#### Maximun Ratings and Electrical Characteristics at 25°C

| SYMBOL                          | PARAMETER   | COND  | DITIONS       | Value      | Unit             |
|---------------------------------|---|---|---------------|------------|------------------|
| I <sub>T(RMS)</sub>             | RMS On-state Current (full sine wave)                 | All Conduction Angle, $T_c = 100 \ ^{\circ}C$               |               | 25         | А                |
| I <sub>TSM</sub>                | Non-repetitive On-State Current                       | Full Cycle, 60 Hz (t = 16.7 ms)                             |               | 215        | А                |
| I <sub>TSM</sub>                | Non-repetitive On-State Current                       | Full Cycle, 50 H  | z (t = 20 ms) | 200        | А                |
| l <sup>2</sup> t                | Fusing Current  | tp = 10 ms, Half Cycle                                      |               | 205        | A <sup>2</sup> s |
| I <sub>GM</sub>                 | Peak Gate Current                                     | 20 µs max.  | Tj = 125 °C   | 4          | А                |
| P <sub>G(AV)</sub>              | Average Gate Power Dissipation                        | Tj = 125 °C   |               | 1          | W                |
| dl/dt                           | Critical rate of rise of on-state current             | I <sub>G</sub> = 2x I <sub>GT</sub> , t <sub>r</sub> ≤100ns |               | 50         | A/µs             |
|                                 |   | f = 120 Hz, Tj = 125 °C                                     |               |            |                  |
| Tj                              | Operating Temperature                                 |   |               | (-40 +125) | °C               |
| T <sub>stg</sub>                | Storage Temperature                                   |   |               | (-40 +150) | °C               |
| T <sub>sld</sub>                | Soldering Temperature                                 | 10s max   |               | 260        | °C               |
| V <sub>iso</sub>                | R.M.S. isolation voltage 50/60 Hz sinusoidal waveform |   |               | 2.500      | Vac              |
| SYMBOL                          | PARAMETER   | VOLTAGE   |               | Unit       |                  |
|                                 |   | D   | М             | Ν          | Unit             |
| $V_{\text{DRM}}/V_{\text{RRM}}$ | Repetitive Peak Off State Voltage                     | 400   | 600           | 800        | V                |



#### **Electrical Characteristics at Tamb = 25 °C**

| OVMDOL                             | PARAMETER                     | CONDITIONS  | Quadrant |     | SENSITIVITY |      | Unit |
|------------------------------------|-------------------------------|---|----------|-----|-------------|------|------|
| SYMBOL                             | PARAMETER                     | PARAMETER CONDITIONS Qu   |          |     | 14          | 16   |      |
| I <sub>GT</sub> <sup>(1)</sup>     | Gate Trigger Current          | $V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25$ °                             | C Q1÷Q3  | MAX | 35          | 50   | mA   |
| V <sub>GT</sub>                    | Gate Trigger Voltage          | $V_D = 12 V_{DC}, R_L = 33 \Omega, \ T_j = 25 \ ^\circ$                   | C Q1÷Q3  | MAX | 1           | .3   | V    |
| $V_{\text{GD}}$                    | Gate Non Trigger Voltage      | $V_D = V_{DRM}, R_L = 3.3 \text{ K}\Omega, T_j = 125 \circ$               | C Q1÷Q3  | MIN | 0.2         |      | V    |
| I <sub>H</sub> <sup>(2)</sup>      | Holding Current               | $I_T$ =100 mA, Gate open, $T_j$ = 25 °                                    | С        | MAX | 50          | 75   | mA   |
| ۱L                                 | Latching Current              | $I_{G} = 1.2 I_{GT}, T_{j} = 25 \text{ °C}$                               | Q1,Q3    | MAX | 70          | 80   | mA   |
|                                    |                               |   | Q2       | MAX | 80          | 100  |      |
| dV/dt <sup>(2)</sup>               | Critical Rate of Voltage Rise | $V_D = 0.67 \times V_{DRM}$ , Gate open                                   |          | MIN | 500         | 1000 | V/µs |
|                                    |                               | T <sub>j</sub> = 125 °C   |          |     |             |      |      |
| (dl/dt)c <sup>(2</sup>             | Critical Rate of Current Rise | $(dv/dt)c = 0.1 V/\mu s$ T <sub>j</sub> = 125 °                           | C        | MIN | -           | -    | A/ms |
|                                    |                               | $(dv/dt)c = 10 V/\mu s$ T <sub>j</sub> = 125 °                            |          | MIN | -           | -    |      |
|                                    |                               | witthout snubber $T_j = 125$ °  |          | MIN | 13          | 22   |      |
| V <sub>TM</sub> <sup>(2)</sup>     | On-state Voltage              | $I_T = 35 \text{ Amp, tp} = 380 \mu\text{s, T}_j = 25 ^{\circ}\text{H}_j$ |          | MAX | 1.55        |      | V    |
| $V_{t(0)}^{(2)}$                   | Threshold Voltage             | T <sub>j</sub> = 125 °C   |          | MAX | 0.85        |      | V    |
| r <sub>d</sub> <sup>(2)</sup>      | Dynamic resistance            | T <sub>j</sub> = 125 °C   |          | MAX | 16          |      | mΩ   |
| I <sub>DRM</sub> /I <sub>RRM</sub> | Off-State Leakage Current     | $V_D = V_{DRM},$ $T_j = 125 \circ$  | 0        | MAX | 2           | 2    | mA   |
|                                    |                               | $V_{\rm R} = V_{\rm RRM},$ $T_{\rm j} = 25$ °                             |          | MAX | Ę           | 5    | μA   |
| R <sub>th(j-c)</sub>               | Thermal Resistance            | for AC 360° conduction angle  |          |     | 2           | .5   | °C/W |
|                                    | Junction-Case                 |   |          |     |             |      |      |
| R <sub>th(j-a)</sub>               | Thermal Resistance            |   |          |     | 5           | 5    | °C/W |
|                                    | Junction-Ambient              |   |          |     |             |      |      |

(1) Minimum  $I_{GT}$  is guaranted at 5% of  $I_{GT}$  max.

(2) For either polarity of electrode MT2 voltage with reference to electrode MT1.

### **Part Number Information**





#### **Ordering information**

| PREFERRED P/N | PACKAGE CODE | DELIVERY MODE | BASE QUANTITY | UNIT WEIGHT (g) |
|---------------|--------------|---------------|---------------|-----------------|
| FT2514MW 00TU | TU           | TUBE          | 1,000         | 2.00            |

### Package Outline Dimensions: (mm) TO-220F





#### Ratings and Characteristics (Ta 25 °C unless otherwise noted)



Fig. 3: Relative variation of thermal impedance versus pulse duration.



Fig. 5: Surge peak on-state current versus number of cycles



Fig. 2: RMS on-state current versus case temperature (full cycle).



Fig. 4: On-state characteristics (maximum values)



Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 20 ms, and corresponding value of  $l^{2}t$ .





## Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)



Fig. 8: Relative variation of critical rate of decrease of main current versus junction temperature





#### **Revision History**

| Date        | Revision | Description of Changes                                       |
|-------------|----------|--|
| 14-Jun-2011 | 0        | Original Data Sheet  |
| 12-Apr-2017 | 1        | Change values of: $I_{TSM}$ / $I^2t$ / $r_d$ / $R_{th(j-a)}$ |

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