

DATASHEET

Infrared Receiver Module IRM-H5XXM/TR2 series



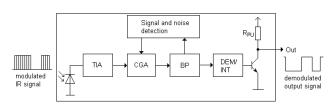
Pin Configuration

(1): **OUT**

②: Vcc

③: GND

Block Diagram



Features

- · High protection ability against EMI
- · Available for various carrier frequencies
- · Min burst length: 6 cycles
- · Min gap length: 10 cycles
- · Low operating voltage and low power consumption
- · High immunity against ambient light
- · Optimized immunity against TFT backlight interferences
- Long reception range
- · High sensitivity
- · Pb free and RoHS compliant
- · Compliance with EU REACH
- Compliance Halogen Free (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)

Description

The device is miniature SMD type infrared receiver that has been developed and designed by utilizing the latest IC technology.

The PIN diode and preamplifier are assembled onto a lead frame and molded into an epoxy package which operates as an IR filter.

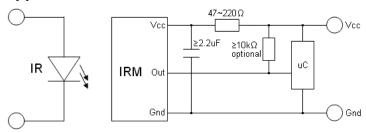
The demodulated output signal can directly be decoded by a microprocessor



Applications

- AV equipment such as TV, VCR, DVD, CD, MD, etc.
- Short pause time protocols
- Toy applications
- · CATV set top boxes
- Multi-media Equipment
- Other devices using IR remote control

Application Circuit



Parts Table

Model No.	Carrier Frequency
IRM-H538M/TR2	38 kHz



Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{cc}	6	V
Operating Temperature	T _{opr}	-20 ~ +80	$^{\circ}$ C
Storage Temperature	T _{stg}	-40 ~ +85	$^{\circ}$ C
Soldering Temperature *1	T _{sol}	260	$^{\circ}\!$

^{*1}Soldering time < 5 seconds

Electro-Optical Characteristics (T_a=25°C, V_{cc}=3V)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Current consumption	Icc		0.4	0.6	mA	No input signal
Supply voltage	V _{CC}	2.7	-	5.5	V	
Peak wavelength	λ_{p}		940		nm	
Reception range	L ₀	8				
	L ₄₅	5			m	
Half angle(horizontal)	ϕ_{h}		±45		deg	See chapter ,Test method ^{*2}
Half angle(vertical)	ϕ_{v}		±45		deg	
High level pulse width	Тн	450		700	μs	Test signal according to figure 1 *3
Low level pulse width	T_L	500		750	μs	
High level output voltage	V_{OH}	Vcc-0.4			V	$I_{SOURCE} \leq 1 \mu A$
Low level output voltage	V_{OL}		0.2	0.5	V	I _{SINK} ≦2mA

^{*2} The ray receiving surface at a vertex and relation to the ray axis in the range of θ =0° and θ =45°.
^{*3} A range from 30cm to the arrival distance. Average value of 50 pulses.



Test method

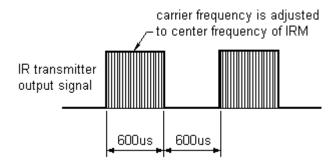
The specified electro-optical characteristics are valid under the following conditions.

- 1. Measurement environment
 - A place without extreme light reflections.
- 2. External light

The environment contains an ordinary, white fluorescent lamp without high frequency modulation. The color temperature is 2856K and the illumination at the IR receiver is less than 10 Lux ($Ev \le 10Lux$).

- 3. Standard transmitter
 - The test transmitter is calibrated by using the circuit shown in figure 2. The radiation intensity of the transmitter is adjusted until **Vo=400mVp-p.** Both, the test transmitter and the photo diode, have a peak wavelength of 940nm. The photo diode for calibration is PD438B (λp=940nm, Vr=5V).
- 4. The measurement system is shown in Fig.-3

Fig.-1 Transmitter Wave Form



D.U.T output Pulse

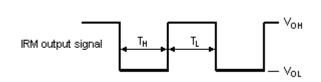


Fig.-2 standard transmitter calibration

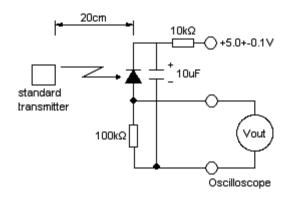
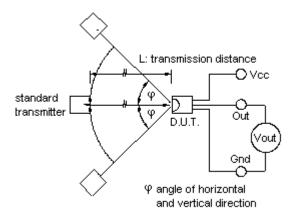
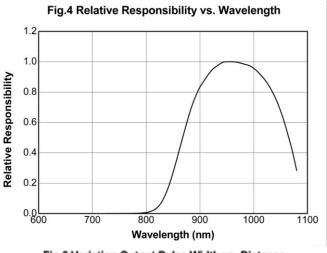


Fig.-3 Measuring System





Typical Electro-Optical Characteristics Curves



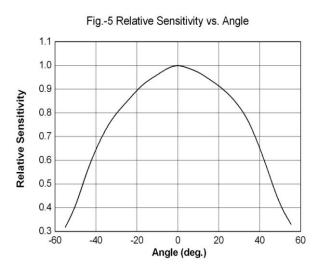
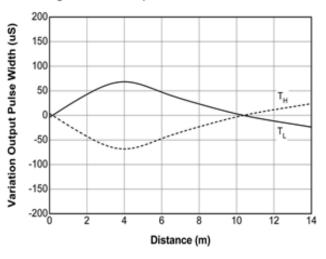
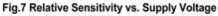


Fig.6 Variation Output Pulse Width vs. Distance





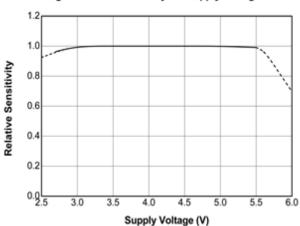
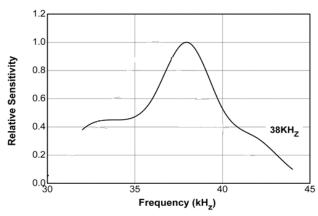
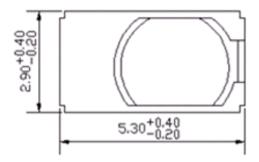


Fig.8 Relative Sensitivity vs. Frequency



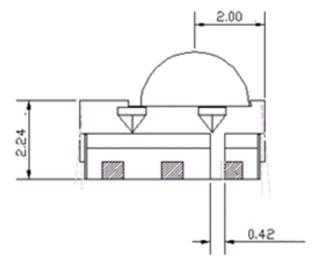


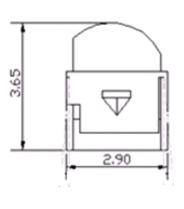
Package Dimension

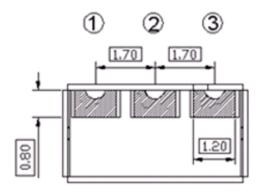


Pin Function

- ①: Vout
- ②: Vcc
- ③: GND







Notes:

- 1. All dimensions are in millimeters.
- 2. Tolerances unless dimensions ±0.5mm.

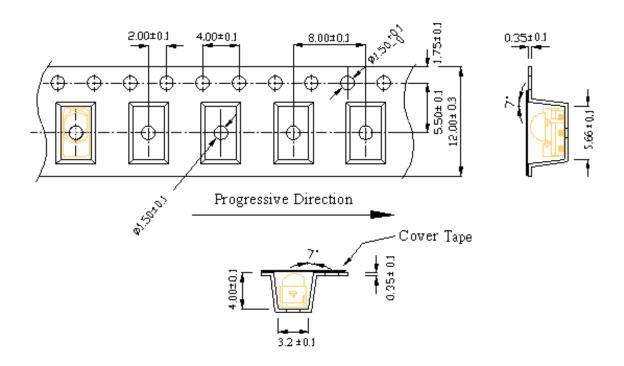


Code information

Protocol	Suitable	Protocol	Suitable
JVC	Yes	Sharp	Yes
Matsushita	Yes	Sony 12 Bit	Yes
Mitsubishi	Yes	Sony 15 Bit	No
NEC	Yes	Sony 20 Bit	No
RC5	Yes	Toshiba	Yes
RC6	Yes	XMP-1	Yes
RCA	Yes	r-step ¹⁾	Yes

¹⁾ For r-step 38kHz version M3 is the best choice, for r-step 56kHz version only M is recommended.

Tape & Reel Packing Specifications (Dimensions in mm)



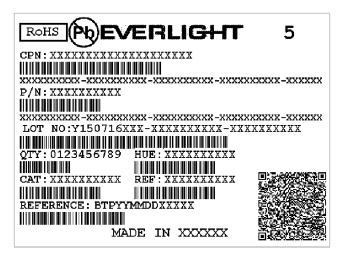
Packing Quantity

2000 pcs / Reel

5 Reels / Carton



Label format



Moisture Classification-storage and used condition label



Notes: These labels are only the examples and please be according to the actual shipping labels.

Recommended method of storage

The following are general recommendations for moisture sensitive level (MSL) 4 storage and use:

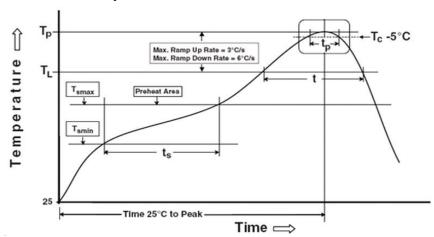
- 1. Shelf life in sealed bag from the bag seal date: 12 months at 10°C ~30°C and < 90% relative humidity (RH)
- After bag is opened, devices that will be subjected to reflow solder or other high temperature process must mounted within 72 hours of factory conditions at 10°C~30°C and 60%RH.
- If the moisture absorbent material (silica gel) has faded away or the IRM has exceeded the storage time. Baking treatment is required, refer to IPC/JEDEC J-STD-033 for bake procedure or recommend the conditions: 96 hours at 60°C ± 5°C and < 5 % RH.



ESD Precaution

Proper storage and handing procedures should be followed to prevent ESD damage to the devices especially when they are removed from the Anti-static bag. Electro-Static Sensitive Devices warning labels are on the packing.

Solder Reflow Temperature Profile



Note: Reference: IPC/JEDEC J-STD-020D

Preheat

Temperature min (T _{smin})	150 °C
Temperature max (T _{smax})	200°C
Time $(T_{smin} \text{ to } T_{smax})$ (t_s)	60-120 seconds
Average ramp-up rate (T _{smax} to T _p)	3 °C/second max

Other

Liquidus Temperature (T _L)	217 °C
Time above Liquidus Temperature (t L)	60-100 sec
Peak Temperature (T _P)	260°C
Time within 5 °C of Actual Peak Temperature: T _P - 5°C	30 s
Ramp- Down Rate from Peak Temperature	6°C /second max.
Time 25°C to peak temperature	8 minutes max.
Reflow times	2 times

Note:

- 1. Suggest that reflow soldering should not be done more than two times.
- 2. When soldering, do not put stress on the IRM device during heating.
- 3. After soldering, do not warp the circuit board.



Application Restrictions

- 1. Above specification may be changed without notice. EVERLIGHT will reserve authority on material change for above specification.
- 2. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
- 3. When using this product, please observe the absolute maximum ratings and the instructions for use outlined in these specification sheets. EVERLIGHT assumes no responsibility for any damage resulting from use of the product which does not comply with the absolute maximum ratings and the instructions included in these specification sheets.
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