



DFLR1800

1.0A SURFACE MOUNT GLASS PASSIVATED RECTIFIER PowerDI®123

Product Summary (@TA = +25°C)

V _{RRM} (V)	I _O (A)	V _{FMAX} (V)	I _{RMAX} (μΑ)
800	1	1.1	10

Features and Benefits

- Ideally Suited for Automated Assembly
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- Patented Interlocking Clip Design for High Surge Capacity, US Patent #7,095,113

Description and Applications

Packaged in the compact thermally efficient POWERDI123 package, the DFLR1800 provides high surge capacity and high efficiency. It is ideally suited to use in

- AC/DC Adaptors/Chargers
- DC/DC Converters
- Power Supply

Mechanical Data

- Case: PowerDI123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (approximate)



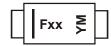
Top View

Ordering Information (Note 4)

Part Number	Marking Code	Case	Packaging
DFLR1800-7	F18 or F1 <u>8</u>	PowerDI [®] 123	3000/Tape & Reel

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Fxx = Product Type Marking Code F18 or F18 = DFLR1800YM = Date Code Marking Y = Year (ex: B = 2014)M = Month (ex: 9 = September)

Date Code Key

Year	201) 20)11	2012	2013	2014	2015	201	16	2017	2018	2019
Code	Х		Υ	Z	Α	В	С	D)	E	F	G
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	800	٧
Average Rectified Output Current (see figure 4)	Ю	1.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	25	Α

Thermal Characteristics

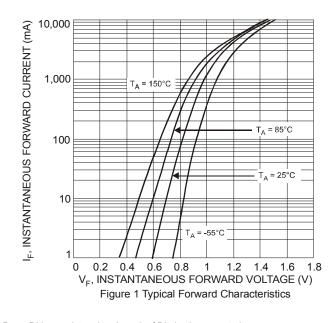
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ hetaJA}$	134	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 6)	$R_{ heta JS}$	_	6	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	_	-65 to +150	°C

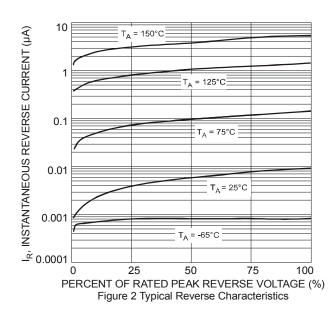
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	800	_	_	V	$I_R = 10\mu A$
		_	0.65	_		I _F = 1.0mA, T _J = 0°C
		_	0.60	_		$I_F = 1.0 \text{mA}, T_J = +25 ^{\circ}\text{C}$
Forward Voltage Drop	V_{F}	_	0.48	_		$I_F = 1.0 \text{mA}, T_J = +85 ^{\circ}\text{C}$
		_	0.94	1.1		I _F = 1.0A, T _J = +25°C
		_	0.83	1.0		I _F = 1.0A, T _J = +125°C
Poverse Leekage Current (Note 7)	1	_	_	10	μA	V _R = 800V, T _J = +25°C
Reverse Leakage Current (Note 7)	I _R	_	_	150	μΑ	$V_R = 800V, T_J = +125^{\circ}C$
Total Capacitance	C _T	_	10	_	pF	$V_R = 4.0V_{DC}$, $f = 1MHz$

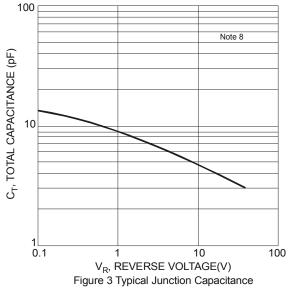
Notes:

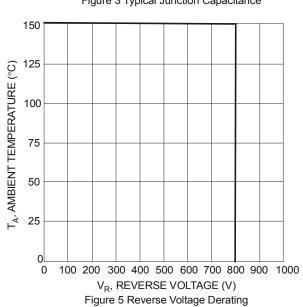
- 5. Device mounted on 1" x 1", FR-4 PCB; 2 oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf. T_A = +25°C
- 6. Theoretical R_{OJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
- 7. Short duration test pulse used to minimize self-heating effect.
- 8. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

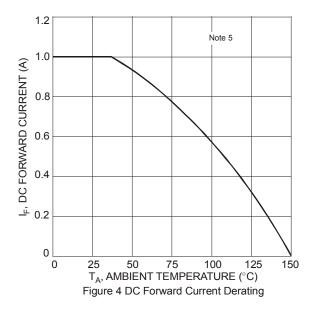


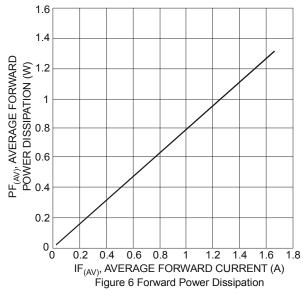






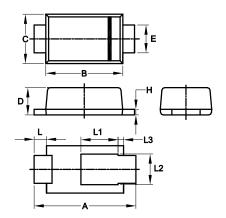






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

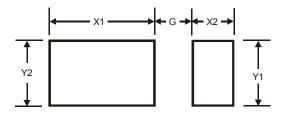


POWERDI [®] 123							
Dim	Min	Max	Тур				
Α	3.50	3.90	3.70				
В	2.60	3.00	2.80				
С	1.63	1.93	1.78				
D	0.93	1.00	0.98				
Е	0.85	1.25	1.00				
Н	0.15	0.25	0.20				
L	0.40	0.50	0.45				
L1	1.25	1.40	1.35				
L2	1.025	1.125	1.10				
L3	0.125	0.275	0.20				
All D	All Dimensions in mm						



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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