



LIN-BUS BIDIRECTIONAL TVS DIODE

Product Summary

V _{BR (Min)}	IPP (Max)	C _{T (Typ)}
25.4V & 17.1V	3A	13pF

Features and Benefits

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DESD1LIN2WSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This DIODES™ DESD1LIN2WSQ is a next generation ESD and surge protection device packaged in a small footprint surface mount package. It is qualified to AEC-Q101, supported by a PPAP and is designed to protect one data line of the Local Information Network (LIN) in an automotive.

• LIN bus protections

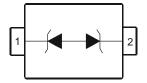
Mechanical Data

- Package: SOD323
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.005 grams (Approximate)

SOD323



Top View



Device Schematic

Ordering Information (Note 4)

Part Number	Paakaga	Marking	Reel Size (inches)	Tape Width (mm)	Packing		
Fait Number	Package	Marking Reel St.	Reel Size (Inches)	rape widin (inin)	Qty.	Carrier	
DESD1LIN2WSQ-7	SOD323	A24	7	8	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



A24 = Product Type Marking Code Bar Denotes Pin 1



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	Ppp	160	W	8/20µs, Per Figure 1
Peak Pulse Current	IPР	3.0	Α	8/20µs, Per Figure 1
ESD Protection – Contact Discharge	V _{ESD_Contact}	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_Air}	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

			T			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage, from Pin 1 to Pin 2	V _{RWM1}		—	15	V	_
Reverse Standoff Voltage, from Pin 2 to Pin 1	VRWM2	_	_	24	V	_
Channel Leakage Current, from Pin 1 to Pin 2 (Note 6)	I _{RM1}		1	50	nA	V _{RWM} = 15V
Channel Leakage Current, from Pin 2 to Pin 1 (Note 6)	I _{RM2}		1	50	nA	V _{RWM} = 24V
Breakdown Voltage, from Pin 1 to Pin 2	V _{BR1}	17.1	18.9	20.3	V	$I_R = 1mA$
Breakdown Voltage, from Pin 2 to Pin 1	V _{BR2}	25.4	27.8	30.3	V	I _R = 1mA
Olemania a Maltana da an Ria 4 ta Ria 0	V _{CL1}	_	_	25	V	$I_{PP} = 1A, t_P = 8/20 \mu s$
Clamping Voltage, from Pin 1 to Pin 2		_	_	35	V	$I_{PP} = 5A, t_P = 8/20\mu s$
Olemania a Malta ara farana Bia O ta Bia 4	.,	_	_	40	V	$I_{PP} = 1A, t_P = 8/20 \mu s$
Clamping Voltage, from Pin 2 to Pin 1	V _{CL2}	_	_	50	V	$I_{PP} = 3A$, $t_P = 8/20 \mu s$
Clamping Voltage TLP, from Pin 1 to Pin 2	VcL	_	23.5	_	V	I _{TLP} = 16A, t _P = 100ns
Olamping Voltage TEI , Holli I III I to I III 2	VCL	_	26.6	_	V	ITLP = 30A, tP = 100ns
Clamping Voltage TI D. from Din 2 to Din 1		_	33	_	V	I _{TLP} = 16A, t _P = 100ns
Clamping Voltage TLP, from Pin 2 to Pin 1 V _{CL}		_	37.7	_	V	I _{TLP} = 30A, t _P = 100ns
Differential Resistance	Rdif	_	0.5	_	Ω	I _R = 1A, t _P = 8/20µs
Channel Input Canacitance	Ст	_	13	17	pF	V _R = 0V, f = 1MHz
Channel Input Capacitance		_	_	100	pF	V _R = 12V, f = 100kHz

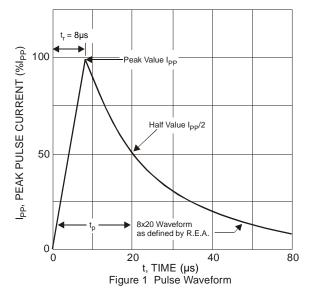
Notes:

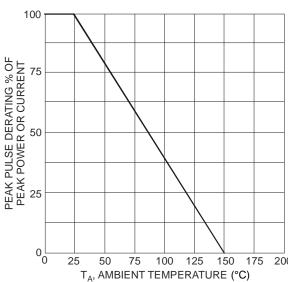
^{5.} Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

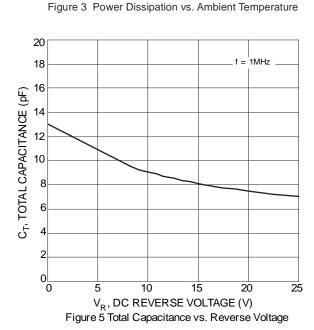
^{6.} Short duration pulse test used to minimize self-heating effect.

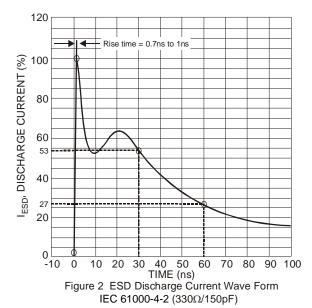


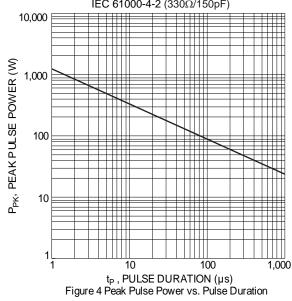


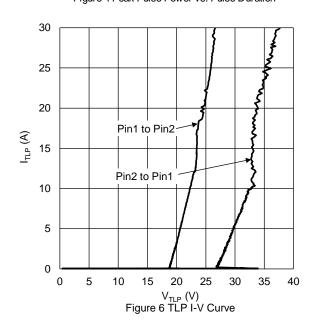




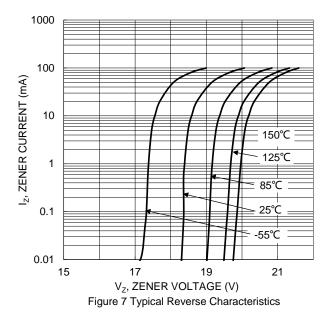


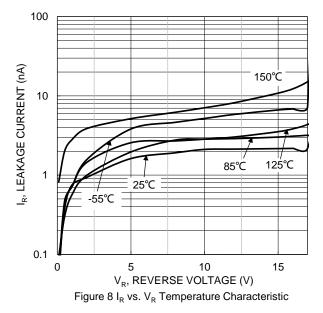










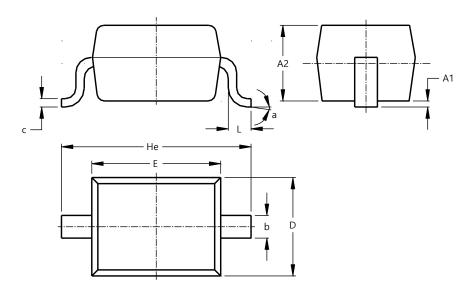




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD323

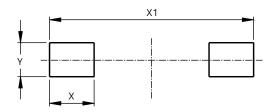


SOD323						
Dim	Min	Max	Тур			
A1		0.10	0.05			
A2	1.00	1.10	1.05			
b	0.25	0.35	0.30			
C	0.10	0.15	0.11			
D	1.20	1.40	1.30			
Е	1.60	1.80	1.70			
He	2.30	2.70	2.50			
L	0.20	0.40	0.30			
а	00	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD323



Dimensions	Value (in mm)
Х	0.590
X1	2.700
Υ	0.450



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