

NOT RECOMMENDED FOR NEW DESIGN CONTACT US



DMN60H3D5SK3

600V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON) Max	I _D Tc = +25°C	
600V	3.5Ω @ V _{GS} = 10V	2.8A	

Description

This new generation complementary MOSFET features low onresistance and fast switching, making it ideal for high efficiency power management applications.

Applications

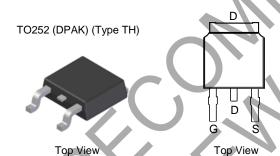
- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

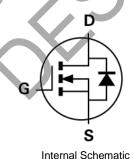
Features

- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Package: TO252
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Fiñish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.33 grams (Approximate)





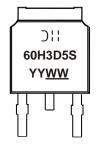
Ordering Information (Note 4)

- =					
I	Part Number		Packago	Paci	king
	Part Number		г аскауе	Qty.	Carrier
ı	DMN60H3D5SK3-13	TO)252 (DPAK) (Type TH)	2,500	Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ Hamufacturer's Marking

60H3D5S = Product Type Marking Code

YYWW = Date Code Marking

YY or YY = Last Two Digits of Year (ex: 21 = 2021)

WW or WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	600	V		
Gate-Source Voltage	V_{GSS}	±30	V		
Continuous Drain Current (Note 5) V _{GS} = 10V	lD	2.8 1.8	А		
Maximum Body Diode Forward Current (Note 5)	ls	2.5	Α		
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	4.4	Α
Avalanche Current, L = 60mH (Note 7)			las	1.0	Α
Avalanche Energy, L = 60mH (Note 7)			Eas	30	mJ
Peak Diode Recovery dv/dt (V _{DD} = 400V, I _D = 2.7A)			dv/dt	2.7	V/ns

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	41 16	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{eJA}	46	°C/W
Thermal Resistance, Junction to Case (Note 5)	Reuc	3.0	C/VV
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

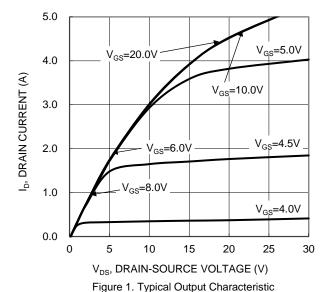
2 1	0 1 1		-		11.11	T 10 1111	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)		13					
Drain-Source Breakdown Voltage	BVDSS	600			V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS		_	1.0	μΑ	$V_{DS} = 600V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	\ -	±100	nA	$V_{GS} = \pm 30V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(TH)	2.0	3.1	4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	. —	2.7	3.5	Ω	V _G S = 10V, I _D = 1.5A	
Diode Forward Voltage	V _{SD}		0.9	1.5	V	$V_{GS} = 0V, I_{S} = 3.0A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		354	_			
Output Capacitance	Coss		41	_	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	4	_			
Gate Resistance	R _G		2.6		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	12.6	_			
Gate-Source Charge	Qgs	_	1.7	_	nC	$V_{DS} = 480V, I_{D} = 2.5A$	
Gate-Drain Charge	Qgd	_	7.1	_			
Turn-On Delay Time	td(ON)	_	10.6	_			
Turn-On Rise Time	t _R	_	22	_		$V_{GS} = 10V$, $V_{DD} = 300V$, $R_G = 25\Omega$, $I_D = 2.5A$	
Turn-Off Delay Time	tD(OFF)	_	34	_	ns		
Turn-Off Fall Time	t _F	_	28	_			
Body Diode Reverse Recovery Time	trr	_	198	_	ns	\\ 0\\ - 0\\ - 0.50 d \/dt 4000\/\\-	
Body Diode Reverse Recovery Charge	Qrr	_	952	_	nC	$V_{GS} = 0V$, $I_{S} = 2.5A$, $dI/dt = 100A/$	

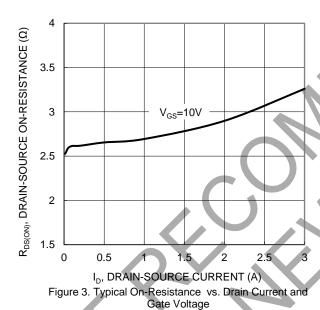
Notes:

- 5. Device mounted on infinite heatsink.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 7. Guaranteed by design. Not subject to production testing.
 8. Short duration pulse test used to minimize self-heating effect.









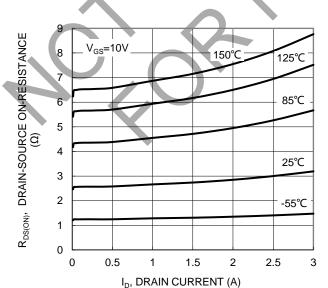
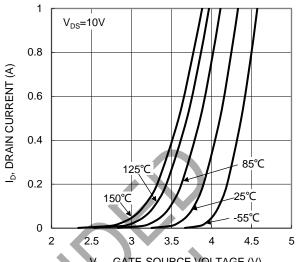


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

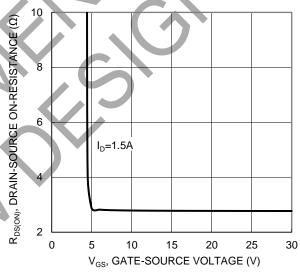


Figure 4. Typical Transfer Characteristic

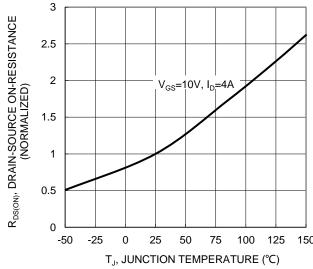


Figure 6. On-Resistance Variation with Temperature





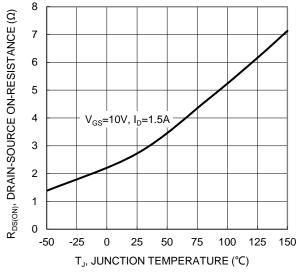


Figure 7. On-Resistance Variation with Temperature

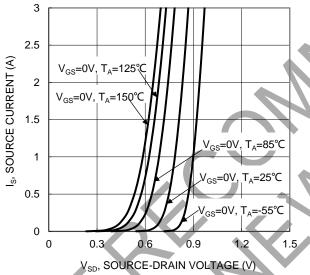


Figure 9. Diode Forward Voltage vs. Current

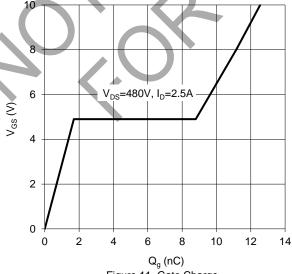


Figure 11. Gate Charge

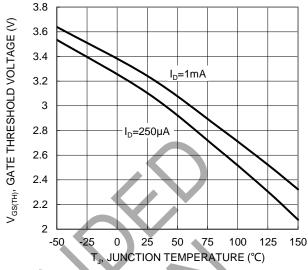
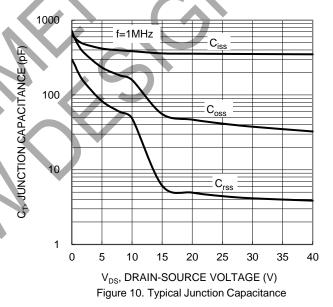


Figure 8. Gate Threshold Variation vs. Temperature



10 R_{DS(ON)} Limited ID, DRAIN CURRENT (A) =1ms 0.1 P_w=100ms $T_{J(MAX)}$ =150°C T_A=25°C DC Single Pulse DUT on infinite heatsink V_{GS} =10V0.01 10 1000 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



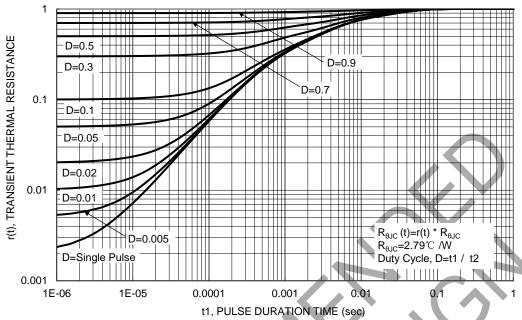


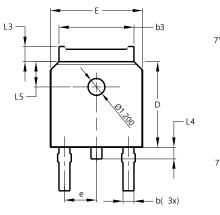
Figure 13. Transient Thermal Resistance

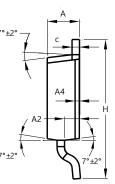


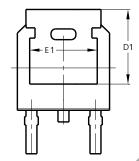
Package Outline Dimensions

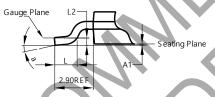
Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK) (Type TH)







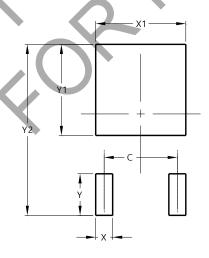


TO252 (DPAK)							
	(Type TH)						
Dim	Min	Max	Тур				
Α	2.20	2.38	2.30				
A 1	0.00	0.10	-				
A2	0.97	1.17	1.07				
A4	0	.10 RE	F				
b	0.72	0.85	0.78				
b3	5.23	5.45	5.33				
O	0.47	0.58	0.53				
D	6.00	6.20	6.10				
D1	5.30 REF						
е	2.	286 BS	C				
П	6.50	6.70	6.60				
E	4.70	4.92	4.83				
Ŧ	9.90	10.30	10.10				
F	1.40	1.70	1.60				
L2	0.51 BSC						
L3	0.90	1.25	-				
L4	0.60	1.00	0.80				
L5	1.70	1.90	1.80				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK) (Type TH)



Dimensions	Value (in mm)			
C	4.572			
X	1.060			
X1	5.632			
Υ	2.600			
Y1	5.700			
Y2	10.700			



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