



Product Summary

BV _{DSS}	RDS(ON) max	I _D max T _A = +25°C
2014	45mΩ @ V _{GS} = 4.5V	4.5A
20V	$55m\Omega @ V_{GS} = 2.5V$	4.1A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208@4

G2

Internal Schematic

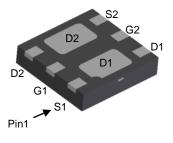
D2

S2

Weight: 0.0065 grams (Approximate)

D1

S1



U-DFN2020-6 (Type B)

Bottom View

Ordering Information (Note 4)

J		
Part Number	Case	Packaging
DMN2050LFDB -7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMN2050LFDB -13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

G1

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



 $\begin{array}{l} M5 = Product Type Marking Code \\ YM = Date Code Marking \\ Y = Year (ex: H = 2020) \\ M = Month (ex: 9 = September) \end{array}$

Date Code Key

Year	2013		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	A		Н		J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
WOITUI	Jan	гер	IVIAI	Арі	iviay	Juli	Jui	Aug	Sep	001	NOV	Dec

Site 2



M5 = Product Type Marking Code YWX = Date Code Marking

Y = Year (ex: 0 = 2020)

W = Week (ex: a = week 27; z represents week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key												
Year	2013		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	3		0	1	2	3	4	5	6	7	8	9
										_		
Week	1-26			27-52				53				
Code		A	λ-Z			а	-Z			2	<u>Z</u>	
Internal Code	Sun		Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т	T U			V	١	V	Х		Y		Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	Vdss	20	V		
Gate-Source Voltage	V _{GSS}	±12	V		
Continuous Drain Current (Note 5) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	3.3 2.6	А
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ State $T_A = +25^{\circ}$ State $T_A = +70^{\circ}$			ID	4.5 3.6	А
Maximum Continuous Body Diode Forward Curre	nt (Note 6)		ls	1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1		Ідм	25	А	
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	9	А		
Avalanche Energy (Note 7) L = 0.1mH			Eas	4.5	mJ

Thermal Characteristics

Characteristic	Characteristic					
Total Dower Dissinction (Note 5)	T _A = +25°C	D	0.73	W		
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.46	vv		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	173	°C/W		
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	RθJA	110			
Total Power Dissipation (Note 6)	T _A = +25°C	D-	1.42	W		
Total Power Dissipation (Note 6)	T _A = +70°C	PD	0.90			
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	89			
memar resistance, sunction to Ambient (Note 6)	t<10s	Reja	57	°C/W		
Thermal Resistance, Junction to Case (Note 6)	Rejc	18				
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C		

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BVDSS	20	—		V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	—	—	1.0	μA	$V_{DS} = 16V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	—	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.4	—	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance		—	28	45	mΩ	V _{GS} = 4.5V, I _D = 5.0A
Static Drain-Source On-Resistance	RDS (ON)	—	36	55	11122	V _{GS} = 2.5V, I _D = 4.2A
Forward Transfer Admittance	Y _{fs}	_	9	_	S	$V_{DS} = 5V, I_D = 5A$
Diode Forward Voltage	Vsd	—	0.75	1.0	V	$V_{GS} = 0V$, $I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	—	389	_	pF	
Output Capacitance	Coss	—	72	_	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	63	_	pF	1 = 1.00012
Gate Resistance	Rg	—	2.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	—	5.7	—	nC	
Total Gate Charge (V _{GS} = 10V)	Qg		12		nC	
Gate-Source Charge	Qgs	_	0.7	_	nC	V _{DS} = 15V, I _D = 5.8A
Gate-Drain Charge	Q _{gd}		1.5		nC	7
Turn-On Delay Time	tD(ON)	_	5	_	ns	
Turn-On Rise Time	t _R	_	8	_	ns	$V_{DS} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	_	25	—	ns	Rg = 6Ω, IDs = 1A
Turn-Off Fall Time	tF	_	8	—	ns	7
Reverse Recovery Time	trr	_	8.5	—	ns	
Reverse Recovery Charge	QRR	_	2.1	_	nC	IF = 5A, di/dt = 100A/μs

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

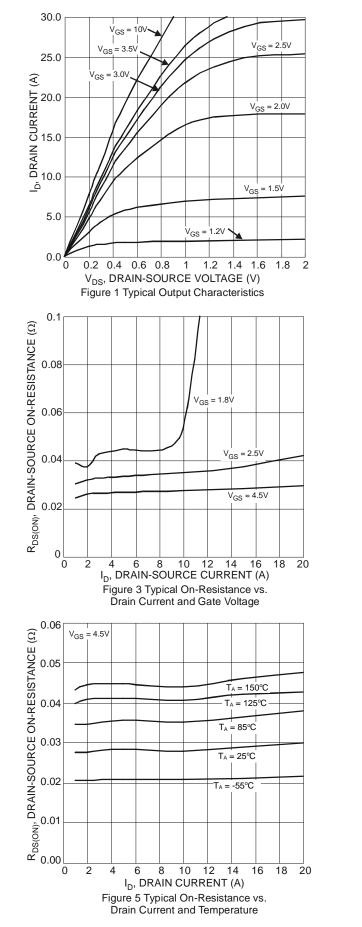
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



DMN2050LFDB



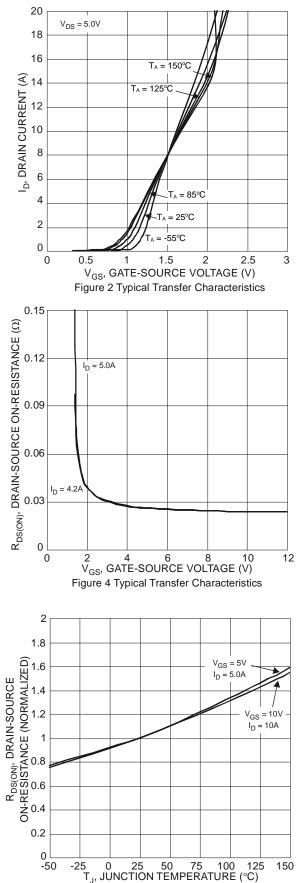
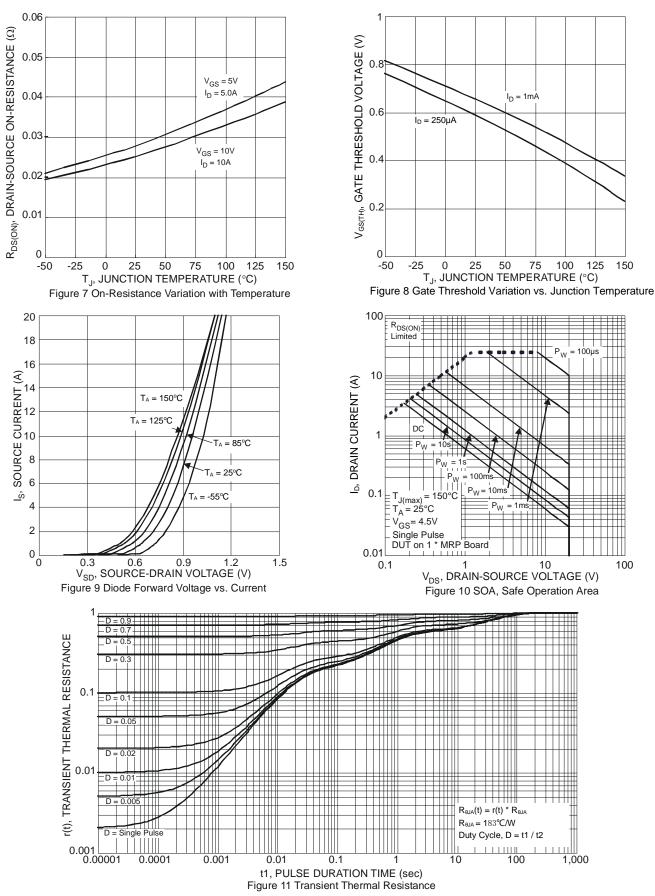


Figure 6 On-Resistance Variation with Temperature

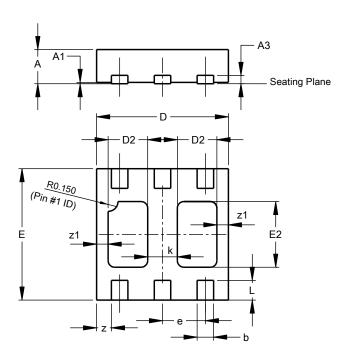






Package Outline Dimensions

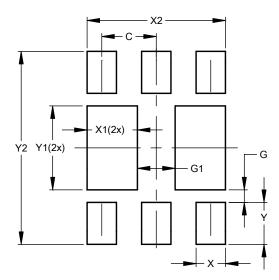
Please see http://www.diodes.com/package-outlines.html for the latest version.



	U-DFN	2020-6	
		e B	
Dim	Min	Max	Тур
Α	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
е	-	-	0.65
Е	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.175
All	Dimens	ions in	mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-6 (Type B)

U-DFN2020-6 (Type B)

Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300



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