



DMN601TK

#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
001/	2Ω @ V <sub>GS</sub> = 10V	0.3A
60V	3Ω @ V <sub>GS</sub> = 5V	0.2A

### **Features and Benefits**

- Low On-Resistance: R<sub>DS(ON)</sub>
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions

#### **Mechanical Data**

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

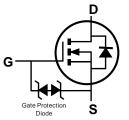




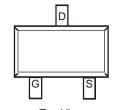
ESD Protected up to 2k\



Top View



**Equivalent Circuit** 



Top View Pin Out Configuration

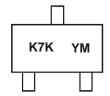
#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN601TK-7	SOT523	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



K7K = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

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Year	2005		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	S		Н	ı	J	K	L	М	N	0	Р	R
0.000												
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Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		VDSS	60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Drain Current (Note 5)	Continuous Pulsed (Note 6)	lp	300 800	mA

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	150	mW
Thermal Resistance, Junction to Ambient	Reja	833	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

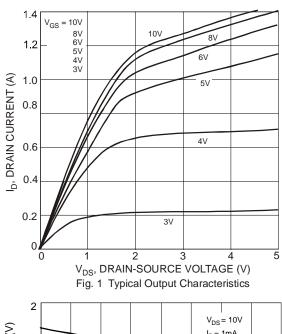
# Electrical Characteristics (@TA = +25°C unless otherwise specified.)

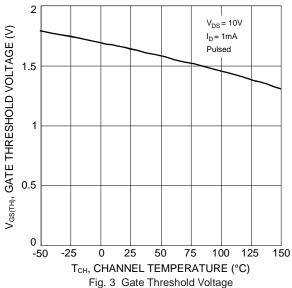
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	1.6	2.5	V	$V_{DS} = 10V$ , $I_D = 1mA$	
Static Drain-Source On-Resistance	Pro/our		1.3	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$	
Static Drain-Source On-Resistance	RDS(ON)		1.4	3.0	12	$V_{GS} = 5V, I_D = 0.05A$	
Forward Transfer Admittance	Y <sub>F</sub> S	80	_	_	ms	$V_{DS} = 10V, I_{D} = 0.2A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	_	50	pF	V 05V V 0V	
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	_	5.0	pF	1 = 1.000112	
Turn-On Delay Time	tD(ON)	_	3.4	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	2.4	_	ns	$V_{DD} = 25V, V_{GS} = 10V,$	
Turn-Off Delay Time	tD(OFF)	_	11.0	_	ns	$R_G = 25\Omega$ , $I_D = 500mA$	
Turn-Off Fall Time	t <sub>F</sub>	_	4.9	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB.
- 6. Pulse width ≤10µS, Duty Cycle ≤1%.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.







vs. Channel Temperature

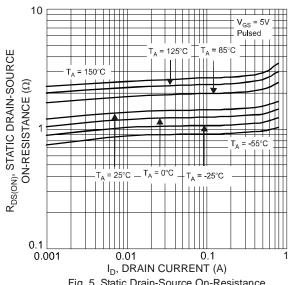
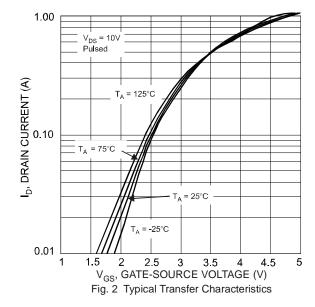


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



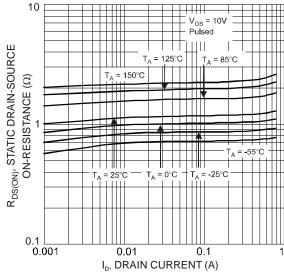


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

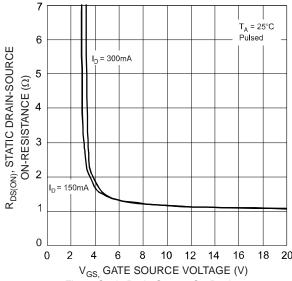


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage



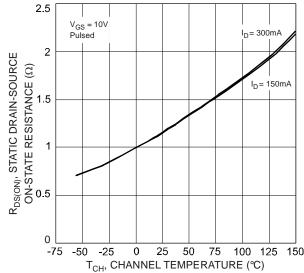
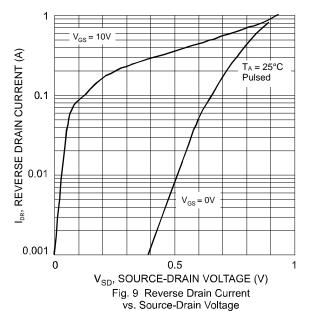
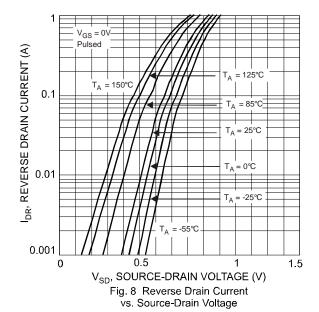
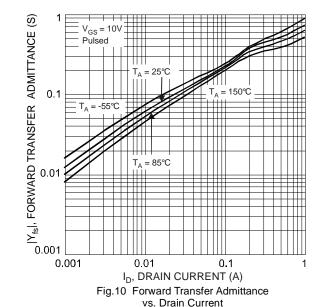


Fig. 7 Static Drain-Source On-State Resistance vs. Channel Temperature





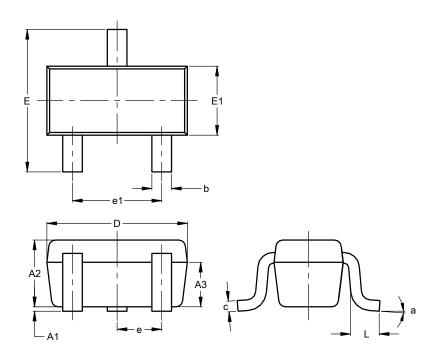




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT523**

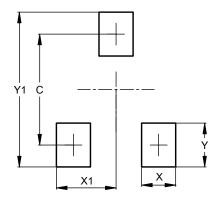


SOT523					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.60	0.80	0.75		
A3	0.45	0.65	0.50		
b	0.15	0.30	0.22		
С	0.10	0.20	0.12		
D	1.50	1.70	1.60		
Е	1.45	1.75	1.60		
E1	0.75	0.85	0.80		
е	0.50 BSC				
e1	0.90	1.10	1.00		
L	0.20	0.40	0.33		
а	0°		8°		
All Dimensions in mm					

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT523**



Dimensions	Value (in mm)
C	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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