





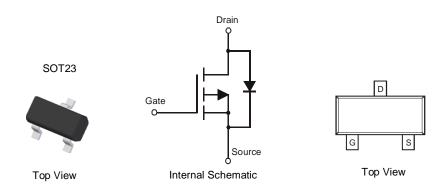
P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low R_{DS(ON)}:
 - $75m\Omega$ @V_{GS} = -4.5V
 - 110m Ω @V_{GS} = -2.7V
 - $125m\Omega$ @V_{GS} = -2.5V
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material Molded Plastic, "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

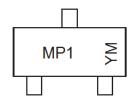


Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2130L-7	SOT23	3000/Tape & Reel

- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



MP1 = Product Type Marking Code YM or \overline{Y} M = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2007	~	2019	2020	202	1 20)22	2023	202	4	2025	2026	2027
Code	U	~	G	Н	ı		J	K	L		М	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Ju	I Au	g	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8		9	0	N	D



Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Drain Current (Note 5) Continuous	$T_A = +25$ °C $T_A = +70$ °C	l _D	-3.0 -2.4	А
Pulsed Drain Current (Note 6)		I _{DM}	-15	A
Body-Diode Continuous Current (Note 5)		Is	-2.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 5); Steady-State	$R_{ hetaJA}$	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS							
Drain-Source Breakdown Voltage	BV_{DSS}	-20	_		V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Body Leakage Current	I _{GSS}	_	_	±100	nA	$V_{DS} = 0V$, $V_{GS} = \pm 12V$	
Gate Threshold Voltage	V _{GS(TH)}	-0.6		-1.25	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
On State Drain Current (Note 7)	I _{D(ON)}	-15			Α	$V_{GS} = -4.5V, V_{DS} = -5V$	
			51	75		$V_{GS} = -4.5V$, $I_D = -3.5A$	
Static Drain-Source On-Resistance (Note 7)	R _{DS(ON)}	_	87	110	$m\Omega$	$V_{GS} = -2.7V$, $I_D = -3.0A$	
			99	125		$V_{GS} = -2.5V, I_D = -2.6A$	
Forward Transconductance (Note 7)	g FS	_	7.3		S	$V_{DS} = -10V, I_D = -3.0A$	
Diode Forward Voltage (Note 7)		_	-0.79	-1.26	V	$I_S = -1.7A$, $V_{GS} = 0V$	
Maximum Body-Diode Continuous Current (Note 5)		_	_	1.7	Α	_	
DYNAMIC PARAMETERS (Note 8)							
Total Gate Charge	Qg	_	7.3		nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$	
Gate-Source Charge	Q_{gs}	_	2.0		nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$	
Gate-Drain Charge	Q_{gd}	_	1.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$	
Turn-On Delay Time	t _{D(ON)}	_	12	_	ns		
Turn-On Rise Time	t _R	_	20	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	38	_	ns	$R_L = 10\Omega, R_G = 6\Omega$	
Turn-Off Fall Time	t _F	_	41		ns		
Input Capacitance		_	443		pF	., ., ., ., ., ., ., ., ., ., ., ., ., .	
Output Capacitance		_	128	_	pF	$V_{DS} = -16V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	101	_	pF	-1 = 1.UIVIMZ	

5. Device mounted on 1"x1", FR-4 PC board with 2 oz. copper and test pulse width t \leq 10s. 6. Repetitive Rating, pulse width limited by junction temperature. 7. Test pulse width t = 300 μ s. 8. Guaranteed by design. Not subject to production testing. Notes:



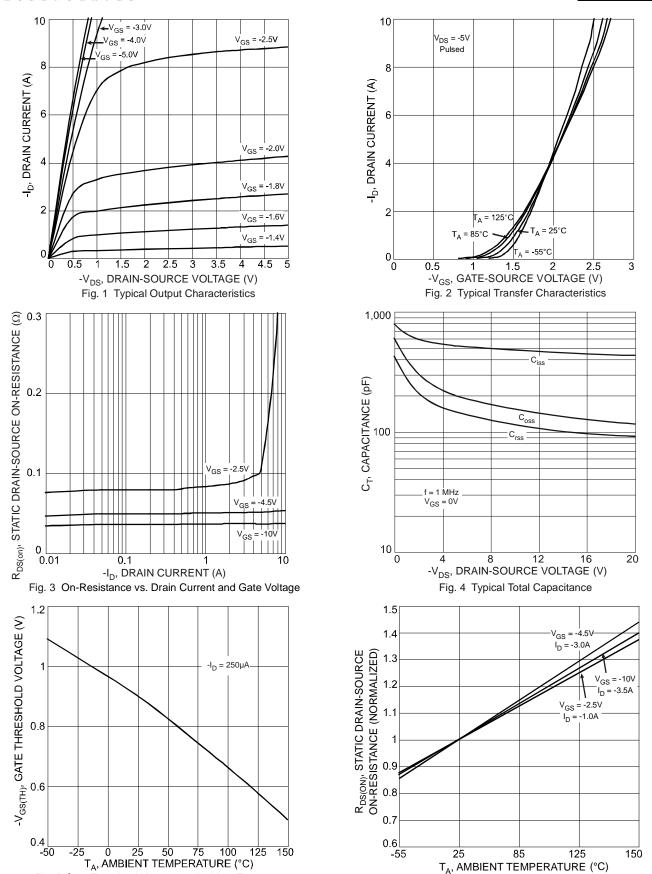
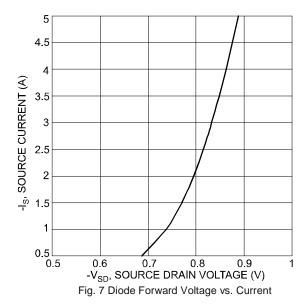
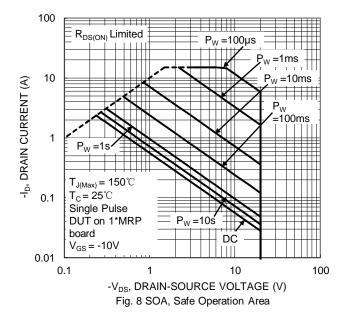


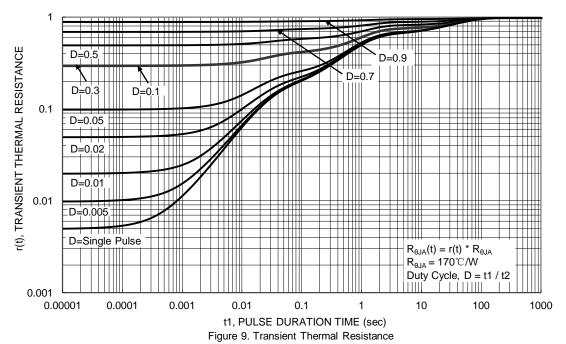
Fig. 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

Fig. 5 Gate Threshold Voltage vs. Ambient Temperature







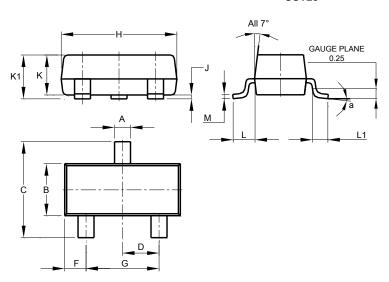




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

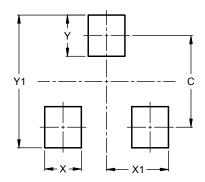


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	29



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