



#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	90mΩ @ V <sub>GS</sub> = 4.5V	2.8A
20V	120mΩ @ V <sub>GS</sub> = 2.5V	2.4A

### **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

# Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

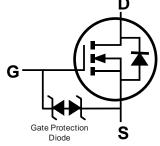
#### **Mechanical Data**

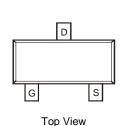
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)











Internal Schematic

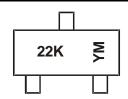
### **Ordering Information** (Note 5)

Part Number	Case	Packaging
DMG2302UKQ-7	SOT23	3,000/Tape & Reel
DMG2302UKQ-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



22K = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$ = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2015		2016	2017		2018	2019		2020	2021		2022
Code	С		D	Е		F	G		Н			J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

May 2018

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## **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		$V_{DSS}$	20	V
Gate-Source Voltage		$V_{GSS}$	±12	V
Continuous Drain Current (Note 7) V <sub>GS</sub> = 4.5V	ΙD	2.8 2.2	Α	
Maximum Continuous Body Diode Forward Curre	ent (Note 7)	I <sub>S</sub>	1.1	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)	I <sub>DM</sub>	12	A

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		$P_{D}$	0.66	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	192	°C/W
Total Power Dissipation (Note 7)		$P_{D}$	1.1	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	115	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

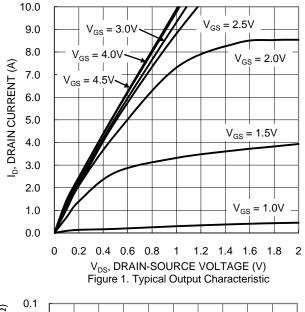
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

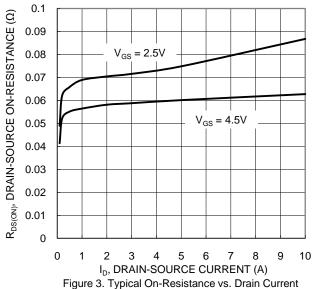
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	1	_	٧	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_		10	μΑ	$V_{DS} = 16V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	_	±10	μΑ	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.3	0.6	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance		_	61	90	mΩ	$V_{GS} = 4.5V, I_D = 3.6A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	80	120	11122	$V_{GS} = 2.5V, I_D = 3.1A$
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.0A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C <sub>iss</sub>	_	130	_	pF	\/ 40\/ \/ 0\/
Output Capacitance	Coss	_	26	_	рF	$V_{DS} = 10V, V_{GS} = 0V$ -f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	18	_	pF	1 – 1.000112
Gate Resistance	$R_g$	_	2.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	1.4	_	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	2.8	_	nC	V <sub>DS</sub> = 10V. I <sub>D</sub> = 3.6A
Gate-Source Charge	$Q_{gs}$	_	0.1	_	nC	$V_{DS} = 10V, I_D = 3.6A$
Gate-Drain Charge	$Q_{gd}$	_	0.5	_	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	0.6	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	2.7	_	ns	$V_{DS} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	4.2	_	ns	$R_g = 1\Omega$ , $R_L = 2.78\Omega$
Turn-Off Fall Time	t <sub>F</sub>	_	1.7	_	ns	
Reverse Recovery Time	t <sub>RR</sub>	_	5.3	_	ns	$I_F = 3.6A$ , $di/dt = 100A/\mu s$
Reverse Recovery Charge	$Q_{RR}$	_	0.5	_	nC	$I_F = 3.6A$ , di/dt = 100A/ $\mu$ s

Notes:

- 6. Device mounted on FR-4 PCB with minimum recommended pad layout.
- 7. Device mounted on 1" x 1" FR-4 PCB with high-coverage 2oz copper, single sided. 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.







and Gate Voltage

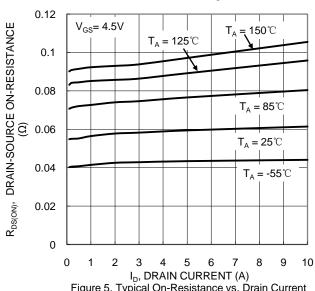
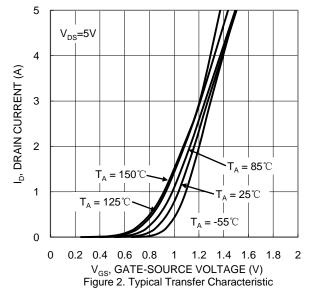
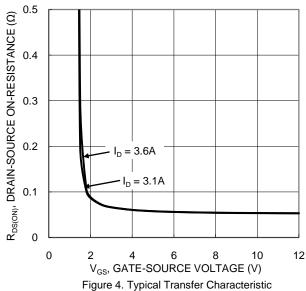


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





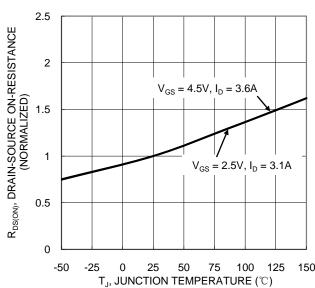


Figure 6. On-Resistance Variation with Junction Temperature



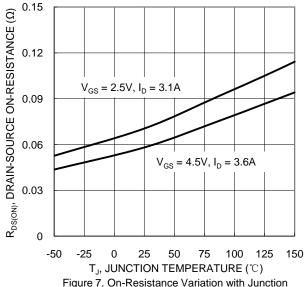


Figure 7. On-Resistance Variation with Junction Temperature

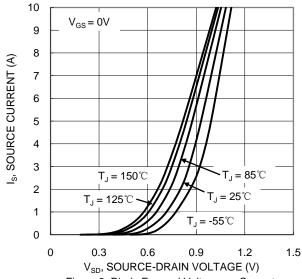


Figure 9. Diode Forward Voltage vs. Current 10 8 6  $V_{GS}$  (V)  $V_{DS} = 10V, I_{D} = 3.6A$ 2 0 2 0 0.5 1.5 2.5 3  $Q_q$  (nC) Figure 11. Gate Charge

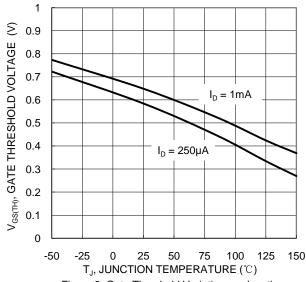
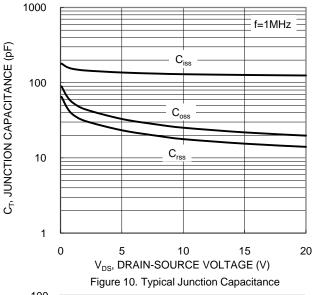
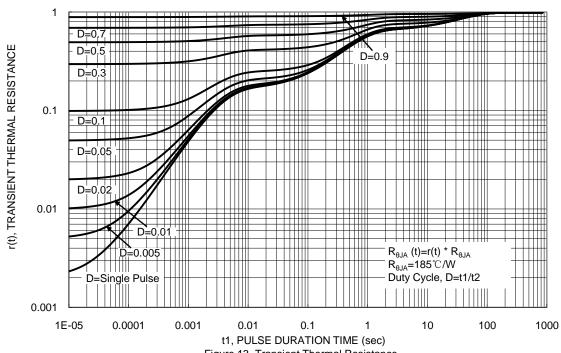


Figure 8. Gate Threshold Variation vs. Junction Temperature



100 R<sub>DS(ON)</sub> Limited =10ms 10 ID, DRAIN CURRENT (A) 1 T<sub>J(Max)</sub>=150°C 0.1 T<sub>C</sub>=25 ℃ Single Pulse DUT on 1\*MRP Board DC V<sub>GS</sub>=4.5V 0.01 0.1 10 100 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

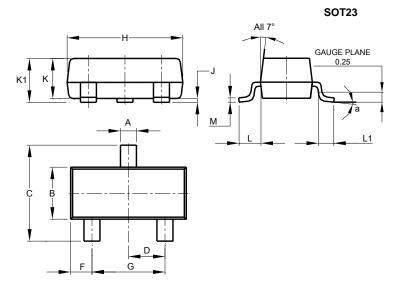






## **Package Outline Dimensions**

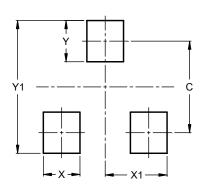
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9

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