



DFLS160Q

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER PowerDI123

Product Summary

V _R (V)	I _F (A)	V _{F MAX} (V) @ +25°C	I _{R MAX} (mA) @ +25°C
60	1.0	0.50	0.1

Description and Applications

This Schottky Barrier Rectifier has been designed to meet the stringent requirements of Automotive Applications. It is ideally suited to use as:

- Polarity Protection Diode
- Re-Circulating Diode
- Switching Diode

Features and Benefits

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: PowerDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202 Method 208 (@3)
- Weight: 0.01 grams (Approximate)

PowerDI123



Top View

Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
DFLS160Q-7	Automotive	PowerDI123	3000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



F17 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	В	С	D	E	F	G	Н	I	J	K	L	М	Ν	0
Month	Jan	Feb	Ma	ar	Apr	Мау	Jun	Jul	Aug	Se	o (Oct	Nov	Dec
Code	1	2	3	3	4	5	6	7	8	9		0	Ν	D



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
RMS Reverse Voltage	V _{R(RMS)}	42	V
Average Forward Current	I _{F(AV)}	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	50	A
Electrostatic Discharge	HBM	4000	V
Electrostatic Discharge	MM	400	V
Electrostatic Discharge	CDM	1	kV

Thermal Characteristics

Characteristic	Symbol	Тур	Мах	Unit
Thermal Resistance Junction to Soldering Point (Cathode) (Note 6)	R _{ejs}	_	6	°C/W
Thermal Resistance Junction to Ambient (Note 7)	R _{θJA}	125	—	°C/W
Thermal Resistance Junction to Ambient (Note 8)	R _{0JA}	60	—	°C/W
Typical Thermal Resistance to Case (Note 9)	Rejc	_	18	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to -	+150	°C

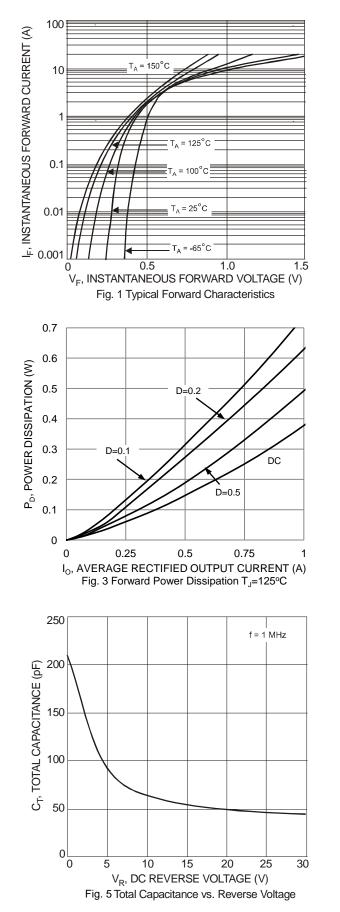
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

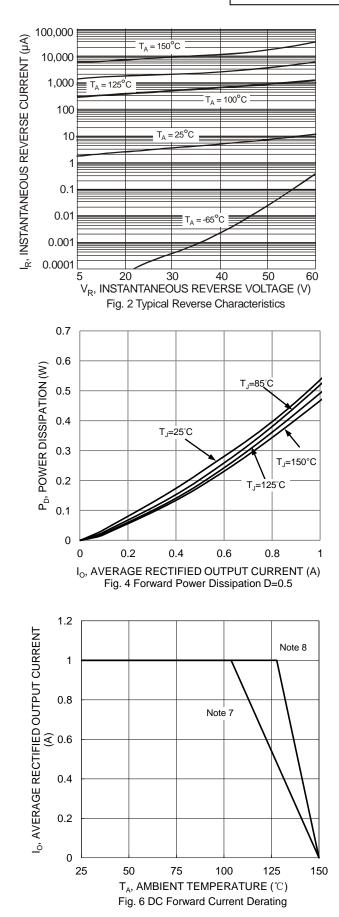
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 10)	V _{(BR)R}	60	_	—	V	$I_R = 0.2 \text{mA}$
Forward Voltage	V _F		_	0.50	V	I _F = 1.0A
Leakage Current (Note 10)	I _R	_	_	0.1	mA	$V_R = 60V, T_A = +25^{\circ}C$
Total Capacitance	CT		67	—	pF	V _R = 10V, f = 1.0MHz
Switching Speed t _{RR}	t _{RR}	—	12	—	ns	I _F =0.5A, I _R =1A, I _{RR} =0.25A (RG1)

Notes:

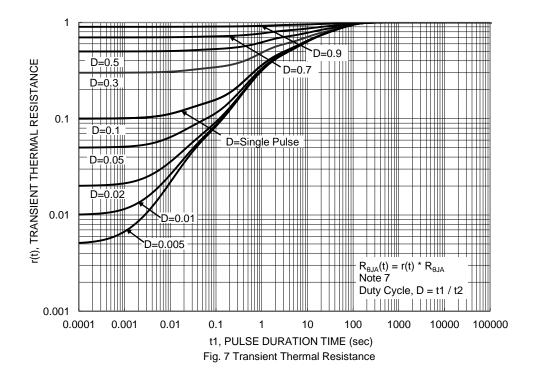
 Theoretical R_{eus} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
Device mounted on Polymide substrate, 1" x 1" 2oz copper double-sided PC board with minimum recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html. 8. Part mounted on 50.8mm*50.8mm GETEK board with 25.4mm*25.4mm copper pad, 25% anode, 75% cathode. $T_A = +25^{\circ}C$ 9. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. $T_A = +25^{\circ}C$ 10. Short duration pulse test to minimize self-heating effect







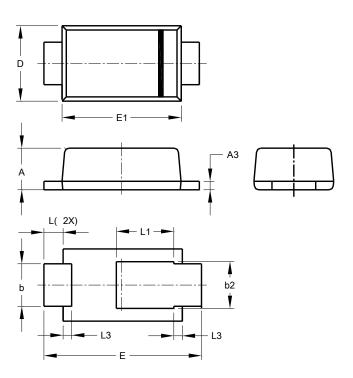






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

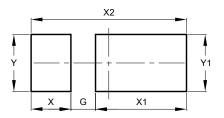


	PowerDI123							
Dim	Min	Max	Тур					
Α	0.93	1.00	0.98					
A3	0.15	0.25	0.20					
b	0.85	1.25	1.00					
b2	1.025	1.125	1.10					
D	1.63	1.93	1.78					
Е	3.50	3.90	3.70					
E1	2.60	3.00	2.80					
L	0.40	0.50	0.45					
L1	1.25	1.40	1.35					
L3	0.125	0.275	0.20					
All	Dimensi	ions in r	nm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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