



30V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C		
30V	7mΩ @ V _{GS} = 10V	16A		
307	10mΩ @ V _{GS} = 4.5V	13.5A		

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Available (Note 4)

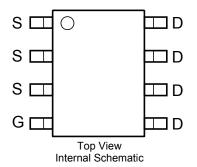
Mechanical Data

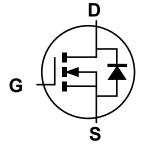
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202. Method 208
- Weight: 0.074 grams (approximate)











Equivalent Circuit

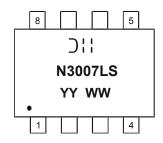
Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
DMN3007LSSQ-13	Automotive	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



];; = Manufacturer's Marking N3007LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 13 = 2013) WW = Week (01 - 53)



Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 6)	Steady State	T _A = +25°C T _A = +70°C	ID	16 13	А
Pulsed Drain Current (Note 7)			I _{DM}	64	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P_{D}	2.5	W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	50	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C

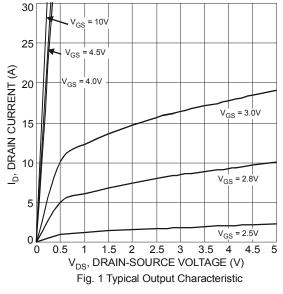
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

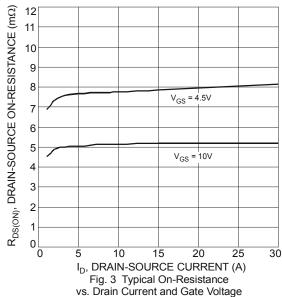
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	1			ı	1		
Drain-Source Breakdown Voltage	BV _{DSS}	30			V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I_{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.3		2.1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	_		5	7	0	$V_{GS} = 10V, I_D = 15A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	7.9	10	mΩ	$V_{GS} = 4.5V, I_D = 13A$	
Forward Transconductance	9 _{fs}	_	16.4	_	S	$V_{DS} = 10V, I_D = 15A$	
Diode Forward Voltage	V_{SD}	_	0.67	1.2	V	$V_{GS} = 0V, I_S = 2.3A$	
DYNAMIC CHARACTERISTICS (Note 9)				_	_		
Input Capacitance	C _{iss}	_	2714	_	pF	\\\ 45\\\\\\ 0\\\	
Output Capacitance	Coss	_	436	_	pF	$V_{DS} = 15V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	380	_	pF	1 - 1.0WH2	
Gate Resistance	R_{G}	_	0.7	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
SWITCHING CHARACTERISTICS (Note 9)							
Total Cata Charge			31.2			$V_{DS} = 15V, V_{GS} = 4.5V, I_{D} = 16A$	
Total Gate Charge	Q_g	_	64.2		-0	$V_{DS} = 15V$, $V_{GS} = 10V$, $I_{D} = 16A$	
Gate-Source Charge	Q_{gs}	_	7.1	_	nC	V _{DS} = 15V, V _{GS} = 10V, I _D = 16A	
Gate-Drain Charge	Q_{gd}	_	17.1	_		$V_{DS} = 15V$, $V_{GS} = 10V$, $I_{D} = 16A$	
Turn-On Delay Time	t _{d(on)}	_	10.3	_			
Rise Time	t _r	_	14.8		no	$V_{DS} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{d(off)}	_	85.1	_	ns	$I_D = 1A, R_G = 6.0\Omega$	
Fall Time	t _f		43.6	_			

6.Device mounted on 2 oz. Copper pads on FR-4 PCB, with $R_{\theta JA}$ = +50°C 7.Pulse width $\leq\!10\mu S,$ Duty Cycle $\leq\!1\%.$ Notes:

^{8.}Short duration pulse test used to minimize self-heating effect.
9.Guaranteed by design. Not subject to product testing.







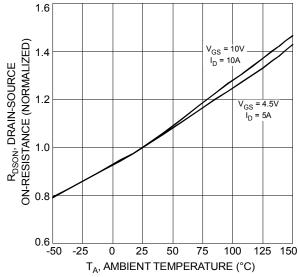
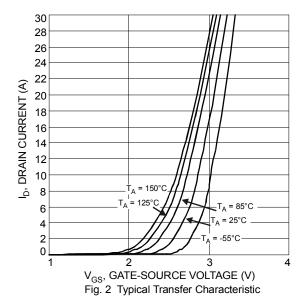


Fig. 5 On-Resistance Variation with Temperature



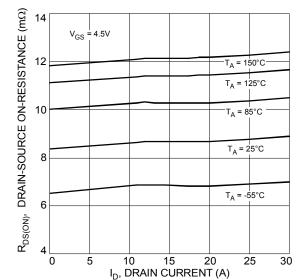


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

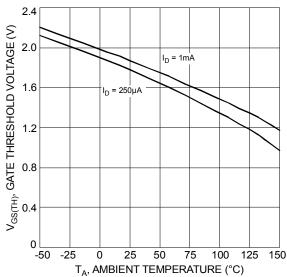
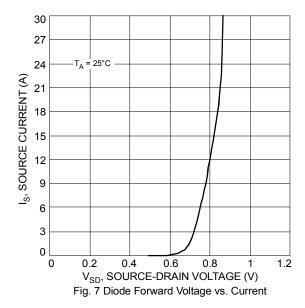
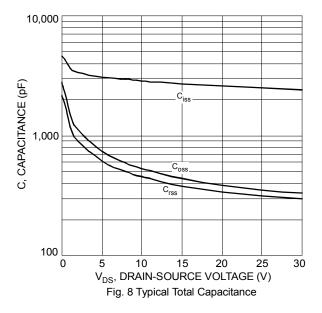
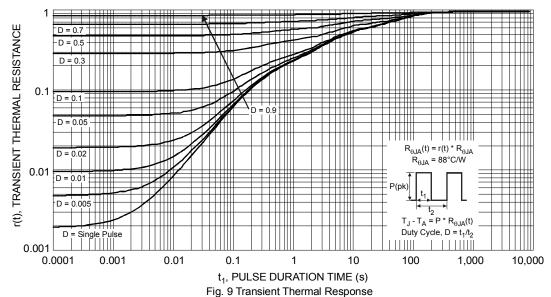


Fig. 6 Gate Threshold Variation vs. Ambient Temperature



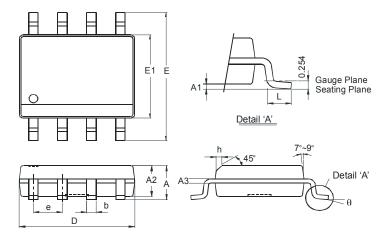






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version

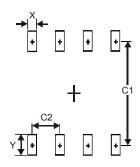


SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
X	0.60
Υ	1.55
C1	5.4
C2	1.27

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