



DMT6015LSS

### **Product Summary**

| BV <sub>DSS</sub> | R <sub>DS(ON)</sub> Max       | I <sub>D</sub> Max<br>T <sub>A</sub> = +25°C |
|-------------------|-------------------------------|--|
| 60V               | 16mΩ @ V <sub>GS</sub> = 10V  | 9.2A   |
| 000               | 21mΩ @ V <sub>GS</sub> = 4.5V | 7.5A   |

# **Description and Applications**

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$ , maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load Switch
- Adaptor Switch
- Notebook PC

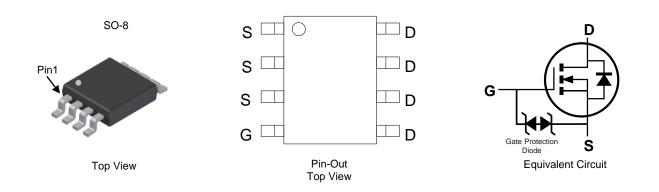
#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.076 grams (Approximate)



## Ordering Information (Note 4)

| Part Number   | Case | Packaging         |
|---------------|------|-------------------|
| DMT6015LSS-13 | SO-8 | 2,500/Tape & Reel |

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

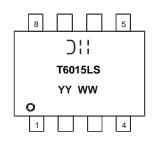
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**

Notes:



)|| = Manufacturer's Marking T6015LS = Product Type Marking Code YYWW = Date Code Marking YY or  $\overline{YY}$  = Year (ex: 16 = 2016) WW = Week (01 - 53)



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic Drain-Source Voltage Gate-Source Voltage  |                 |  | Symbol           | Value       | Units<br>V |
|--|-----------------|--|------------------|-------------|------------|
|  |                 |  | V <sub>DSS</sub> | 60          |            |
|  |                 |  | V <sub>GSS</sub> | ±16         | V          |
|  | Steady<br>State | T <sub>A</sub> = +25°C<br>T <sub>A</sub> = +70°C | ID               | 9.2<br>7.4  | А          |
| Continuous Drain Current (Note 6) $V_{GS} = 10V$         | t<10s           | T <sub>A</sub> = +25°C<br>T <sub>A</sub> = +70°C | Ι <sub>D</sub>   | 11.9<br>9.5 | А          |
| Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V | Steady<br>State | T <sub>A</sub> = +25°C<br>T <sub>A</sub> = +70°C | Ι <sub>D</sub>   | 7.5<br>6.0  | А          |
|  | t<10s           | T <sub>A</sub> = +25°C<br>T <sub>A</sub> = +70°C | ID               | 9.7<br>7.7  | А          |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)       |                 |  | I <sub>DM</sub>  | 60          | А          |
| Maximum Continuous Body Diode Forward Current (Note 6)   |                 |  | Is               | 2           | А          |
| Avalanche Current, L = 0.1mH                             |                 |  | I <sub>AS</sub>  | 15          | А          |
| Avalanche Energy, L = 0.1mH                              |                 |  | EAS              | 11          | mJ         |

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                                   |              | Symbol                           | Value       | Units |
|--|--------------|----------------------------------|-------------|-------|
| Total Power Dissipation (Note 5)                 |              | PD                               | 1.5         | W     |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | Р                                | 85          | °C/W  |
| mermai Resistance, Junction to Ambient (Note 5)  | t<10s        | $R_{\theta JA}$                  | 45          | °C/W  |
| Total Power Dissipation (Note 6)                 |              | PD                               | 2.1         | W     |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | Р                                | 74          | °C/W  |
| mermai Resistance, Junction to Ambient (Note 6)  | t<10s        | $R_{\theta JA}$                  | 37          | °C/W  |
| Thermal Resistance, Junction to Case             |              | R <sub>0JC</sub>                 | 13          | °C/W  |
| Operating and Storage Temperature Range          |              | T <sub>J,</sub> T <sub>STG</sub> | -55 to +150 | °C    |

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

|  |                     |     |       |     |       | -   |  |
|--|---------------------|-----|-------|-----|-------|---|--|
| Characteristic                             | Symbol              | Min | Тур   | Max | Unit  | Test Condition  |  |
| OFF CHARACTERISTICS (Note 7)               |                     | -   | -     | -   |       |   |  |
| Drain-Source Breakdown Voltage             | BV <sub>DSS</sub>   | 60  | —     |     | V     | $V_{GS} = 0V, I_D = 250 \mu A$                              |  |
| Zero Gate Voltage Drain Current            | I <sub>DSS</sub>    | —   | _     | 1   | μA    | $V_{DS} = 48V, V_{GS} = 0V$                                 |  |
| Gate-Source Leakage                        | I <sub>GSS</sub>    | _   | —     | ±10 | μA    | $V_{GS} = \pm 16V, V_{DS} = 0V$                             |  |
| ON CHARACTERISTICS (Note 7)                |                     |     |       |     |       |   |  |
| Gate Threshold Voltage                     | V <sub>GS(TH)</sub> | 0.5 | —     | 2.5 | V     | $V_{DS} = V_{GS}$ , $I_D = 250 \mu A$                       |  |
| Static Drain-Source On-Resistance          |                     | _   | 12.4  | 16  | mΩ    | $V_{GS} = 10V, I_D = 10A$                                   |  |
|  | R <sub>DS(ON)</sub> | _   | 15.8  | 21  | 11122 | VGS = 4.5V, ID = 6A   |  |
| Diode Forward Voltage                      | V <sub>SD</sub>     | —   | 0.7   | 1.2 | V     | $V_{GS} = 0V, I_S = 1A$                                     |  |
| DYNAMIC CHARACTERISTICS (Note 8)           |                     |     |       |     |       |   |  |
| Input Capacitance                          | CISS                | _   | 1,103 | _   |       | $V_{DS} = 30V, V_{GS} = 0V,$<br>f = 1MHz                    |  |
| Output Capacitance                         | Coss                | _   | 251   |     | pF    |   |  |
| Reverse Transfer Capacitance               | C <sub>RSS</sub>    | —   | 20    | _   |       |   |  |
| Gate Resistance                            | R <sub>G</sub>      | —   | 1.5   | _   | Ω     | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$                        |  |
| Total Gate Charge (V <sub>GS</sub> = 4.5V) | $Q_G$               | —   | 8.9   | _   |       | V <sub>DS</sub> = 30V, I <sub>D</sub> = 10A                 |  |
| Total Gate Charge (V <sub>GS</sub> = 10V)  | Q <sub>G</sub>      | _   | 18.9  | _   | nC    |   |  |
| Gate-Source Charge                         | Q <sub>GS</sub>     | —   | 3.0   | _   | nc    |   |  |
| Gate-Drain Charge                          | $Q_{GD}$            | _   | 2.8   | _   |       |   |  |
| Turn-On Delay Time                         | t <sub>D(ON)</sub>  | —   | 4.1   | _   |       | $V_{GS} = 10V, V_{DS} = 30V,$<br>$R_G = 6\Omega, I_D = 10A$ |  |
| Turn-On Rise Time                          | t <sub>R</sub>      | _   | 7.1   | _   |       |   |  |
| Turn-Off Delay Time                        | t <sub>D(OFF)</sub> |     | 19.5  |     | ns    |   |  |
| Turn-Off Fall Time                         | tF                  | _   | 8.6   |     |       |   |  |
| Reverse Recovery Time                      | T <sub>RR</sub>     | —   | 21.2  |     | ns    | $I_{-} = 100$ di/dt = 1000/uc                               |  |
| Reverse Recovery Charge                    | Q <sub>RR</sub>     | _   | 13.2  |     | nC    | I <sub>F</sub> = 10A, di/dt = 100A/μs                       |  |

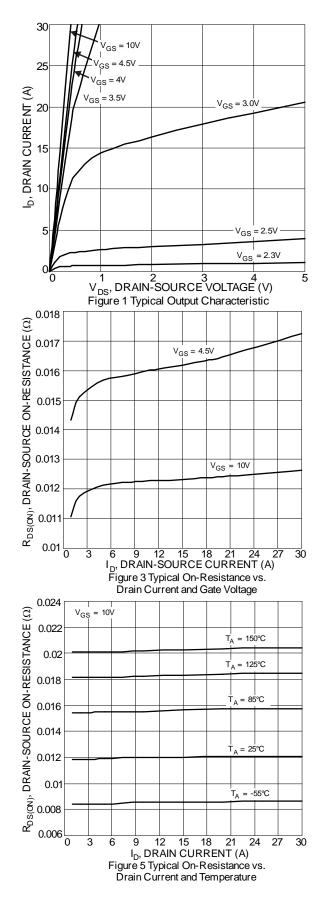
Notes:

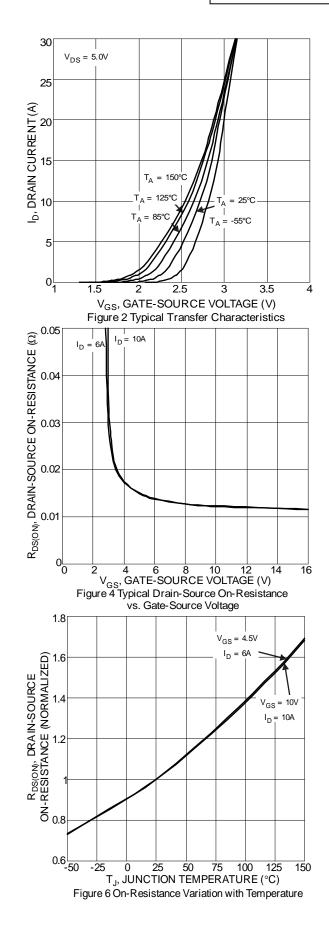
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

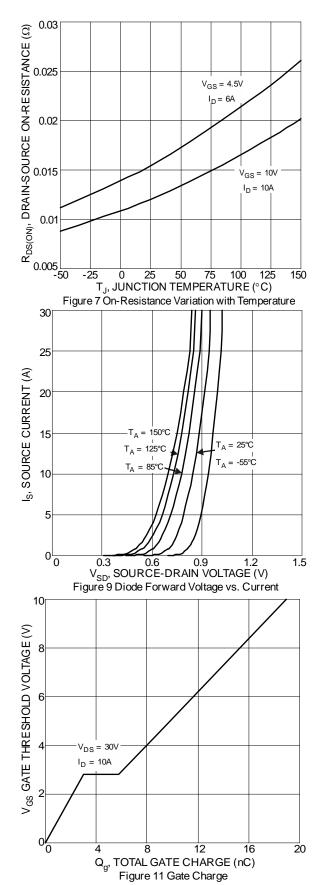


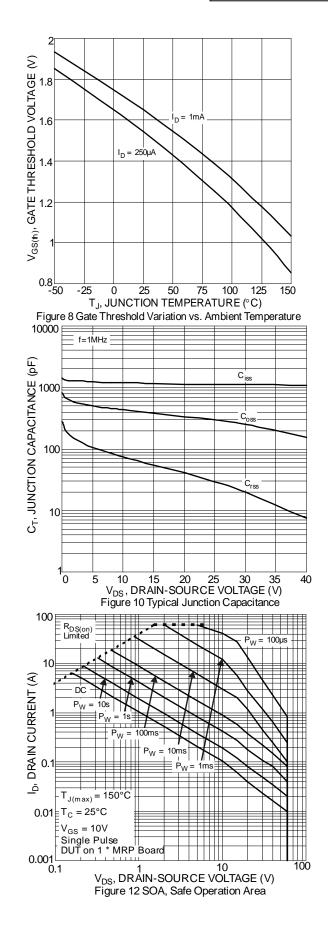
# DMT6015LSS



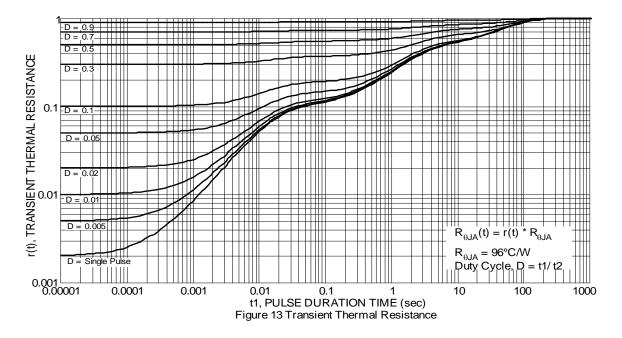








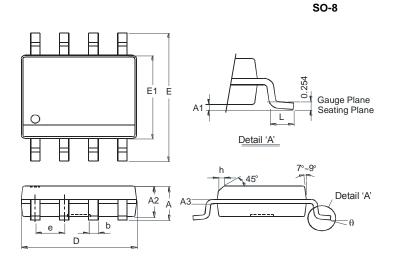






## Package Outline Dimensions

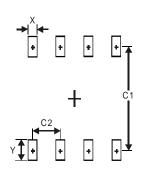
Please see http://www.diodes.com/package-outlines.html for the latest version.



| SO-8                 |          |      |  |  |  |
|----------------------|----------|------|--|--|--|
| Dim                  | Min      | Max  |  |  |  |
| Α                    | -        | 1.75 |  |  |  |
| A1                   | 0.10     | 0.20 |  |  |  |
| A2                   | 1.30     | 1.50 |  |  |  |
| A3                   | 0.15     | 0.25 |  |  |  |
| b                    | 0.3      | 0.5  |  |  |  |
| D                    | 4.85     | 4.95 |  |  |  |
| Е                    | 5.90     | 6.10 |  |  |  |
| E1                   | 3.85     | 3.95 |  |  |  |
| e                    | 1.27 Typ |      |  |  |  |
| h                    | -        | 0.35 |  |  |  |
| L                    | 0.62     | 0.82 |  |  |  |
| θ                    | 0°       | 8°   |  |  |  |
| All Dimensions in mm |          |      |  |  |  |

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



| Dimensions | Value (in mm) |  |  |  |
|------------|---------------|--|--|--|
| Х          | 0.60          |  |  |  |
| Y          | 1.55          |  |  |  |
| C1         | 5.4           |  |  |  |
| C2         | 1.27          |  |  |  |

SO-8



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