



Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D T _C = +25°C (Note 9) |
|-------------------|---|--|
| 40V | 3.3mΩ @ V _{GS} = 10V | 100A |
| 40 V | $5.0 \text{m}\Omega @ V_{\text{GS}} = 5 \text{V}$ | 95A |

Description

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize $R_{\text{DS}(\text{ON})},$ yet maintain superior switching performance.

Applications

- BLDC Motors
- DC-DC Converters

Top View

- Loadswitch
- Loudowiton

PowerDI5060-8

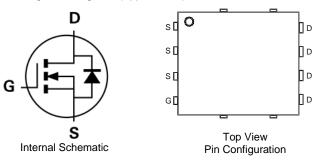
Bottom View

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable And Robust End Application
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMTH43M8LPSQ</u>)

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (@3)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

| | Part Number | Case | Packaging | |
|--------|---|---------------|-------------------|--|
| | DMTH43M8LPS-13 | PowerDI5060-8 | 2,500/Tape & Reel | |
| Notes: | tes: 1 EU Directive 2002/95/EC (RoHS) 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant All applicable RoHS exemptions applied | | | |

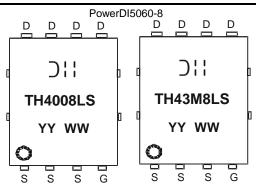
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EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



J | | = Manufacturer's Marking
TH43M8LS or TH4008LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 = 2018)
WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.

DMTH43M8LPS

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | |
|---|---|------------------|------------|----|
| Drain-Source Voltage | | V _{DSS} | 40 | V |
| Gate-Source Voltage | | V _{GSS} | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 5) | T _A = +25°C T _A = +100°C | ID | 22 15.5 | А |
| Continuous Drain Current, V_{GS} = 10V (Note 6) (Note 9) | T _C = +25°C T _C = +100°C | ID | 100 82 | A |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | IDM | 350 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) | | ls | 69 | A |
| Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%) | | I _{SM} | 350 | A |
| Avalanche Current, L = 1mH | | I _{AS} | 13.2 | А |
| Avalanche Energy, L = 1mH | | E _{AS} | 87 | mJ |

Thermal Characteristics

| Characteristic | | Symbol | Value | Unit |
|--|------------------------|----------------------------------|-------------|------|
| Total Power Dissipation (Note 5) | T _A = +25°C | PD | 2.7 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | | R _{θJA} | 55 | °C/W |
| Total Power Dissipation (Note 6) | T _C = +25°C | PD | 83 | W |
| Thermal Resistance, Junction to Case (Note 6) | | R _{θJC} | 1.8 | °C/W |
| Operating and Storage Temperature Range | | T _{J,} T _{STG} | -55 to +175 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| | | | _ | | | | |
|--|---------------------|-----|-------|------|------|---|--|
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
| OFF CHARACTERISTICS (Note 7) | | | | r | 1 | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 40 | | — | V | $V_{GS} = 0V, I_D = 1mA$ | |
| Zero Gate Voltage Drain Current | I _{DSS} | - | — | 1 | μA | $V_{DS} = 32V, V_{GS} = 0V$ | |
| Gate-Source Leakage | Igss | _ | — | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 7) | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 1 | — | 2.5 | V | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | |
| Static Drain-Source On-Resistance | | _ | 2.7 | 3.3 | 0 | V _{GS} = 10V, I _D = 20A | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | | 3.6 | 5.0 | mΩ | V _{GS} = 5V, I _D = 15A | |
| Diode Forward Voltage | V _{SD} | | _ | 1.2 | V | $V_{GS} = 0V, I_{S} = 20A$ | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | | |
| Input Capacitance | CISS | _ | 2,693 | — | | $V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz | |
| Output Capacitance | Coss | — | 1,172 | — | рF | | |
| Reverse Transfer Capacitance | C _{RSS} | _ | 52 | — | | | |
| Gate Resistance | R _G | — | 2.54 | — | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ | |
| Total Gate Charge (V _{GS} = 10V) | Q _G | _ | 38.5 | — | | V _{DS} = 30V, I _D = 20A | |
| Total Gate Charge (V _{GS} = 4.5V) | Q _G | _ | 17.6 | — | nC | | |
| Gate-Source Charge | Q _{GS} | — | 6.9 | — | nc | | |
| Gate-Drain Charge | Q _{GD} | _ | 6.9 | — | | | |
| Turn-On Delay Time | t _{D(ON)} | — | 5.2 | — | | | |
| Turn-On Rise Time | t _R | _ | 5.7 | — | | $V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 20A, R_G = 3\Omega$ | |
| Turn-Off Delay Time | t _{D(OFF)} | | 23.5 | | ns | | |
| Turn-Off Fall Time | tF | | 11 | | | | |
| Body Diode Reverse Recovery Time | t _{RR} | | 35.4 | — | ns | | |
| Body Diode Reverse Recovery Charge | Q _{RR} | | 32.9 | _ | nC | I _F = 20A, di/dt = 100A/μs | |

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

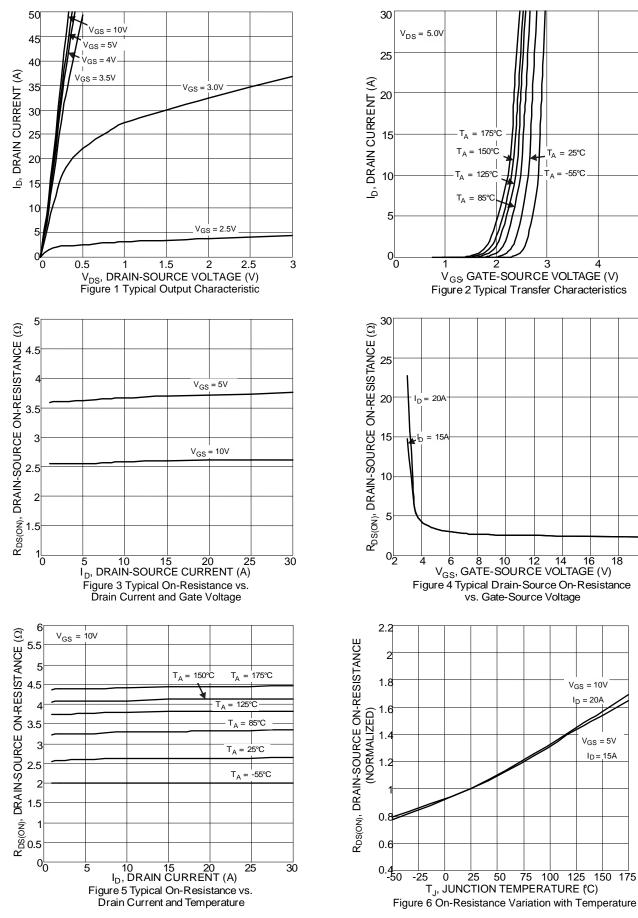
8. Guaranteed by design. Not subject to product testing.

9. Package limit.



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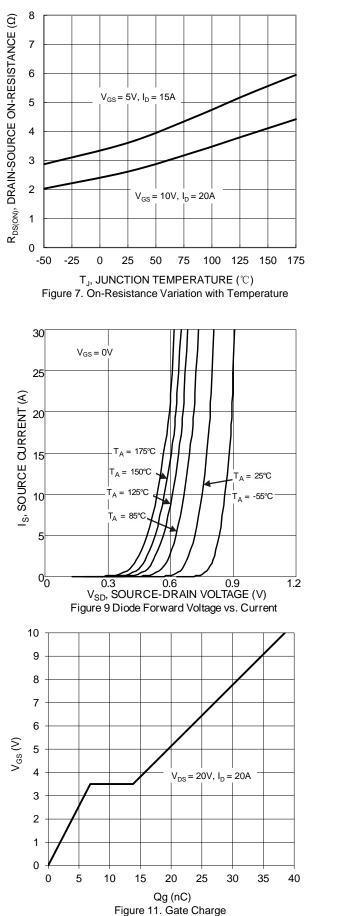
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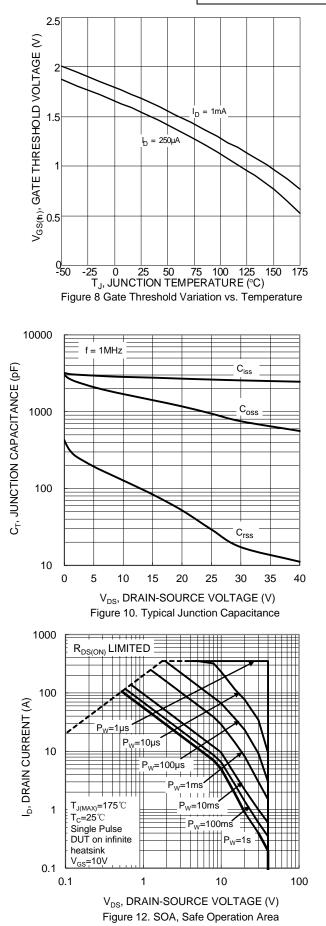


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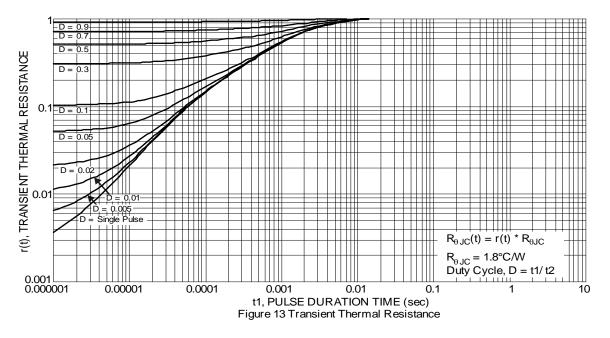






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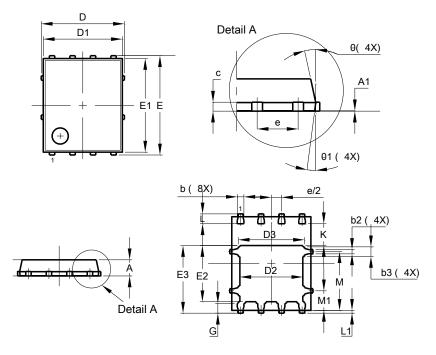




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

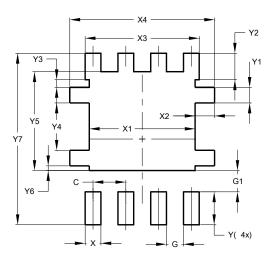


| | PoworD | 15060-9 | | | | |
|-----|----------------------------------|-----------|-------|--|--|--|
| Dim | PowerDI5060-8 Dim Min Max Typ | | | | | |
| | | | Тур | | | |
| A | 0.90 | 1.10 | 1.00 | | | |
| A1 | 0.00 | 0.05 | | | | |
| b | 0.33 | 0.51 | 0.41 | | | |
| b2 | 0.200 | 0.350 | 0.273 | | | |
| b3 | 0.40 | 0.80 | 0.60 | | | |
| С | 0.230 | 0.330 | 0.277 | | | |
| D | ; | 5.15 BSC | ; | | | |
| D1 | 4.70 | 5.10 | 4.90 | | | |
| D2 | 3.70 | 4.10 | 3.90 | | | |
| D3 | 3.90 | 3.90 4.30 | | | | |
| E | - | 6.15 BSC | ; | | | |
| E1 | 5.60 | 6.00 | 5.80 | | | |
| E2 | 3.28 | 3.68 | 3.48 | | | |
| E3 | 3.99 | 4.39 | 4.19 | | | |
| е | | 1.27 BSC | | | | |
| G | 0.51 | 0.71 | 0.61 | | | |
| K | 0.51 | _ | - | | | |
| L | 0.51 | 0.71 | 0.61 | | | |
| L1 | 0.100 | 0.200 | 0.175 | | | |
| М | 3.235 | 4.035 | 3.635 | | | |
| M1 | 1.00 | 1.40 | 1.21 | | | |
| θ | 10° | 12° | 11° | | | |
| θ1 | 6° | 8° | 7° | | | |
| AI | All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 1.270 |
| G | 0.660 |
| G1 | 0.820 |
| Х | 0.610 |
| X1 | 4.100 |
| X2 | 0.755 |
| X3 | 4.420 |
| X4 | 5.610 |
| Y | 1.270 |
| Y1 | 0.600 |
| Y2 | 1.020 |
| Y3 | 0.295 |
| Y4 | 1.825 |
| Y5 | 3.810 |
| Y6 | 0.180 |
| Y7 | 6.610 |



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