



DMN2053U

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
20V	29mΩ @ V <sub>GS</sub> = 10V	6.5A
	$35m\Omega @ V_{GS} = 4.5V$	5.4A

### Description

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- General Purpose Interfacing Switch
- **Power Management Functions**

#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

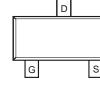
### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

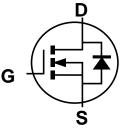


Top View

SOT23



Top View



Equivalent Circuit

#### **Ordering Information** (Notes 4)

	Part Number	Case	Packaging		
	DMN2053U-7	SOT23	3000/Tape & Reel		
DMN2053U-13		SOT23	10000/Tape & Reel		
Notes:	1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.				

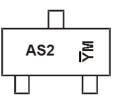
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



<u>AS2</u> = Product Type Marking Code <u>YM</u> = Date Code Marking  $\overline{Y}$  = Last Digit of Year (ex: 8 = 2018) M = Month (ex: 9 = September)

Date Code Ke	ey .											
Year	2017	2018	20	019	2020	2021		2022	2023	202	24	2025
Code	E	F		G	Н			J	K	L		М
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage		V <sub>GSS</sub>	±12	V	
Continuous Drain Current (Note 6)Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			۱ <sub>D</sub>	6.5 5.4	A
Pulsed Drain Current (380µs Pulse, Du	uty Cycle = 1%)		I <sub>DM</sub>	22	А

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

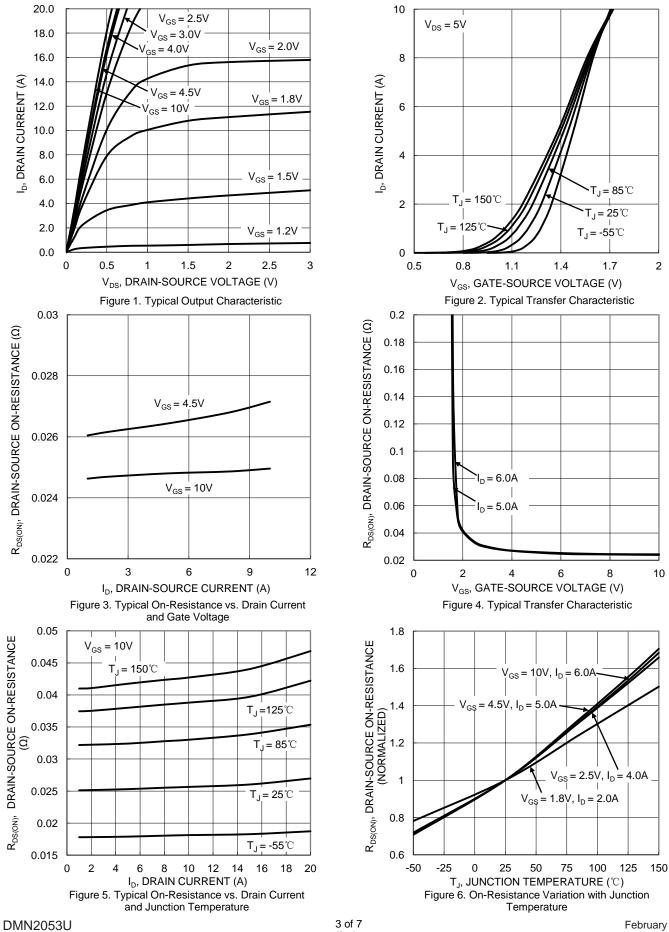
Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	160	°C/W	
Total Power Dissipation (Note 6)		PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0</sub> JA	93	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			51				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>		_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				•	•	•	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	0.95	1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			26	29		$V_{GS} = 10V, I_D = 6A$	
Static Drain-Source On-Resistance			28	35		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	35	48	mΩ	$V_{GS} = 2.5V, I_D = 4A$	
			47	91		$V_{GS} = 1.8V, I_D = 2A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.0	V	$V_{GS} = 0V, I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		414	—	pF		
Output Capacitance	C <sub>oss</sub>		58	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	43	—	pF	1 - 1.00012	
Gate Resistance	Rg	_	3.6	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	_	4.6	—	nC		
Gate-Source Charge	Q <sub>gs</sub>		0.5	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q <sub>gd</sub>		1.4	—	nC	$-I_D = 6A$	
Turn-On Delay Time	t <sub>D(ON)</sub>		2.6	_	ns		
Turn-On Rise Time	t <sub>R</sub>		2.9	_	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 5V,	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		13.5	—	ns	$R_L = 1.7\Omega, R_G = 6\Omega$	
Turn-Off Fall Time	tF	_	3.8	—	ns	1	
Reverse Recovery Time	t <sub>RR</sub>	_	6.8	—	ns	I <sub>F</sub> = 1.0A, di/dt = 100A/µs	
Reverse Recovery Charge	Q <sub>RR</sub>	_	1.2	_	nC	I <sub>F</sub> = 1.0A, di/dt = 100A/µs	

 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing Notes:



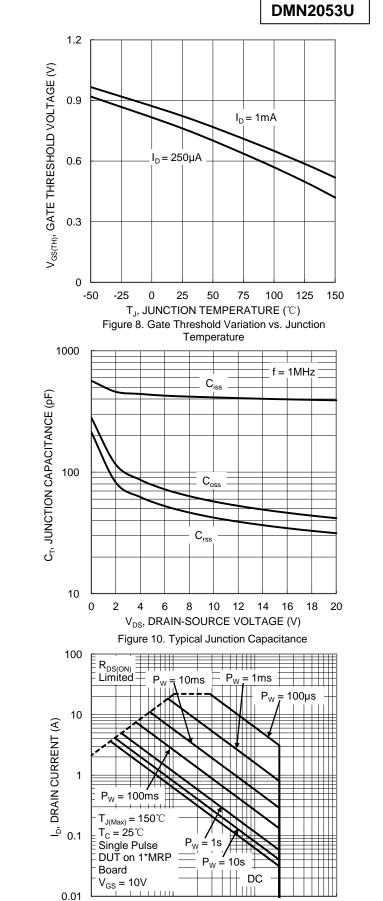
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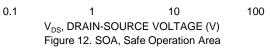


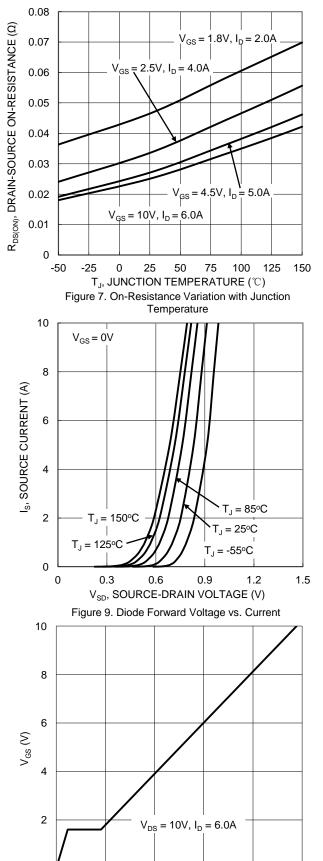
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2

4

 $Q_{a}(nC)$ 

Figure 11. Gate Charge

6

8

0 1

10



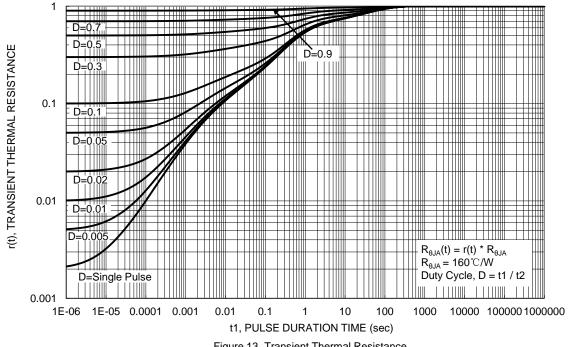


Figure 13. Transient Thermal Resistance

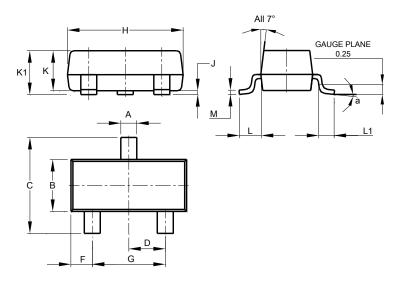


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

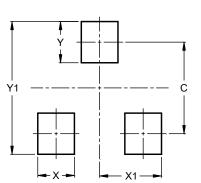
SOT23



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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