





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
20V	0.6Ω @ $V_{GS} = 4.5V$	0.9A
	0.8Ω @ $V_{GS} = 2.5V$	0.7A
	1.0Ω @ V _{GS} = 1.8V	0.5A
	1.6Ω @ V _{GS} = 1.5V	0.3A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{\rm DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Load Switch

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max.
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

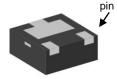
- Case: U-DFN1212-3 (Type C)
- Case Material: Molded Plastic;
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)

U-DFN1212-3 (Type C)

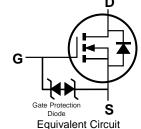


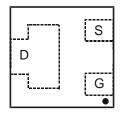


Top View



Bottom View





Pin-Out Top View

Ordering Information (Note 5)

I	Part Number	Case	Packaging
	DMN2400UFDQ-7	U-DFN1212-3 (Type C)	3,000/Tape & Reel
I	DMN2400UFDQ-13	U-DFN1212-3 (Type C)	10,000/Tape & Reel

- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
 - 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

U-DFN1212-3 (Type C)

K24 YM ●

K24 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	3 201	9 20	20	2021	2022	2023	2024	2025
Code	С	D	Е	F	G		Н	1	J	K	L	М
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	• • • • • • • • • • • • • • • • • • • •		iviai	Ap.	itiay	Juli	Jui	Aug	CCP		1101	D00



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	20	V		
Gate-Source Voltage	V_{GSS}	±12	V		
Continuous Drain Current (Note 7) V _{GS} = 4.5V	ID	0.9 0.7	А		
Continuous Drain Current (Note 7) V _{GS} = 2.5V	I _D	0.7 0.5	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	3.0	Α		
Maximum Body Diode Forward Current (Note 7)			Is	0.8	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P_{D}	0.44	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	283	°C/W
Total Power Dissipation (Note 7)	•	P _D	0.85	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{ heta JA}$	147	°C/W
Thermal Resistance, Junction to Case (Note 7)	•	$R_{\theta Jc}$	112	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

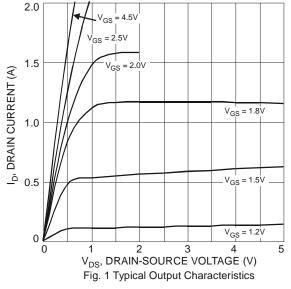
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

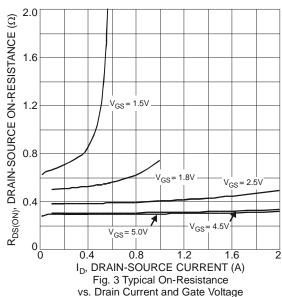
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	, ,	I	71	I.		1	
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	80 100	nA	$V_{DS} = 4.5V, V_{GS} = 0V$ $V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±1.0	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	0.45	-	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		-	0.35	0.6		$V_{GS} = 4.5V, I_D = 200mA$	
Static Drain-Source On-Resistance	Б	-	0.45	0.8	Ω	$V_{GS} = 2.5V, I_D = 200mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.6	1.0		V _{GS} = 1.8V, I _D = 100mA	
		-	0.7	1.6		$V_{GS} = 1.5V, I_D = 50mA$	
Forward Transfer Admittance	Y _{fs}	-	1.4	-	S	$V_{DS} = 3V, I_D = 200 \text{mA}$	
Diode Forward Voltage		-	0.7	1.2	V	V _{GS} = 0V, I _S = 500mA	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	-	37.0	-	pF	101/1/	
Output Capacitance	Coss	-	5.7	-	pF	$V_{DS} = 16V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	4.2	-	pF	1 = 1:01VII 12	
Gate Resistance	Rg	-	68	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	-	0.5	-	nC	15// 10//	
Gate-Source Charge	Qgs	-	0.07	-	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q _{qd}	-	0.1	-	nC	I _D = 250mA	
Turn-On Delay Time	t _{D(ON)}	-	4.06	-	ns		
Turn-On Rise Time	t _R	-	7.28	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	13.74	-	ns	$R_L = 47\Omega$, $R_G = 10\Omega$,	
Turn-Off Fall Time	t _F	-	10.54	-	ns	$I_D = 200 \text{mA}$	

Notes:

- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.







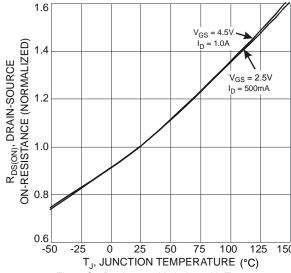
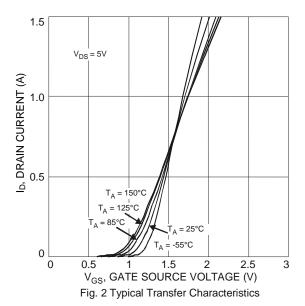


Fig. 5 On-Resistance Variation with Temperature



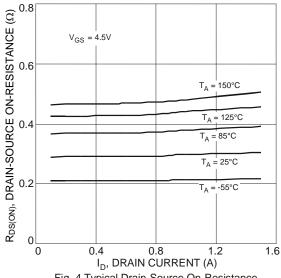


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

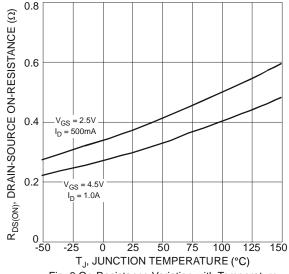


Fig. 6 On-Resistance Variation with Temperature



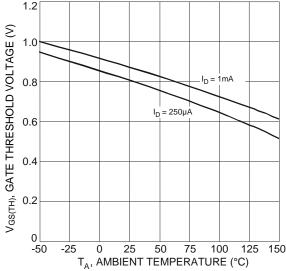
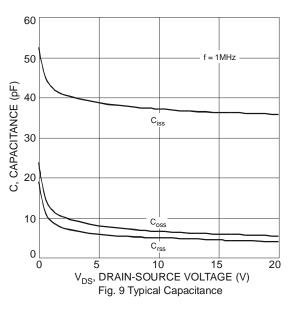
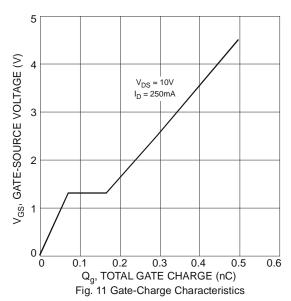
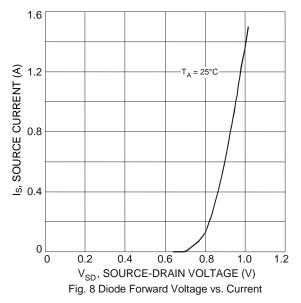
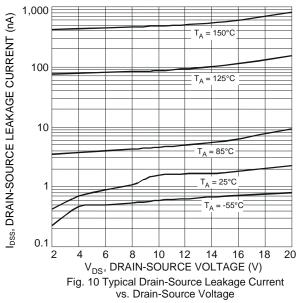


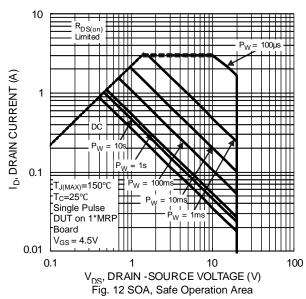
Fig. 7 Gate Threshold Variation vs. Ambient Temperature



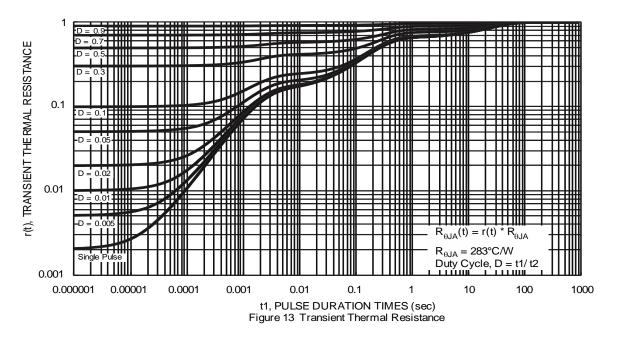








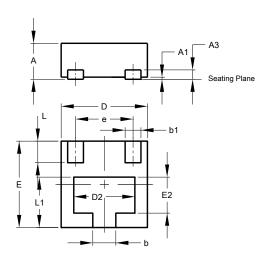




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1212-3 (Type C)



U-DFN1212-3							
Type C							
Dim	Min	Max	Тур				
Α	0.47	0.53	0.50				
A1	0	0.05	0.02				
A3	1	-	0.13				
b	0.27	0.37	0.32				
b1	0.17	0.27	0.22				
D	1.15	1.25	1.20				
D2	0.75	0.95	0.85				
е	-	-	0.80				
Е	1.15	1.25	1.20				
E2	0.40	0.60	0.50				
L	0.25	0.35	0.30				
L1	0.65	0.75	0.70				
All	Dimens	sions in	mm				

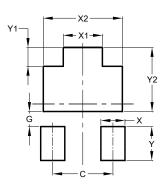
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Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1212-3 (Type C)



Dimensions	Value (in mm)				
С	0.800				
G	0.200				
Х	0.320				
X1	0.520				
X2	1.050				
Υ	0.450				
Y1	0.250				
Y2	0.850				

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