



Product Summary

BV _{DSS}	R _{DS(ON)} Max	Ι _D T _A = +25°C
-60V	$250m\Omega @ V_{GS} = -10V$	-2.1A
-00 v	$300m\Omega @ V_{GS} = -4.5V$	-1.9A

Description

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- **DC-DC Converters**
- **Power Management Functions**
- Uninterrupted Power Supply

Features and Benefits

- Low Gate Drive
- Low Input Capacitance
- Fast Switching Speed •
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

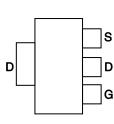
Mechanical Data

- Case: SOT223 •
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.112 grams (Approximate)

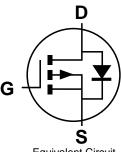


SOT223

Top View



Pin Out - Top View



Equivalent Circuit

Ordering Information (Note 4)

	Part Number Qualification		Case	Packaging	
	DMP6250SE-13	Standard	SOT223	2,500 / Tape & Reel	
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.					

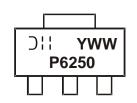
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



∃ = Manufacturer's Marking P6250 = Marking Code YWW = Date Code Marking \overline{Y} or Y = Year (ex: 7 = 2017) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value -60	Unit V	
Drain-Source Voltage	V _{DSS}			
Gate-Source Voltage (Note 5)		V _{GS}	±20	V
	T _A = +25°C T _A = +70°C	I _D	-2.1 -1.7	А
Continuous Drain Current (Note 6) $V_{GS} = -10V$	T _C = +25°C T _C = +70°C	I _D	-6.1 -4.9	А
Maximum Body Diode Continuous Current	ls	-1.8	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-11	А	
Single Pulsed Avalanche Current (Note 7) L = 0.1mH	I _{AS}	-12	А	
Single Pulsed Avalanche Energy (Note 7) L = 0.1mH		E _{AS}	8	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Dower Discinction (Note 6)	T _A = +25°C	D	1.8	W
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.1	
Thermal Resistance, Junction to Ambient (Note 6)		R _{θJA}	69	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	14	W
Thermal Resistance, Junction to Case (Note 6)	•	R _{θJC}	8.7	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	• • • • • •		. 76		•		
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	I _D = -250μA, V _{GS} = 0V	
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μA	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	Igss		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)						·	
Gate Threshold Voltage	V _{GS(TH)}	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			128	250		$V_{GS} = -10V, I_D = -1.0A$	
Static Drain-Source On-Resistance			128	250	mΩ	$V_{GS} = -10V, I_D = -1.9A$	
Static Drain-Source Off-Resistance	R _{DS(ON)}	_	156	300	11122	$V_{GS} = -4.5V, I_D = -0.5A$	
			158	300		V _{GS} = -4.5V, I _D = -1.5A	
Diode Forward Voltage	V _{SD}		_	-1.2	V	$V_{GS} = 0V, I_{S} = -2.0A$	
DYNAMIC CHARACTERISTICS (Note 9)						·	
Input Capacitance	Ciss	_	551	_	pF		
Output Capacitance	Coss	_	25.7	_	pF	− V _{DS} = -30V, V _{GS} = 0V − f= 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	19.1	_	pF		
Gate Resistance	Rg	_	12.1	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	4.8	_	nC		
Total Gate Charge (V _{GS} = -10V)	Qg	_	9.7	_	nC	N/ 201/ I 24	
Gate-Source Charge	Q _{gs}	_	1.5	_	nC	$V_{DS} = -30V, I_D = -2A$	
Gate-Drain Charge	Q _{gd}	_	1.6	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	6.3	_	ns	V _{DS} = -30V, V _{GS} = -10V,	
Turn-On Rise Time	t _R	_	10.3	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	91.4	_	ns	$R_G = 50\Omega, I_D = -1A$	
Turn-Off Fall Time	tF		39.8	_	ns	7	
Reverse Recovery Time	t _{RR}		9.2	_	ns		
Reverse Recovery Charge	Q _{RR}		3.9	—	nC	-I _S = -1A, di/dt= 100A/μs	

Notes:

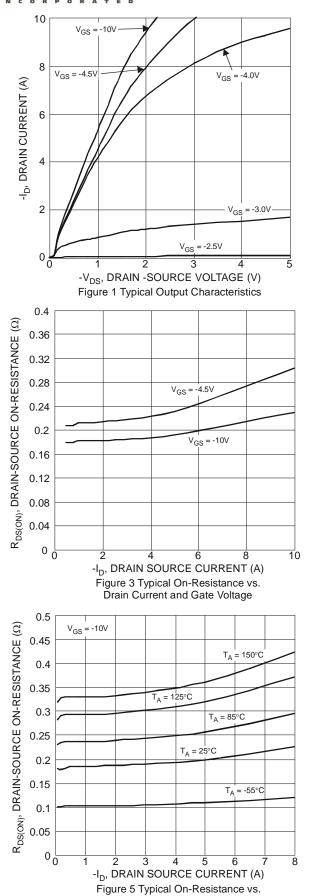
5. AEC-Q101 V_{GS} maximum is ±16V. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. For design aid only, not subject to production testing.





Drain Current and Temperature

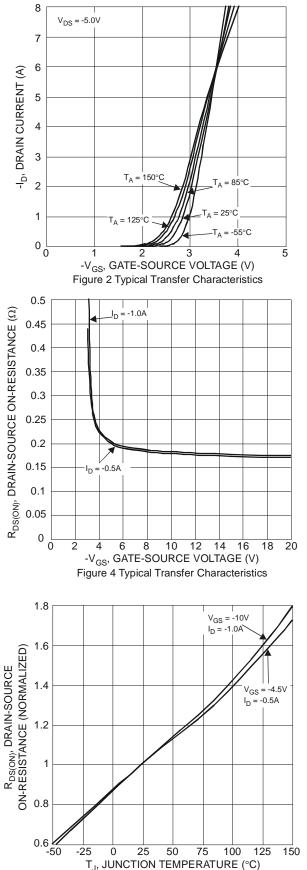
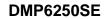
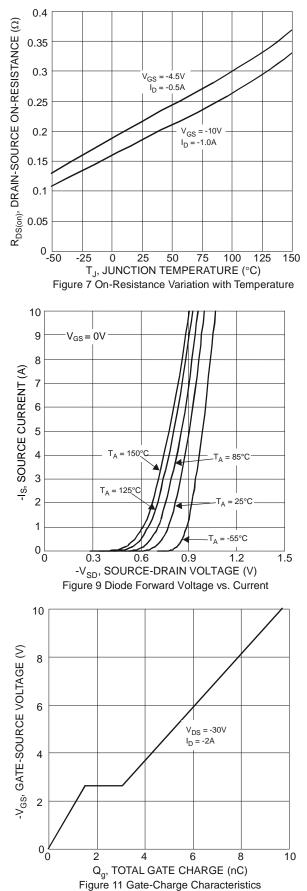


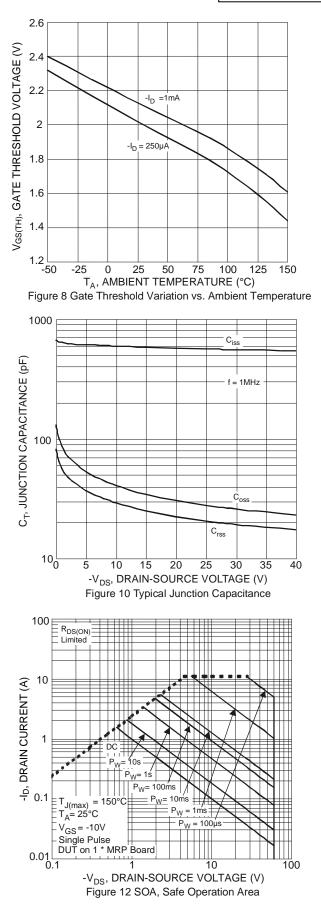
Figure 6 On-Resistance Variation with Temperature

DMP6250SE

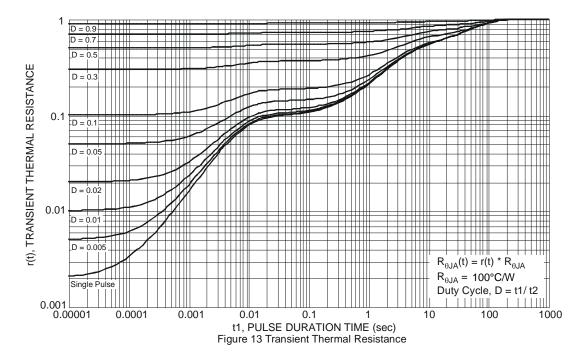








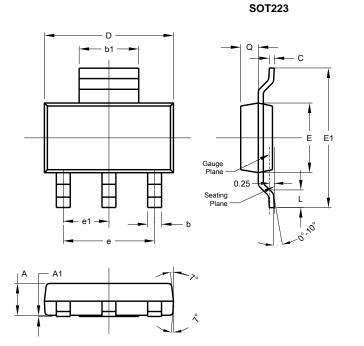






Package Outline Dimensions

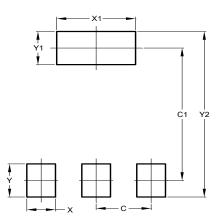
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
E	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



<u> </u>	[
Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

DMP6250SE Document Number DS36696 Rev. 2 - 2

SOT223



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2017, Diodes Incorporated

www.diodes.com