



80V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C		
	16.5mΩ @ V _{GS} = 10V	9.7A		
80V	20mΩ @ V _{GS} = 4.5V	8.8A		

Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- · Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Description and Applications

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize R_{DS(ON)}, yet maintain superior switching performance. This device is ideal for use in:

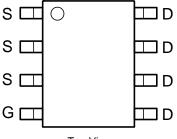
- Notebook Battery Power Management
- Loadswitches
- Backlighting
- Power Management Functions
- DC-DC Converters

Mechanical Data

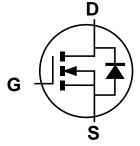
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.074 grams (Approximate)



Top View



Top View Internal Schematic



Equivalent Circuit

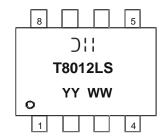
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT8012LSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



⊃¦¦ = Manufacturer's Marking T8012LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 16 = 2016) WW = Week (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	80	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Prain Correct (Note C) // 40//	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	9.7 7.8	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	11.6 9.3	А
Maximum Continuous Body Diode Forward Current (N	Is	3	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	80	Α		
Avalanche Current, L=0.1mH	I _{AS}	11.6	Α		
Avalanche Energy, L=0.1mH	E _{AS}	10.2	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

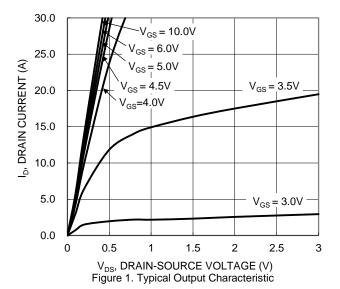
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_D	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	ם	80	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	48	°C/W
Total Power Dissipation (Note 6)		P_D	2	W
Thermal Decistores, Junction to Ambient (Note C)	Steady State	2	53	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	37	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	6.5	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	80		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 64V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	2	_	12.7	16.5	mΩ	$V_{GS} = 10V, I_D = 12A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	15	20		$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{ISS}	_	1,949	_		101/11/101/	
Output Capacitance	Coss	_	177	_	pF	$V_{DS} = 40V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C _{RSS}	_	10	_		I = IIVIDZ	
Gate Resistance	R _G	_	0.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_G	_	15	_			
Total Gate Charge (V _{GS} = 10V)	Q_G	_	34	_	nC	$V_{DS} = 40V, I_{D} = 12A$	
Gate-Source Charge	Q_GS	_	6	_	IIC	V _{DS} = 40V, I _D = 12A	
Gate-Drain Charge	Q_GD	_	4.5	_			
Turn-On Delay Time	t _{D(ON)}	_	4.9	_			
Turn-On Rise Time	t _R	_	3.8	_	ne	$V_{DD} = 40V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	16.5	_	ns	$I_D = 12A, R_G = 1.6\Omega$	
Turn-Off Fall Time	t _F	_	3.5	_			

 Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz. copper, with 1-inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:





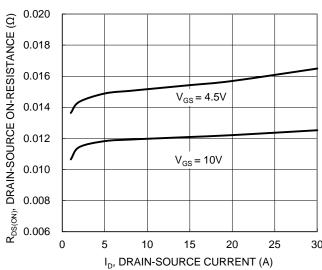


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

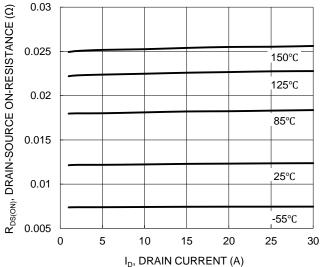


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

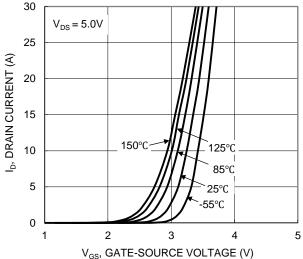


Figure 2. Typical Transfer Characteristic

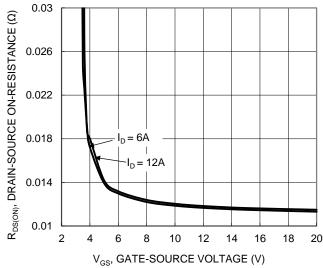
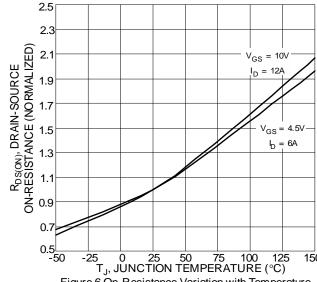
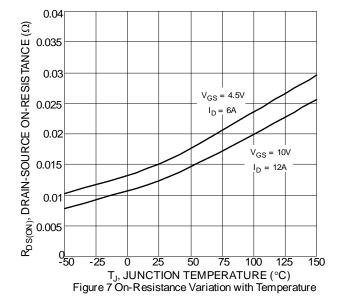
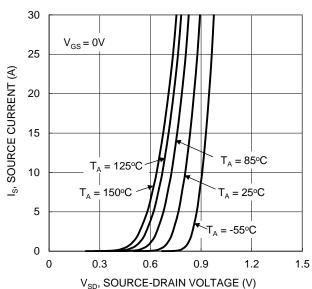


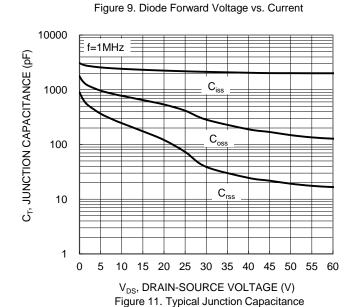
Figure 4. Typical Transfer Characteristic











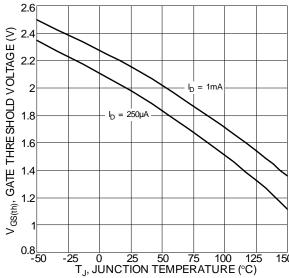


Figure 8 Gate Threshold Variation vs. Ambient Temperature

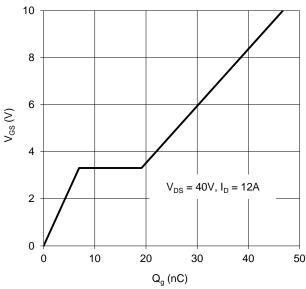


Figure 10. Gate Charge

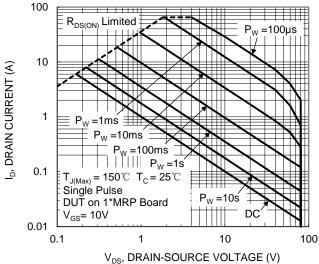


Figure 12. SOA, Safe Operation Area



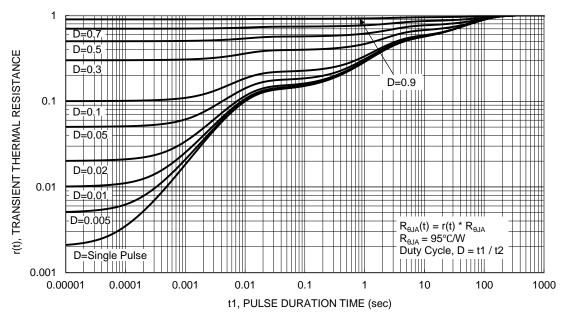
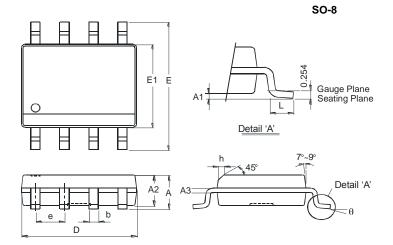


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

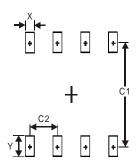


SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85 3.95			
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27



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