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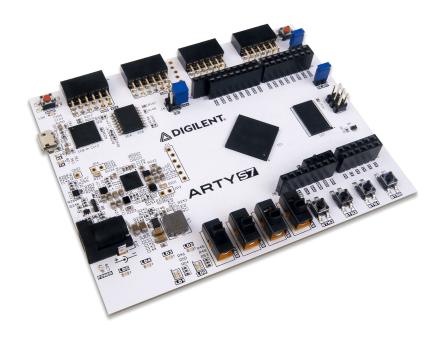
Arty S7: Spartan-7 FPGA Development Board

\$119.00

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SKU: 410-352-25











Arty S7 options: (Required)

Arty S7-25

O Arty S7-50

Quantity:





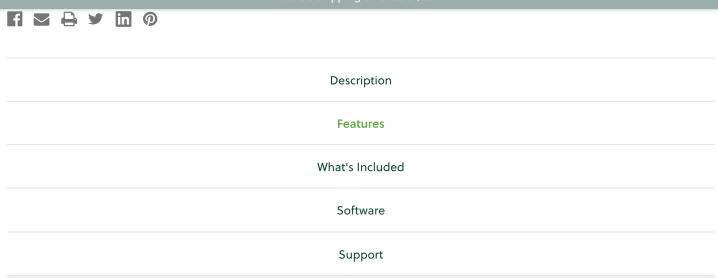








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	Arty S7-25	Arty S7-50
FPGA part	XC7S25-CSGA324	XC7S50-CSGA324
1 MSPS On-chip ADC	Yes	Yes
Logic Cells	23,360	52,160
Slices	3,650	8,150
Flip-flops	29,200	65,200
Block RAM (Kbits)	1,620	2,700
Clock Management Tiles	3	5
DSP Slices	80	120

- FPGA Features
 - o Internal clock speeds exceeding 450MHz;
 - o On-chip analog-to-digital converter (XADC).
 - o Programmable over JTAG and Quad-SPI Flash
- System Features
 - $\circ~$ 256 MB DDR3L with a 16-bit bus @ 325 MHz (650 MT/s)
 - o 128 Mbits Quad-SPI Flash
 - Due to supply chain constraints, either the S25FL127S or S25FL128S Flash Memory may be loaded on your board. These parts are not functionally equivalent, but flash programming through Vivado and the QSPI boot mode are not affected. More information can be found in the Arty S7 Reference Manual, available in the Support tab.
 - o 100 MHz External Clock
 - o USB-JTAG Programming circuitry (Micro B USB cable required add one free with your FPGA purchase!).
 - o Powered from USB or any 7V-15V source
- System Connectivity
 - o USB-UART Bridge
- Interaction and Sensory Devices
 - o 4 Switches
 - o 4 Buttons
 - o 1 Board Reset Button
 - o 1 FPGA Reset Button
 - o 4 Green LEDs
 - o 2 RGB LEDs
- Expansion Connectors
 - o 4 Pmod connectors
 - Arduino/chipKIT Shield connector







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Xilinx Debuts the First Spartan-7 FPGA

This video introduces the first member of the Spartan-7 family...



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