The documentation and process conversion measures necessary to comply with this document shall be completed by 24 September 2013.

INCH-POUND

MIL-PRF-19500/609J 24 June 2013 SUPERSEDING MIL-PRF-19500/609H 16 October 2012

PERFORMANCE SPECIFICATION SHEET

SEMICONDUCTOR DEVICE, DIODE, SILICON, SWITCHING, TYPES 1N6639, 1N6640, 1N6641, 1N6639US, 1N6640US, AND 1N6641US JAN, JANTX, JANTXV, JANS, JANHC, AND JANKC

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification sheet and MIL-PRF-19500.

1. SCOPE

- 1.1 <u>Scope</u>. This specification covers the performance requirements for controlled forward voltage switching diodes. Four levels of product assurance are provided for each device type as specified in MIL-PRF-19500.
- * 1.2 Physical dimensions. See figure 1 (DO-35), figure 2 (US), figure 3, and figure 4.
 - 1.3 Maximum ratings. $T_A = +25^{\circ}C$ (unless otherwise specified).

Types	VBR	VRWM	IO(PCB) T _A =75°C (1) (2)	IFSM t _p = 1/120 s	R _θ JL L = .375 inch (9.53 mm) (1) (2)	RθJEC L = 0 (1)	RθJA(PCB) (1)	TJ, TSTG
	<u>V (pk)</u>	<u>V (pk)</u>	<u>mA</u>	<u>A (pk)</u>	<u>°C/W</u>	<u>°C/W</u>	<u>°C/W</u>	<u>°C</u>
1N6639	100	75	300	2.5	150		250	-65 to +175
1N6639US	100	75	300	2.5		40	250	-65 to +175
1N6640	75	50	300	2.5	150		250	-65 to +175
1N6640US	75	50	300	2.5		40	250	-65 to +175
1N6641	75	50	300	2.5	150		250	-65 to +175
1N6641US	75	50	300	2.5		40	250	-65 to +175
1						I	l	

- (1) See figure 5 for temperature-current derating curve.
- (2) See figures, 6, 7, and 8 for thermal impedance curves. T_A = +75°C for both axial and Metal Electrode Leadless Face diodes (MELF) (US) on printed circuit board (PCB), PCB = FR4 .0625 inch (1.59 mm) 1-layer 1-Oz Cu, horizontal, in still air, pads for (US) = .061 inch (1.55 mm) x .105 inch (2.67 mm); pads for axial = .092 inch (2.34 mm) diameter, strip = 0.030 inch (0.76 mm) x 1 inch (25.4 mm) long, lead length L ≤ .187 inch (≤ 4.75 mm); R_{θ,IA} with a defined PCB thermal resistance condition included, is measured at I_O = 300mA.

Comments, suggestions, or questions on this document should be addressed to DLA Land and Maritime, ATTN: VAC, P.O. Box 3990, Columbus, OH 43218-3990, or emailed to Semiconductor@dla.mil. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at https://assist.dla.mil.

AMSC N/A FSC 5961

1.4 Primary electrical characteristics. Unless otherwise specified, primary electrical characteristics at T_A = +25°C.

	VF4	V _{F5}	I _{R1}	I _{R2}	t _{fr}	t _{rr}	C _{T1}
Types	IF = 200 mA (pulsed)	IF = 500 mA (pulsed)	at T _A = +25°C V _R = V _{RWM}	at T _A = +150°C V _R = V _{RWM}	IF = 200 mA	I _{RM} = I _F = 10 mA	V _R = 0
	V dc	V dc	nA dc	μA dc	<u>ns</u>	<u>ns</u>	<u>pF</u>
1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	1.0 1.1	1.2	100 100 100	90 90 90	10 10 10	4.0 4.0 5.0	2.5 2.5 3.0

2. APPLICABLE DOCUMENTS

2.1 <u>General</u>. The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 <u>Specifications, standards, and handbooks</u>. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

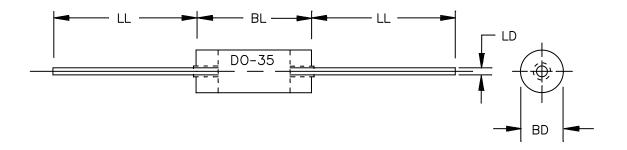
MIL-PRF-19500 - Semiconductor Devices, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-750 - Test Methods for Semiconductor Devices.

2.3 <u>Order of precedence</u>. Unless otherwise noted herein or in the contract, in the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

^{* (}Copies of these documents are available online at http://quicksearch.dla.mil or https://assist.dla.mil or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

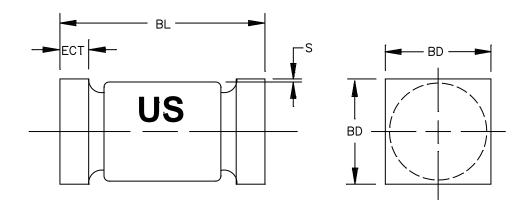


Symbol	Inc	hes	Millin	Notes		
	Min	Max	Min	Max		
BD	.056	.080	1.42	2.03	3, 4	
BL	.130	.180	3.30	4.57	4	
LD	.018	.022	0.46	0.56	5	
LL	1.00	1.50	25.40	38.10		

- 1. Dimensions are in inches.
- 2. Millimeters are given for general information only.
- 3. Dimension BD shall be measured at the largest diameter.
- 4. The minimum dimension of BD shall apply over at least .065 inch (1.65 mm) of dimension BL.
- 5. The specified lead diameter applies in the zone between .050 inch (1.27 mm) from the diode body to the end of the lead. Outside of this zone lead shall not exceed BD.
- 6. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.

Types 1N6639, 1N6640, and 1N6641.

FIGURE 1. Physical dimensions (DO-35).

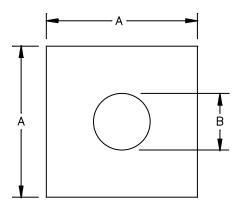


Symbol	Dimensions						
	Inch	nes	Millimeters				
	Min	Max	Min	Max			
BD	.070	.085	1.78	2.16			
BL	.165	.195	4.19	4.95			
ECT	.019	.028	0.48	0.71			
S	.003		0.08				

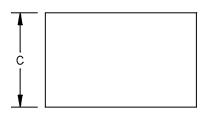
- Dimensions are in inches.
 Millimeters are given for general information only.
- 3. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.

Types 1N6639US, 1N6640US, and 1N6641US.

FIGURE 2. Physical dimensions of surface mount family.



BACKSIDE IS CATHODE



Ltr	Dimensions						
	Inc	ches	Millimeters				
	Min	Max	Min	Max			
Α	.014	.018	0.360	0.460			
В	.005	.007	0.120	0.180			
С	.008	.012	0.20	0.30			

- 1. Dimensions are in inches. Millimeters are given for general information only.
- 2. Element evaluation accomplished utilizing TO-5 package.
- 3. The physical characteristics of the die are:

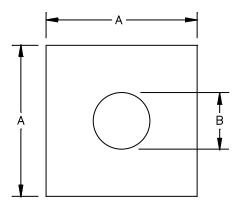
Metallization:

Top (anode): Al Back (cathode): Au

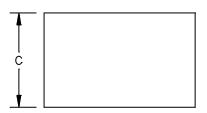
Al thickness: 25,000 Å minimum. Gold thickness: 4,000 Å minimum.

Chip thickness: .010 inch (0.25 mm) ±.002 inch (0.05 mm).

FIGURE 3. Physical dimensions JANHCA and JANKCA die.



BACKSIDE IS CATHODE



Ltr	Dimensions						
	Inc	ches	Millimeters				
	Min	Max	Min	Max			
Α	.0130	.0170	0.330	0.432			
В	.0059	.0061	0.150	0.155			
С	.008	.012	0.20	0.30			

- 1. Dimensions are in inches. Millimeters are given for general information only.
- 2. Element evaluation accomplished utilizing TO-5 package.
- 3. The physical characteristics of the die are:

Metallization:

Top (anode): Al Back (cathode): Au

Al thickness: 25,000 $\mbox{\normalfont\AA}$ minimum. Gold thickness: 4,000 $\mbox{\normalfont\AA}$ minimum.

Chip thickness: .010 inches (0.25 mm) ±.002 inches (0.05 mm).

* FIGURE 4. Physical dimensions, JANHCB and JANKCB die.

3. REQUIREMENTS

- 3.1 General. The individual item requirements shall be as specified in MIL-PRF-19500 and as modified herein.
- 3.2 <u>Qualification</u>. Devices furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.2 and 6.3).
- 3.3 <u>Abbreviations, symbols, and definitions</u>. Abbreviations, symbols, and definitions used herein shall be as specified in MIL-PRF-19500.
- * 3.4 <u>Interface and physical dimensions</u>. Interface and physical dimensions shall be as specified in MIL-PRF-19500 and on figures 1, 2, 3, and 4 herein.
- 3.4.1 <u>Lead finish</u>. Lead finish shall be solderable in accordance with MIL-PRF-19500, MIL-STD-750, and herein. Where a choice of lead finish is desired, it shall be specified in the acquisition document (see 6.2).
- 3.4.2 <u>Diode construction</u>. These devices shall be constructed in a manner and using materials which enable the diodes to meet the applicable requirements of MIL-PRF-19500 and this document.
 - All devices shall be of metallurgically bonded, thermally matched, non-cavity, double-plug construction in accordance with the requirements of category I (see MIL-PRF-19500).
 - The 'US' version shall be structurally identical to the non-'US' versions except for end-cap lead attachment.
- 3.5 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in 1.3, 1.4, and table I.
 - 3.6 Electrical test requirements. The electrical test requirements shall be as specified in table I herein.
- 3.7 <u>Marking</u>. Marking shall be in accordance with MIL-PRF-19500. Manufacturer's identification and date code shall be marked on the devices. Initial container package marking shall be in accordance with MIL-PRF-19500. The polarity shall be indicated with a contrasting color band to denote the cathode end. The prefixes JAN, JANTX, JANTXV, and JANS may be abbreviated as J, JX, JV, and JS respectively. The part number may be reduced to J6639, JX6639, JV6639, or JS6639. No color coding shall be permitted for part numbering.
- 3.7.1 <u>US devices</u>. For 'US' version devices only, all marking, except polarity (and serial number for JANS) may be omitted from the body, but shall be retained on the initial container. 'US' devices shall be marked with a cathode band as a minimum. For 'US' devices, a minimum of three evenly spaced contrasting color dots around the periphery of the cathode end may be used.
- 3.8 <u>Workmanship</u>. Semiconductor devices shall be processed in such a manner as to be uniform in quality and shall be free from other defects that will affect life, serviceability, or appearance.

4. VERIFICATION

- 4.1 Classification of inspections. The inspection requirements specified herein are classified as follows:
 - a. Qualification inspection (see 4.2).
 - b. Screening (see 4.3).
 - c. Conformance inspection (see 4.4).

- 4.2 <u>Qualification inspection</u>. Qualification inspection shall be in accordance with MIL-PRF-19500 and as specified herein.
- 4.2.1 <u>JANHC and JANKC qualification</u>. JANHC and JANKC qualification inspection shall be in accordance with MIL-PRF-19500.
- 4.2.2 <u>Group E qualification</u>. Group E inspection shall be performed for qualification or re-qualification only. In case qualification was awarded to a prior revision of the specification sheet that did not require the performance of table II tests, the tests specified in table II herein that were not performed in the prior revision shall be performed on the first inspection lot of this revision to maintain qualification.
- 4.3 <u>Screening (JANS, JANTXV, and JANTX levels only)</u>. Screening shall be in accordance with table E-IV of MIL-PRF-19500, and as specified herein. Specified electrical measurements shall be made in accordance with table I herein. Devices that exceed the limits of table I herein shall not be acceptable.

Screen (see table E-IV of MIL-PRF-19500)	JANS level	JANTXV and JANTX level
2	Not required	Not required
3b (1) 3c	Not applicable Thermal impedance (see 4.3.3)	Not applicable Thermal impedance (see 4.3.3)
4	Not applicable	Not applicable
5	Not applicable for axial leaded devices	Not applicable
6	Not applicable	Not applicable
9	IR1	Not applicable
10	Method 1038 of MIL-STD-750, condition A	Method 1038 of MIL-STD-750, condition A
11	VF4 (1N6640, 1N6641) or VF5 (1N6639), IR1; and V(BR); Δ IR1 ±15 nA dc or 100 percent of initial value whichever is greater	V _{F4} (1N6640, 1N6641) or V _{F5} (1N6639) and I _{R1}
12	Required, see 4.3.2	Required, see 4.3.2
13	Subgroups 2 and 3 of table I herein; $\Delta I_{R1} \le 100$ percent of initial reading or 15 nA dc, whichever is greater; $\Delta V_{F4} \le \pm 0.030$ V dc for 1N6640, 1N6641), $\Delta V_{F5} \le \pm 0.030$ V dc for 1N6639, (scope display, see 4.5.3)	Subgroup 2 of table I herein; $\Delta I_{R1} \le 100$ percent of initial reading or 15 nA dc, whichever is greater; $\Delta V_{F4} \le \pm 0.030$ V dc for 1N6640, 1N6641), $\Delta V_{F5} \le \pm 0.030$ V dc for 1N6639, (scope display, see 4.5.3)

- (1) Thermal impedance shall be performed any time after screen 3; JANTX and JANTXV levels do not need to be repeated in screening requirements.
- 4.3.1 <u>Screening (JANHC and JANKC)</u>. Screening of JANHC and JANKC die shall be in accordance with appendix G of MIL-PRF-19500. Burn-in duration for the JANKC level follows JANS requirements; the JANHC follows JANTX requirements.

- 4.3.2 <u>Power burn-in conditions</u>. Power burn-in conditions are as follows (See 4.5.1): Method 1038 of MIL-STD-750, condition B. $V_R = \text{rated } V_{RWM}$; f = 50 60 Hz; $I_O = 300$ mA dc or $I_F = 300$ mA dc minimum. T_A = 75°C maximum. The maximum current density of small die shall be submitted to the qualifying activity for approval. Alternate mounting conditions shall be submitted to the qualifying activity for approval. With approval of the qualifying activity and preparing activity, alternate burn-in criteria (hours, bias conditions, and mounting conditions) may be used. A justification demonstrating equivalence is required. In addition, the manufacturing site's burn-in data and performance history will be essential criteria for burn-in modification approval.
- 4.3.3 Thermal impedance measurements. The thermal impedance measurements shall be performed in accordance with method 3101 or 4081 of MIL-STD-750, as applicable, using the guidelines in that method for determining I_H and I_M. Measurement delay time $(t_{MD}) = 70 \mu s$ max, t_{H} shall be 10 ms maximum. The thermal impedance limit shall comply with the thermal impedance graphs in figures, 6, 7, and 8 (less than or equal to the curve value at the same t_{H} time) and shall be less than the process determined statistical maximum limit as outlined in method 3101 or 4081 of MIL-STD-750, as applicable. See group E, subgroup 4 of table II herein.
 - 4.4 Conformance inspection. Conformance inspection shall be in accordance with MIL-PRF-19500.
- 4.4.1 <u>Group A inspection</u>. Group A inspection shall be conducted in accordance with table E-V of MIL-PRF-19500, table I herein, and as specified herein. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein.
- 4.4.2 <u>Group B inspection</u>. Group B inspection shall be conducted in accordance with the conditions specified for subgroup testing in tables E-VIA (JANS) and E-VIB (JAN, JANTX, and JANTXV) of MIL-PRF-19500 and 4.4.2.1 and 4.4.2.2 herein. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein. Read and record the change in thermal impedance. The accept criteria is a maximum change of 10 percent for group B, subgroups 3 and 4 for JANS, or group B, subgroup 2 for JANTX or JANTXV.

4.4.2.1 Group B inspection, table E-VIA (JANS) of MIL-PRF-19500.

Subgroup	Method	Conditions
В3	1056	0°C to 100°C, 25 cycles.
В3	1051	-55°C to 175°C, 100 cycles.
В3	2101	Decap analysis; scribe and break only
B4	1037	t_{ON} = t_{Off} = 1 minute minimum; 2,000 cycles; l_{O} = 300 mA pulsed; in lieu of ac conditions, a dc condition of l_{F} = 300 mA may be used.
B5	1027	I_O = 300 mA minimum, V_R = rated V_{RWM} , f = 50 - 60 Hz (see 4.5.1).
		Option 1: Adjust IO or TA to obtain a minimum TJ of +225°C, t = 216 hours, n = 45, c = 0.
		Option 2: Adjust IO or TA to obtain a minimum TJ of 175°C, t = 1,000 hours, n = 45, c = 0.
B6	4081	L = .375 inch (9.53 mm); $R_{\theta JL} = 150^{\circ}\text{C/W}$ maximum, $R_{\theta JEC} = 40^{\circ}\text{C/W}$ maximum (see 4.3.3).

4.4.2.2 <u>Group B inspection, table E-VIB (JAN, JANTX, and JANTXV) of MIL-PRF-19500</u>. Leaded samples from the same lot may be used in lieu of 'US' suffix sample for life test.

Subgroup	Method	<u>Conditions</u>
B2	1056	0°C to + 100°C, 10 cycles.
B2	1051	-55°C to +175°C, 45 cycles including screening.
В3	1027	$V(pk)$ = rated V_{RWM} ; f = 50 - 60 Hz; I_O = 300 mA dc minimum; adjust T_A or I_O to obtain a minimum T_J of +150°C. (See 4.5.1)
B4	2101	Decap analysis; scribe and break only
В6	1032	$T_A = +175^{\circ}C.$

- 4.4.3 <u>Group C inspection</u>. Group C inspection shall be conducted in accordance with the conditions specified for subgroup testing in table E-VII of MIL-PRF-19500 and as follows. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein.
 - 4.4.3.1 Group C inspection, table E-V of MIL-PRF-19500.

	Subgroup	Method	Condition
	C2	1056	0°C to + 100°C, 10 cycles.
	C2	1051	-55°C to + 175°C, 45 cycles including screening.
*	C2	2036	Axial devices - Tension: Condition A, 6 pounds, t = 15 s. Fatigue: Condition E for all types.
*	C2	2036	US devices - Tension: Condition A, 6 pounds, $t = 15 s$. Suitable fixtures may be used to pull the end-caps in a manner which does not aid construction. Reference to axial lead may be interpreted as end-cap with fixtures used for mounting. (Lead fatigue is not applicable to US diodes).
	C5	4081	L = .375 inch (9.53 mm), R $_{ heta$ JL = 150°C/W maximum; R $_{ heta}$ JEC = 40°C/W; (see 4.3.3).
	C6	1026	1,000 hours minimum, $V(pk)$ = rated V_{RWM} ; f = 50 - 60 Hz; I_O = 300 mA dc minimum; adjust T_A or I_O to obtain a minimum T_J of +150°C. (See 4.5.1)

- 4.4.4 <u>Group E inspection</u>. Group E inspection shall be conducted in accordance with the tests and conditions specified for subgroup testing in table E-IX of MIL-PRF-19500 and table II herein. Electrical measurements (endpoints) shall be in accordance with table I, subgroup 2 herein.
 - 4.5 Methods of inspection. Methods of inspection shall be as specified in the appropriate tables and as follows.
- 4.5.1 <u>Free air power burn-in and life tests</u>. The use of a current limiting or ballast resistor is permitted provided that each device under test still sees the full P_t (minimum) and that the minimum applied voltage, where applicable, maintained through-out the burn-in period. Method 3100 of MIL-STD-750 shall be used to measure T_J .

- 4.5.2 <u>Forward-recovery voltage and time</u>. Forward recovery time shall be measured as the time interval between zero time and the point where the pulse has decreased to 110 percent of the steady-state value of V_F when $I_F = 200$ mA dc. The maximum rise time of the response detector shall be 1 ns. The maximum forward recovery voltage (V_{fr}) during the forward recovery interval shall also be measured.
- 4.5.3 Scope display evaluation. Scope display evaluation shall be stable in accordance with method 4023 of MIL-STD-750. Scope display may be performed on automatic test equipment for screening only with the approval of the qualifying activity. Scope display in table I herein shall be performed on an oscilloscope. Reverse current (IBR) over the knee shall be $100 \, \mu A$ peak.
- 4.5.4 <u>Pulse measurements</u>. Conditions for pulse measurements shall be as specified in section 4 of MIL-STD-750.

TABLE I. Group A inspection.

		MIL-STD-750		Limit		l lmit
Inspection 1/	Method	Conditions	Symbol	Min	Max	Unit
Subgroup 1						
Visual and mechanical examination	2071					
Subgroup 2 Thermal impedance	3101	See 4.3.3	Z _{0JX}			°C/W
Forward voltage 1N6640, 1N6640US	4011	I _F = 1 mA dc pulsed (see 4.5.4)	V _{F1}	0.540	0.620	V dc
Forward voltage 1N6640, 1N6640US	4011	IF = 50 mA dc pulsed (see 4.5.4)	V _{F2}	0.760	0.860	V dc
Forward voltage 1N6640, 1N6640US	4011	I _F = 100 mA dc pulsed (see 4.5.4)	VF3	0.820	0.920	V dc
Forward voltage 1N6640, 1N6640US 1N6641, 1N6641US	4011	I _F = 200 mA dc pulsed (see 4.5.4)	VF4	0.870 0.870	1.00 1.10	V dc V dc
Forward voltage 1N6639, 1N6639US	4011	I _F = 500 mA dc pulsed (see 4.5.4)	V _{F5}		1.20	V dc
Breakdown voltage 1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	4021	I _(BR) = 10 μA dc	V _{BR}	100 75 75		V dc V dc V dc
Reverse current 1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	4016	DC method; V _R = 75 V dc V _R = 50 V dc V _R = 50 V dc	I _{R1}		100 100 100	nA dc nA dc nA dc
Subgroup 3						
High temperature operation:		T _A = +150°C				
Reverse current 1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	4016	DC method $V_R = 75 \text{ V dc}$ $V_R = 50 \text{ V dc}$ $V_R = 50 \text{ V dc}$	I _{R2}		90 90 90	μΑ dc μΑ dc μΑ dc

See footnotes at end of table.

TABLE I. Group A inspection Continued.

In an antian Al		MIL-STD-750	Oh. al	Limit		Unit
Inspection 1/	Method	Conditions	Symbol	Min	Max	Unit
Subgroup 3 continued Low-temperature operation:		T _A = -55°C				
Forward voltage 1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	4011	Pulsed IF = 500 mA pulsed (see 4.5.4) IF = 200 mA pulsed (see 4.5.4) IF = 200 mA pulsed (see 4.5.4)	VF6		1.3 1.1 1.2	V dc V dc V dc
Subgroup 4 Capacitance 1N6639, 1N6639US	4001	$V_R = 0 \text{ V dc}$; $V_{sig} = 50 \text{ mV(p-p)}$, $f = 1 \text{ MHz}$	C _{T1}		2.5	pF
1N6640, 1N6640US 1N6641, 1N6641US					2.5 3.0	pF pF
Reverse recovery time 1N6639, 1N6639US 1N6640, 1N6640US 1N6641, 1N6641US	4031	Condition A, I _F = I _{RM} = 10 mA dc	t _{rr}		4.0 4.0 5.0	ns ns ns
Scope display	4023	Method 4023 of MIL-STD-750, Figure 4023-3, -7, -9, -10 only				
Subgroup 5						
Not applicable						
Subgroup 6						
Surge current	4066	IFSM = 2.5 A(pk) ten surges at 1 per minute (max), surge duration of 1/120 seconds				
Electrical measurements		See table I, subgroup 2 herein				
Subgroup 7						
Forward recovery voltage and time	4026	I _F = 200 mA dc (see 4.5.2)	V _{fr}		5.0	V(pk)
			tfr		10.0	ns

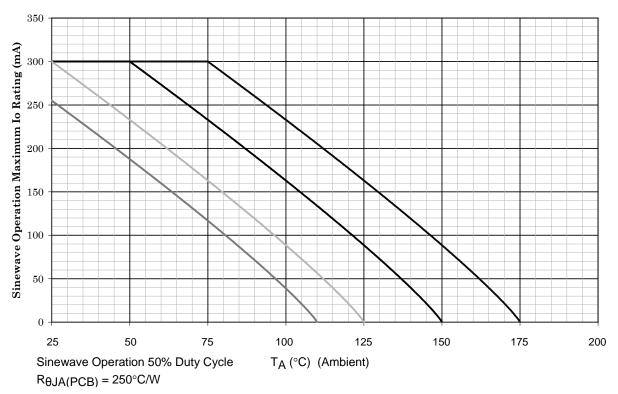
^{1/} For sampling plan, see MIL-PRF-19500.

* TABLE II. Group E inspection (all quality levels) for qualification and requalification.

Increation	MIL-STD-750		Qualification	
Inspection	Method	Conditions	inspection	
Subgroup 1			n = 45, c = 0	
Thermal shock (glass strain) 1/	1056	20 cycles, condition D except low temperature shall be achieved using liquid nitrogen (-195°C). A visual inspection for cracked glass shall be performed.		
Temperature cycling 1/	1051	-65°C to +175°C, 500 cycles.		
Hermetic seal	1071	Gross leak only.		
Electrical measurement		See table I, subgroup 2.		
Subgroup 2			n = 45, c = 0	
Intermittent operation life	1037	10,000 cycles; I _F = 300 mA dc, T _{on} = T _{off} = 1 minute.		
Electrical measurement		See table I, subgroup 2.		
Subgroup 4				
Thermal impedance curves		See MIL-PRF-19500.		
Subgroup 5				
Not applicable				
Subgroup 6			n = 11	
ESD	1020			
Subgroup 8			n = 45	
Resistance to glass cracking	1057	Test condition B. Test until failure occurs or to a maximum of 25 cycles, whichever comes first.		
Subgroup 10			n = 22, c = 0	
Potted environment test	1054			

^{1/} Separate samples may be used for each test.

TEMPERATURE-CURRENT DERATING CURVE 1N6639, 1N6640, 1N6641, 1N6639US, 1N6640US, 1N6641US

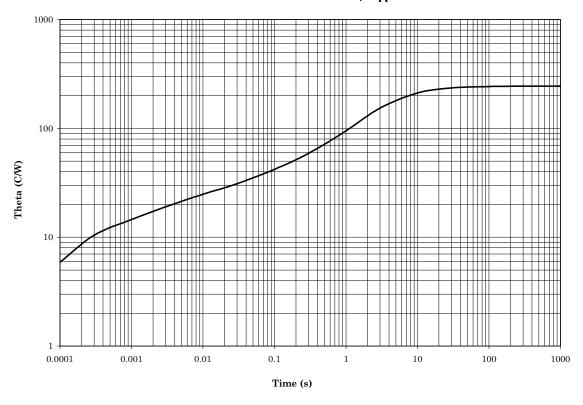


NOTES:

- 1. All devices are capable of operating at ≤ T_J specified on this curve. Any parallel line to this curve will intersect the appropriate current for the desired maximum T_J allowed.
- 2. Derate design curve constrained by the maximum junction temperatures and current rating specified. (See 1.3.)
- 3. Derate design curve chosen at $T_J \le 150$ °C, where the maximum temperature of electrical test is performed.
- Derate design curves chosen at T_J ≤ 125°C, and 110°C to show power rating where most users want to limit T_J in their application.

FIGURE 5. Temperature current derating.

MAXIMUM THERMAL IMPEDANCE PLOTS AXIAL OR 'US' PARTS, T_A = 55°C

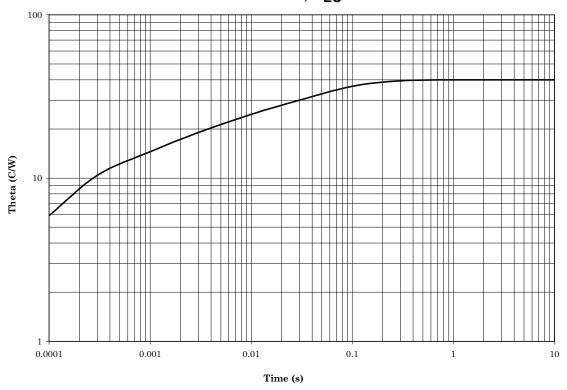


 $R_{\theta JA} = 250^{\circ}C/W$

NOTE: $Z_{\theta JX} = 25^{\circ} \text{C/W}$ maximum at $t_H = 10 \text{ms}$.

FIGURE 6. Thermal impedance - all glass devices.

MAXIMUM THERMAL IMPEDANCE PLOTS 'US' PARTS, T_{EC} = 25°C

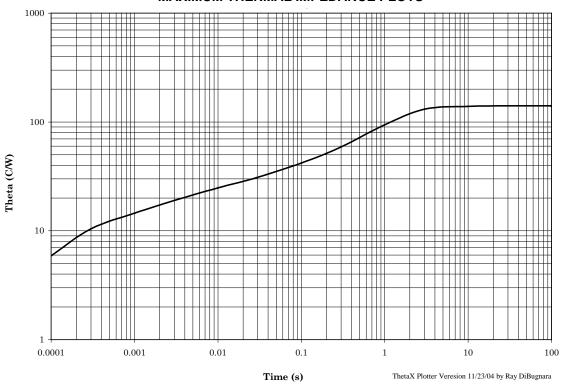


 $R_{\theta JEC} = 40^{\circ}C/W$

NOTE: $Z_{\theta JX} = 25^{\circ}C/W$ maximum at $t_H = 10$ ms.

FIGURE 7. Thermal impedance - all 'US' devices.

JTXV1N6638 FAMILY DO-35 T_L = 25°C MAXIMUM THERMAL IMPEDANCE PLOTS



 $R_{\theta JL} = 150^{\circ}C/W$

NOTE: $Z_{\theta JX} = 25^{\circ}C/W$ maximum at $t_H = 10$ ms.

FIGURE 8. Thermal impedance - all axial lead devices.

5. PACKAGING

5.1 <u>Packaging</u>. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activities within the Military Service or Defense Agency, or within the Military Service's system commands. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory. The notes specified in MIL-PRF-19500 are applicable to this specification.)

- 6.1 <u>Intended use</u>. Semiconductors conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
 - 6.2 Acquisition requirements. Acquisition documents should specify the following:
 - a. Title, number, and date of this specification.
 - b. Lead finish (see 3.4.1).
 - c. Serialization (see 4.3).
 - d. Packaging requirements (see 5.1).
 - e. Product assurance level and type designator. See MIL-PRF-19500.
 - f. Destructive physical analysis (when requested).
- 6.3 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List (QML 19500) whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DLA Land and Maritime, ATTN: VQE, P.O. Box 3990, Columbus, OH 43218-3990 or e-mail vqe.chief@dla.mil . An online listing of products qualified to this specification may be found in the Qualified Products Database (QPD) at https://assist.dla.mil.
- 6.4 <u>Cross reference substitution list</u>. The 1N4150 design is unsuitable for space flight applications and is therefore prohibited and will no longer be built nor qualified to JANS. JANS1N4150-1 is prohibited and will no longer be built nor qualified. Devices in stock are acceptable provided the date code does not exceed 9412 (the date of implementation of MIL-S-19500/231F). A PIN for PIN replacement table follows, and these devices are directly interchangeable. The JANS1N6640 will be used in place of the JANS1N4150-1.

Non-preferred PIN	Preferred PIN
JANS1N4150-1	JANS1N6640
JANS1N4150UR-1	JANS1N6640US

6.5 Suppliers of die. The qualified die suppliers with the applicable letter version (e.g., JANHCB1N6639) will be identified on the QML.

JANHC and JANKC ordering information				
PIN	Man	ufacturer		
FIN	52GC4	43611		
1N6639 1N6640 1N6641	JANHCA1N6639, 6640, and 6641 JANKCA1N6639, 6640, and 6641	JANHCB1N6639, 6640, and 6641 JANKCB1N6639, 6640, and 6641		

6.6 Changes from previous issue. The margins of this specification are marked with asterisks to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Preparing activity:

(Project 5961-2013-063)

DLA - CC

Custodians:

Army - CR Navy - EC

Air Force - 85

DLA - CC

NASA - NA

Review activities:

Army - AR, MI, SM

Navy - AS, MC

Air Force - 19, 71, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at https://assist.dla.mil.