

HA1SV24005

120W DC/DC Power Modules



FEATURES

- High efficiency : 89% @110Vin full load
- Size:61.0mm*57.9mm*12.7mm(2.4" *2.28" *0.5")
- Industry standard pin out and footprint
- Fixed frequency operation
- Input UVP/ OVP
- Hiccup output over current protection (OCP)
- Hiccup output over voltage protection (OVP)
- Output current limited protection(OCL)
- Auto recovery OTP
- Positive enable (negative enable optional)
- Monotonic startup into normal
- 3000V isolation and reinforce insulation
- No minimum load required
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- EN61373,EN50155
- EN60950-1

Delphi Series HA1SV24, half Brick Family DC/DC Power Modules: 53~154V in, 24V/5A out, 120W

The Delphi Module HA1SV24005, half brick, 53~154V input, single output, isolated DC/DC converter is the latest offering from a world leader in power system and technology and manufacturing — Delta Electronics, Inc. This product provides up to 100 watts power in an industry standard footprint and pin out. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performances, as well as extremely high reliability under highly stressful operating conditions.

APPLICATIONS

Railway /Transportation system



TECHNICAL SPECIFICATIONS

PARAMETER	NOTES and CONDITIONS	HA1SV24005				
		Min.	Typ. Max.		Units	
1. ABSOLUTE MAXIMUM RATINGS			21			
1.1 Input Voltage	EN50155	53	110	154	Vdc	
1.2 Input surge withstand	<100ms			250	Vdc	
1.3 Operating Ambient Temperature		-40		100	°C	
1.4 Storage Temperature		-55		125	°C	
1.5 Input/Output Isolation Voltage	reinforce			3000	Vrms	
2. INPUT CHARACTERISTICS						
2.1 Operating Input Voltage		53	110	154	Vdc	
2.2 Input Under-Voltage Lockout						
2.2.1 Turn-On Voltage Threshold		49	51	53	Vdc	
2.2.2 Turn-Off Voltage Threshold		46	48	50	Vdc	
2.3 Input Over-Voltage Lockout						
2.3.1 Turn-On Voltage Threshold		154	158	162	Vdc	
2.3.2 Turn-Off Voltage Threshold		158	162	166	Vdc	
2.4 Maximum Input Current	Full Load, 53Vin		2.55	2.65	A	
2.5 No-Load Input Current	Vin=110V, Io=0A		25	35	mA	
2.6 Off Converter Input Current	Vin=110V		10	25	mA	
2.7 Input Reflected-Ripple Current (pk-pk)	Vin=110V, Io=full load, Cin=150uF/400V		35		mA	
3. OUTPUT CHARACTERISTICS						
3.1 Output Voltage Set Point	Vin=110V, lo=0, Tc=25°C	23.8	24	24.2	Vdc	
3.1.1 Load regulation	Vin=110V, Io=Io min to Io max		±0.05	±0.2	%	
3.1.2 Line regulation	Vin=53V to154V, Io=full load		±0.01	±0.2	%	
3.1.3 Temperature regulation	Vin=110V, Tc= min to max case temperatrue		±0.004	± 0.007	%/℃	
3.2 Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth					
3.2.1 Peak-to-Peak	Full Load,		70	120	mV	
3.2.2 rms	Full Load,		23	35	mV	
3.3 Operating Output Current Range		0		5	А	
3.4 Output DC Current-Limit Inception		5.2	5.5	5.8	А	
4.DYNAMIC CHARACTERISTICS						
4.1 Output Voltage Current Transient	110V, 0.1A/µs					
4.1.1 Positive Step Change in Output Current	50% lo.max to 75%		1500	2000	mV	
4.1.2 Negative Step Change in Output Current	75% lo.max to 50%		1500	2000	mV	
4.2 Turn-On Transient						
4.2.1 Start-Up Time, From On/Off Control						
			60	100	ms	
4.2.2 Start-Up Time, From Input			60 48			
4.2.2 Start-Up Time, From Input 4.2.3 Rise time(Vout from 10% to 90%)			48	100 80 50	ms	
4.2.3 Rise time(Vout from 10% to 90%)	Vout nominal at full load			80	ms ms	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor	Vout nominal at full load		48 30	80	ms	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY		85.5	48 30 300	80 50	ms <mark>ms</mark> μF	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load	Vin=110V	<u>85.5</u> 85.5	48 30 300 88.5	80 50 91.5	ms ms μF %	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load		<mark>85.5</mark> 85.5	48 30 300	80 50	ms <mark>ms</mark> μF	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS	Vin=110V		48 30 300 88.5	80 50 91.5 90	ms ms μF %	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output	Vin=110V		48 30 300 88.5	80 50 91.5 90 3000	ms ms μF %	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base	Vin=110V		48 30 300 88.5	80 50 91.5 90 3000 1500	ms ms µF % % Vac	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base	Vin=110V		48 30 300 88.5 87.5	80 50 91.5 90 3000	ms µF % % Vac Vac Vac	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance	Vin=110V		48 30 300 88.5	80 50 91.5 90 3000 1500	ms ms µF % % Vac	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS	Vin=110V		48 30 300 88.5 87.5 100	80 50 91.5 90 3000 1500	ms μF % % Vac Vac Vac MΩ	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 Gov Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency	Vin=110V		48 30 300 88.5 87.5	80 50 91.5 90 3000 1500	ms µF % % Vac Vac Vac	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control,	Vin=110V	85.5	48 30 300 88.5 87.5 100	80 50 91.5 90 3000 1500 1500	ms μF % % Vac Vac Vac MΩ	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High	Vin=110V	85.5	48 30 300 88.5 87.5 100	80 50 91.5 90 3000 1500 1500 1500	ms ms μF % % Vac Vac Vac Vac KHz	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High 7.2.2 Logic Low	Vin=110V	85.5 3 0	48 30 300 88.5 87.5 100	80 50 91.5 90 30000 1500 1500 1500 5 1	ms ms μF % % Vac Vac Vac Vac KHz V V	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High 7.2.2 Logic Low 7.3 Output Voltage Trim Range	Vin=110V Vin=110V	85.5 3 0 -10	48 30 300 88.5 87.5 100 100 300	80 50 91.5 90 3000 1500 1500 1500 5 1 1	ms μF % Vac Vac Vac Vac KHz V V V V V V V V V %	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.100 CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High 7.2.2 Logic Low 7.3 Output Voltage Trim Range 7.4 Output VOver-Voltage Protection	Vin=110V	85.5 3 0	48 30 300 88.5 87.5 100	80 50 91.5 90 30000 1500 1500 1500 5 1	ms ms μF % % Vac Vac Vac Vac Vac KHz V	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 5.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High 7.2.2 Logic Low 7.3 Output Voltage Trim Range 7.4 Output Voltage Trim Range 7.4 Output VOLTAGE Protection 8 GENERAL SPECIFICATIONS	Vin=110V Vin=110V	85.5 3 0 -10	48 30 300 88.5 87.5 100 300 300	80 50 91.5 90 3000 1500 1500 1500 5 1 1	ms ms μF % % Vac Vac Vac Vac Vac Vac Vac Vac Vac Vac	
4.2.3 Rise time(Vout from 10% to 90%) 4.3 Maximum output capacitor 5. EFFICIENCY 5.1 100% Load 6.2 60% Load 6.ISOLATION CHARACTERISTICS 6.1 Input to Output 6.2 Input to base 6.3 Output to base 6.4 Isolation Resistance 7. FEATURE CHARACTERISTICS 7.1 Switching Frequency 7.2 ON/OFF Control, 7.2.1 Logic High 7.2.2 Logic Low 7.3 Output Voltage Trim Range	Vin=110V Vin=110V	85.5 3 0 -10	48 30 300 88.5 87.5 100 100 300	80 50 91.5 90 3000 1500 1500 1500 5 1 1	ms ms μF % % Vac Vac Vac Vac MΩ kHz V V V	

 $(T_A=25^{\circ}C, Natural convection, Vin=110Vdc, nominal Vout unless otherwise noted;$



ELECTRICAL CHARACTERISTICS CURVES

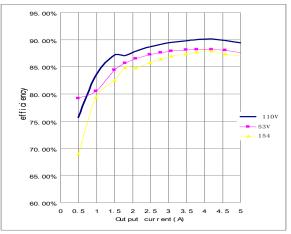


Figure 1: Efficiency vs. load current for 53, 110and 154 input voltage at 25°C.

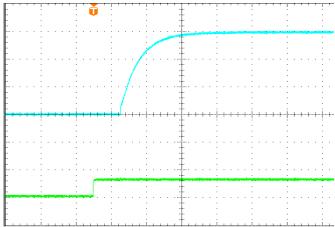
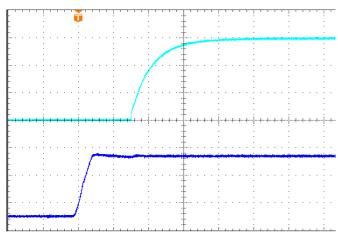
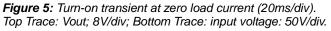


Figure 3: Turn-on transient at zero load current) (20ms/div). Top Trace: Vout; 5V/div; Bottom Trace: ON/OFF input: 5V/div.





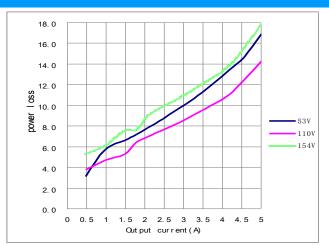


Figure 2: Power dissipation vs. load current for 53, 110and 154 input voltage at 25°C.

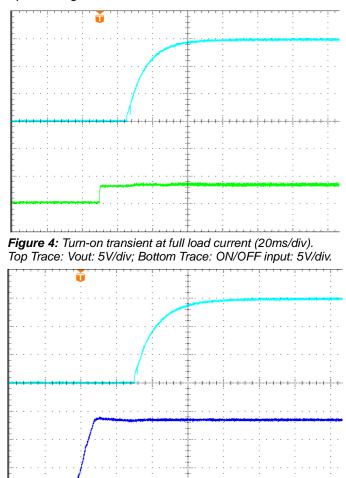


Figure 6: Turn-on transient at full load current (20ms/div). Top Trace: Vout; 8V/div; Bottom Trace: input voltage: 50V/div.



ELECTRICAL CHARACTERISTICS CURVES

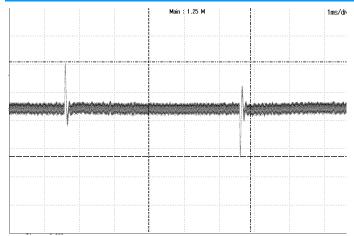


Figure 7: Output voltage response to step-change in load current (50%-75%-50% of full load; di/dt = $0.1A/\mu$ s). Bottom Trace: Vout;500mV/div; Time: 1ms/div

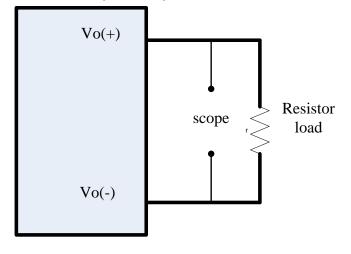


Figure 9: Output voltage noise and ripple measurement test setu

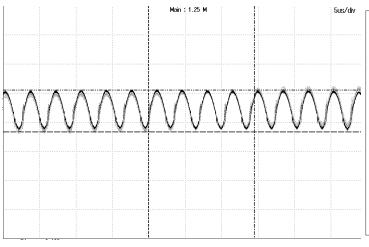


Figure 10: Output voltage ripple at nominal input voltage and max load current (50 mV/div, 5us/div) Bandwidth: 20 MHz.

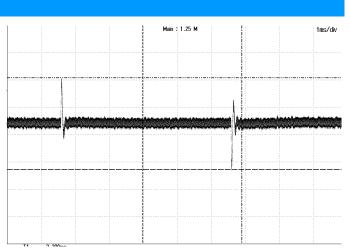
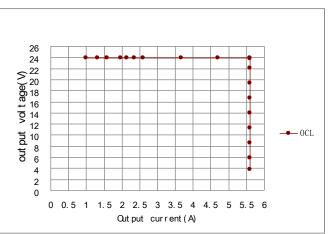
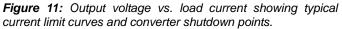


Figure 8: Output voltage response to step-change in load current (50%-75%-50% of full load; di/dt = 2.5A/µs). Bottom Trace: Vout; 500mV/div; Time: 1ms/div





P4



DESIGN CONSIDERATIONS

Input Source Impedance

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few μ H, we advise 150 μ F electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the input of the module to improve the stability.

Layout and EMC Considerations

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. Below is the reference design for an input filter tested with HA1SV24005 to meet class A in CISSPR 22.

Schematic and Components List

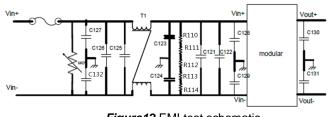


Figure12 EMI test schematic

C121=120Uf/400V

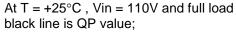
C123,C124,C127,C132 = 220pF/275VAC

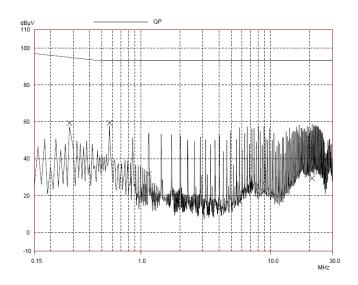
C128,C129,C130,C131=2200pF/300VAC

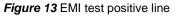
C122,C125,C126=0.47uF/250V

T1=3.4mH, common choke R110,R111,R112,E113,R114=300kohm/1206

Test Result:







Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the end-user's safety agency standard, i.e., UL60950-1, CSA C22.2 NO. 60950-1 2nd and IEC 60950-1 2nd : 2005 and EN 60950-1 2nd: 2006+A11+A1: 2010, if the system in which the power module is to be used must meet safety agency requirements.

Basic insulation based on 110 Vdc input is provided between the input and output of the module for the purpose of applying insulation requirements when the input to this DC-to-DC converter is identified as TNV-2 or SELV. An additional evaluation is needed if the source is other than TNV-2 or SELV.

When the input source is SELV circuit, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 110 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from the ac mains by reinforced or double insulation.
- The input terminals of the module are not operator accessible.
- A SELV reliability test is conducted on the system where the module is used, in combination with the module, to ensure that under a single fault, hazardous voltage does not appear at the module's output.



When installed into a Class II equipment (without grounding), spacing consideration should be given to the end-use installation, as the spacing between the module and mounting surface have not been evaluated.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 10A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

FEATURES DESCRIPTIONS

Over-Current Protection

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will shut down, and will try to restart after shutdown(hiccup mode). If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

Over-Voltage Protection

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the protection circuit will constrain the max duty cycle to limit the output voltage, if the output voltage continuously increases the modules will shut down, and then restart after a hiccup-time (hiccup mode).

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the module will shut down.The module will restart after the temperature is within specification.

Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi (-) terminal. The switch can be an open collector or open drain. For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi (-). For positive logic if the remote on/off feature is not used, please leave the on/off pin to floating.

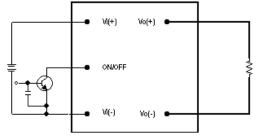


Figure 14: Remote on/off implementation

Output Voltage Adjustment (TRIM)

To increase or decrease the output voltage set point, connect an external resistor between the TRIM pin and SENSE(+) pin or SENSE(-) pin. The TRIM pin should be left open if this feature is not used.

When the input voltage is different, the trim up voltage is different. The relationship between maximum trim up voltage and input voltage is specified as follow :

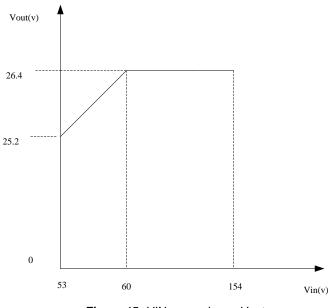


Figure 15: VIN vs maximum Vout



For trim down, the external resistor value required to obtain a percentage of output voltage change \triangle % is defined as:

$$Rtrim - down = \left[\frac{10*Vnom*(1-\Delta)}{Vnom-Vnom*(1-\Delta)}\right](K\Omega)$$

Ex. When Trim-down -10% (24V×0.9=21.6V)
$$Rtrim - down = \left[\frac{10*24*0.9}{24-24*0.9}\right](K\Omega) = 90(K\Omega)$$

For trim up, the external resistor value required to obtain a percentage output voltage change \triangle % is defined as:

$$Rtrim - up = \left[\frac{[Vnom(1+\Delta) - 2.5]*10}{\Delta \times 2.5} - 10\right] K\Omega$$

Ex. When Trim-up +10% (24V×1.1=26.2V)

$$Rtrim - up = \left[\frac{\left[24(1+0.1) - 2.5\right]*10}{\Delta \times 2.5} - 10\right] = 946(K\Omega)$$

The output voltage can be increased by both the remote sense and the trim, however the maximum increase is the larger of either the remote sense or the trim, not the sum of both

The output voltage can be increased by both the remote sense and the trim, however the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

Pin function

The pin was difine as follow in figure 20 ,we will explain the pin function:

+IN, -IN. DC voltage inputs.

Gate IN. The Gate IN pin on a driver module may be used as a logic enable/disable input.When Gate IN is pull low (<1V,referenced to –Vin),the module is turned off . when Gate IN is floating (open collector) ,the module is turned on .The open circuit voltage of Gate in PIN is less than 5V.

Gate OUT. The pulsed signal at the Gate OUT pin of a regulating driver module is used to synchronously drive the surge circuit in order to meet the RIA12 surge needed. If you don't used this function, please floating it. **+OUT, -OUT.** DC voltage outputs.

T(TRIM). Provides fixed or variable adjustment of the module output.

Trimming down. Allows output voltage of the module to be trimmed down, with a decrease in efficiency .ripple as a percent of output voltage goes up and input range widens since input voltage dropout(loss of regulation) moves down

Trimming up. Reverses the above effects.

-Sense,+Sense. Provides for locating the point of optimal voltage regulation external to the converter.

THERMAL CONSIDERATIONS

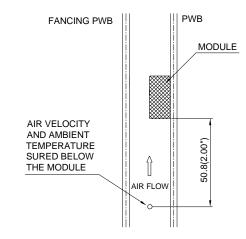
Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 16: Wind tunnel test setup

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



THERMAL CURVES

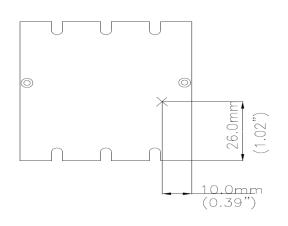


Figure 17: * temperature measured point

THERMAL CURVES

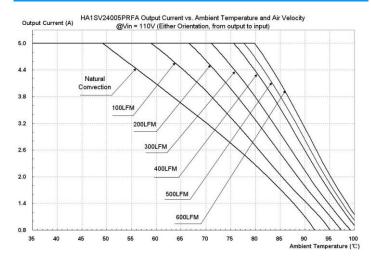


Figure 18: Output current vs. ambient temperature and air velocity @Vin=110V(Either Orientation, airflow from output to input, with heat spreader)

THERMAL CURVES

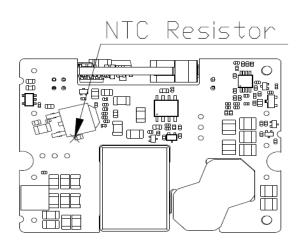


Figure 19: NTC resistor location



LEAD FREE (SAC) PROCESS RECOMMEND TEMP. PROFILE

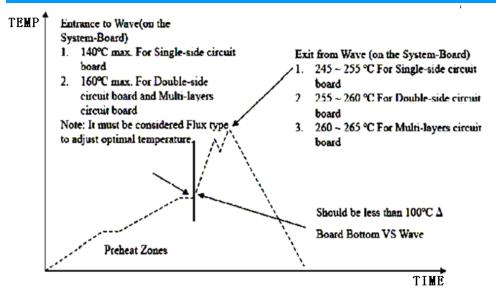
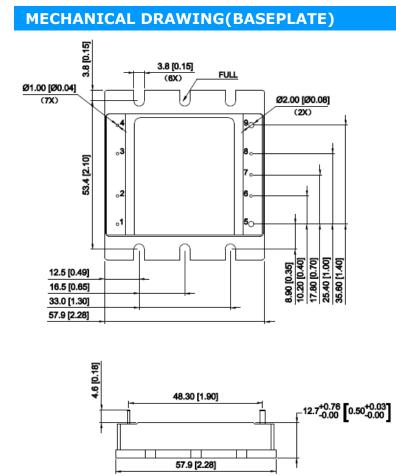
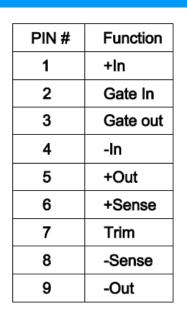


Figure 19 recommended temperature profile for lead-free wave soldering





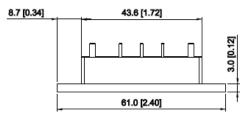
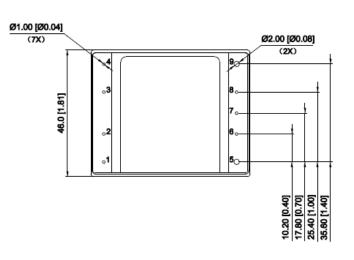


Figure 20 the pin function and mechanical drawing

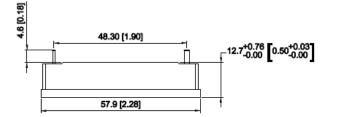
P9

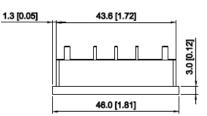


MECHANICAL DRAWING(WITHOUT MOUNTING HOLE)



PIN #	Function
1	+In
2	Gate In
3	Gate out
4	-In
5	+Out
6	+Sense
7	Trim
8	-Sense
9	-Out





DIMENSIONAL TOLERANCE

- X ±0.3mm
- x.x ±0.2mm
- x.xx ±0.1mm



PART NUMBERING SYSTEM

PART NOPBERING STSTEP									
Н	A1	S	V	24	005	N	N	F	А
Form	Input	Number of	Product	Output	Output	ON/OFF	Pin		Option Code
Factor	Voltage	Outputs	Series	Voltage	Current	Logic	Length		
H -	110-	S –	V-	24-	005-	N –	N - 0.145"	F -	A – Baseplate
Half Brick	53V~154V	Single	Series	24V	5A	Negative	R - 0.170" M - SMD pin	RoHS 6/6 (Lead Free)	H-without mounting hole
			Number			P -		Space - RoHS5/6	
						Positive			

MODEL LIST						
MODEL NAME	INPUT		OUTPUT		EFF @ 100% LOAD	
HA1SV24005PRFA	53V~154V	2.6A	24V	5A	89%	
HA1SV24005PRFH	53V~154V	2.6A	24V	5A	89%	

Default remote on/off logic is negative and pin length is 0.170"

For different remote on/off logic and pin length, please refer to part numbering system above or contact your local sales office. For modules with through-hole pins and the optional heatspreader, they are intended for wave soldering assembly onto system boards; please do not subject such modules through reflow temperature profile.

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