



Dual Low Leakage Diode

T.OI-09

ID100 / ID101

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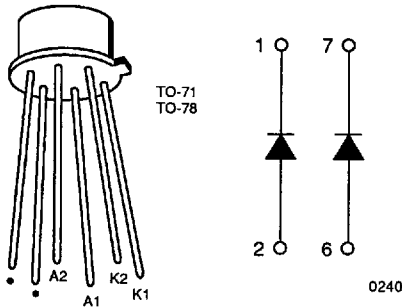
GENERAL DESCRIPTION

The ID100 and ID101 are monolithic dual diodes intended for use in applications requiring extremely low leakage currents. Applications include interstage coupling with reverse isolation, signal clipping and clamping and protection of ultra low leakage FET differential dual and operational amplifiers.

FEATURES

- $I_R = 0.1 \text{ pA}$ (Typical)
- $BV_R > 30 \text{ V}$
- $C_{RSS} = 0.75 \text{ pF}$ (Typical)

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

($T_A = 25^\circ\text{C}$ unless otherwise specified)

Diode Reverse Voltage	30V
Diode to Diode Voltage	$\pm 50 \text{ V}$
Forward Current	20mA
Reverse Current	100 μA
Storage Temperature Range	-65°C to $+200^\circ\text{C}$
Operating Temperature Range	-55°C to $+150^\circ\text{C}$
Lead Temperature (Soldering, 10sec)	$+300^\circ\text{C}$
Power Dissipation	300mW
Derate above 25°C	2.4mW/ $^\circ\text{C}$

NOTE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ORDERING INFORMATION

Part	Package	Temperature Range
ID100	Hermetic TO-78	-55°C to $+150^\circ\text{C}$
XID100	Sorted Chips in Carriers	-55°C to $+150^\circ\text{C}$
ID101	Hermetic TO-71	-55°C to $+150^\circ\text{C}$
XID101	Sorted Chips in Carriers	-55°C to $+150^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	ID100 / ID101			UNITS	TEST CONDITIONS
		MIN	TYP	MAX		
V_F	Forward Voltage Drop	0.8		1.1	V	$I_F = 10 \text{ mA}$
BV_R	Reverse Breakdown Voltage	30			V	$I_R = 1 \mu\text{A}$
I_R	Reverse Leakage Current		0.1		μA	$V_R = 1 \text{ V}$
			2.0	10		$V_R = 10 \text{ V}$
				10	nA	$T_A = 125^\circ\text{C}$
$ I_{R1} - I_{R2} $	Differential Leakage Current			3	pA	$V_R = 10 \text{ V}$
C_{RSS}	Total Reverse Capacitance		0.75	1	pF	$V_R = 10 \text{ V}, f = 1 \text{ Hz}$ (Note 1)

NOTES: 1. For design reference only, not 100% tested.

2. Pins 3 and 5 should not be connected together nor connected to the circuit in any way.

TYPICAL PERFORMANCE CHARACTERISTICS

