

DATA SHEET : CG2415X3

0.5 to 6.0 GHz High Power SPDT Switch

Features

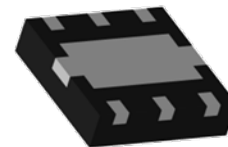
- Control voltage:
 $VC(H) = 1.8 \text{ to } 5.3 \text{ V (3.0V TYP.)}$
 $VC(L) = -0.2 \text{ to } 0.2 \text{ V (0V TYP.)}$
- Low insertion loss:
 $L_{ins1} = 0.30 \text{ dB TYP. @ } f = 0.5 \text{ to } 2.0 \text{ GHz}$
 $L_{ins2} = 0.35 \text{ dB TYP. @ } f = 2.0 \text{ to } 2.5 \text{ GHz}$
 $L_{ins3} = 0.40 \text{ dB TYP. @ } f = 2.5 \text{ to } 3.8 \text{ GHz}$
 $L_{ins4} = 0.45 \text{ dB TYP. @ } f = 3.8 \text{ to } 6.0 \text{ GHz}$
- High isolation:
 $ISL1 = 32 \text{ dB TYP. @ } f = 0.5 \text{ to } 2.0 \text{ GHz}$
 $ISL2 = 32 \text{ dB TYP. @ } f = 2.0 \text{ to } 2.5 \text{ GHz}$
 $ISL3 = 32 \text{ dB TYP. @ } f = 2.5 \text{ to } 3.8 \text{ GHz}$
 $ISL4 = 26 \text{ dB TYP. @ } f = 3.8 \text{ to } 6.0 \text{ GHz}$
- Handling power:
 $P_{in(0.5dB)} = +34 \text{ dBm TYP.}$
@ $VC(H) = 3.0 \text{ V, } VC(L) = 0 \text{ V}$

Package

- 6-pin Thin SON Package (X3)
(1.5mm x 1.5mm x 0.37mm)

Description

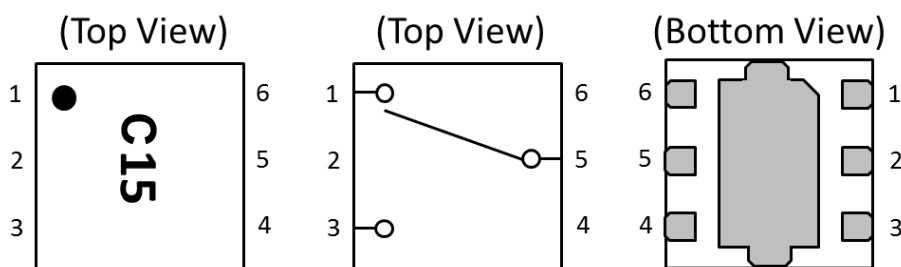
- The CG2415X3 is a GaAs MMIC SPDT (Single Pole Double Throw) switch which was developed for WiMAX and wireless LAN



Applications

- WiMAX and wireless LAN
(IEEE802.11a/b/g/n/ac), etc.

Pin Configuration and Internal Block Diagram



Pin No.	Pin Name
1	RF1
2	GND
3	RF2
4	VC2
5	RFC
6	VC1

Remark Exposed pad : GND

Ordering Information

Part Number	Order Number	Package	Marking	Supplying Form
CG2415X3-C2	CG2415X3-C2	6-pin TSON (Pb-Free)	C15	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 1, 6 face the perforation side of the tape • Qty 10 kpcs/reel

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Absolute Maximum Ratings

(T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 ^{Note 1}	V
Input Power	P _{in}	+34.5 ^{Note 2}	dBm
Operating Ambient Temperature	T _A	-45~+85	°C
Storage Temperature	T _{stg}	-55~+150	°C

- Note**
1. $|VC1 - VC2| \leq 6.0V$
 2. $3.0V \leq |VC1 - VC2| \leq 5.0V$

Recommended Operating Range

(T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.5	-	6.0	GHz
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.3	V
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V

Truth Table

VC1	VC2	RFC-RF1	RFC-RF2
High	Low	ON	OFF
Low	High	OFF	ON

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Electrical Characteristics 1

($T_A=+25\text{ }^\circ\text{C}$, $V_C(H)=3.0\text{V}$, $V_C(L)=0\text{V}$, $Z_0=50\ \Omega$, DC Block Capacitance=8pF, unless otherwise specified)

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Insertion Loss	L_{INS1}	$f=0.5$ to 2.0GHz ^{Note 1}	---	0.30	0.50	dB
	L_{INS2}	$f=2.0$ to 2.5GHz	---	0.35	0.55	dB
	L_{INS3}	$f=2.5$ to 3.8GHz	---	0.40	0.60	dB
	L_{INS4}	$f=3.8$ to 6.0GHz	---	0.45	0.70	dB
Isolation	$ISL1$	$f=0.5$ to 2.0GHz ^{Note 1}	29	32	---	dB
	$ISL2$	$f=2.0$ to 2.5GHz	29	32	---	dB
	$ISL3$	$f=2.5$ to 3.8GHz	29	32	---	dB
	$ISL4$	$f=3.8$ to 6.0GHz	23	26	---	dB
Return Loss	$RL1$	$f=0.5$ to 2.0GHz ^{Note 1}	15	20	---	dB
	$RL2$	$f=2.0$ to 2.5GHz	15	20	---	dB
	$RL3$	$f=2.5$ to 6.0GHz	10	15	---	dB
0.1dB Loss Compression Input Power ^{Note 2}	$P_{in(0.1dB)}$	$f=0.5$ to 2.0GHz ^{Note 1}	---	+32	---	dBm
		$f=2.0$ to 6.0GHz	---	+31	---	dBm
		$f=0.5$ to 6.0GHz ^{Note 1} $V_C(H)=5.0\text{V}$	---	+35	---	dBm
0.5dB Loss Compression Input Power ^{Note 3}	$P_{in(0.5dB)}$	$f=0.5$ to 2.0GHz ^{Note 1}	---	+34	---	dBm
		$f=2.0$ to 6.0GHz	---	+34	---	dBm
3rd Order Input Intercept Point	IIP_3	$f=2.5\text{GHz}$, 2-tone 1MHz Spacing	---	60	---	dBm
2nd Harmonics	$2f_0$	$f=2.5\text{GHz}$, $P_{in}=+20\text{dBm}$	---	-90	---	dBc
		$f=6.0\text{GHz}$, $P_{in}=+20\text{dBm}$	---	-90	---	dBc
3rd Harmonics	$3f_0$	$f=2.5\text{GHz}$, $P_{in}=+20\text{dBm}$	---	-90	---	dBc
		$f=6.0\text{GHz}$, $P_{in}=+20\text{dBm}$	---	-90	---	dBc

Note 1. DC block capacitance = 56pF at $f=0.5$ to 2.0GHz

Note 2. $P_{in(0.1dB)}$ is the measured input power level when the insertion loss increases 0.1dB more than that of the linear range.

Note 3. $P_{in(0.5dB)}$ is the measured input power level when the insertion loss increases 0.5dB more than that of the linear range.

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Electrical Characteristics 2

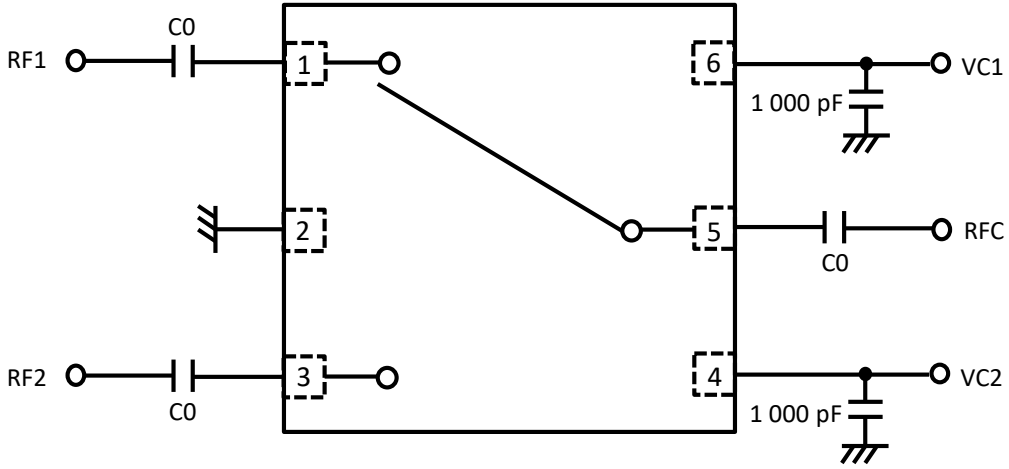
($T_A=+25\text{ }^\circ\text{C}$, $V_C(H)=3.0\text{V}$, $V_C(L)=0\text{V}$, $Z_0=50\ \Omega$, DC Block Capacitance=8pF, unless otherwise specified)

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Error Vector Magnitude	EVM	802.11a, 64QAM, 54Mbps, $P_{in} \leq +25\text{dBm}$	---	0.5	---	%
		802.11g, 64QAM, 54Mbps, $P_{in} \leq +25\text{dBm}$	---	0.5	---	%
		802.11ac, 256QAM, MCS9, 80MHz, $P_{in} \leq +25\text{dBm}$	---	0.5	---	%
Switch Control Current	I_{CONT}	RF none	---	2	10	μA
Switching Speed	T_{SW}	50% CTL to 90/10% RF	---	100	250	ns

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Evaluation Circuit

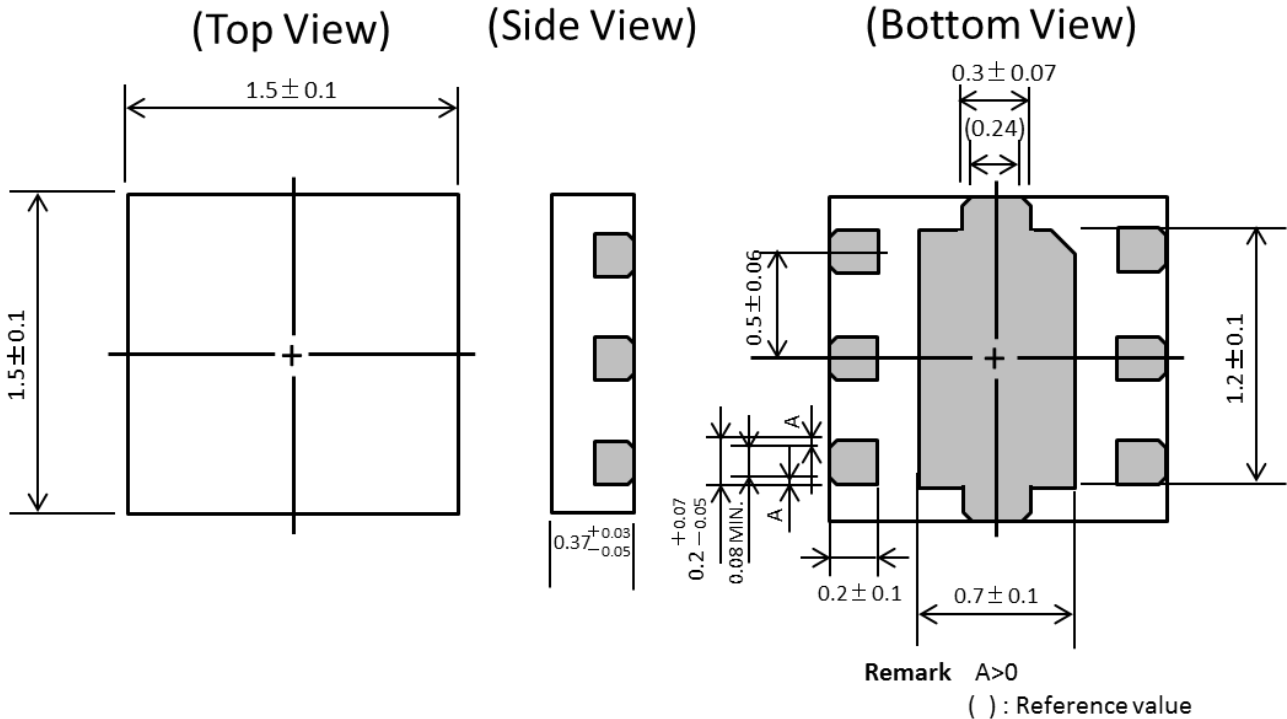


Note C0 : 0.5 to 2.0 GHz 56pF
: 2.0 to 6.0 GHz 8pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins. DC Blocking Capacitors are required at all RF Ports.

Package Dimensions

6-pin TSON (Unit : mm)



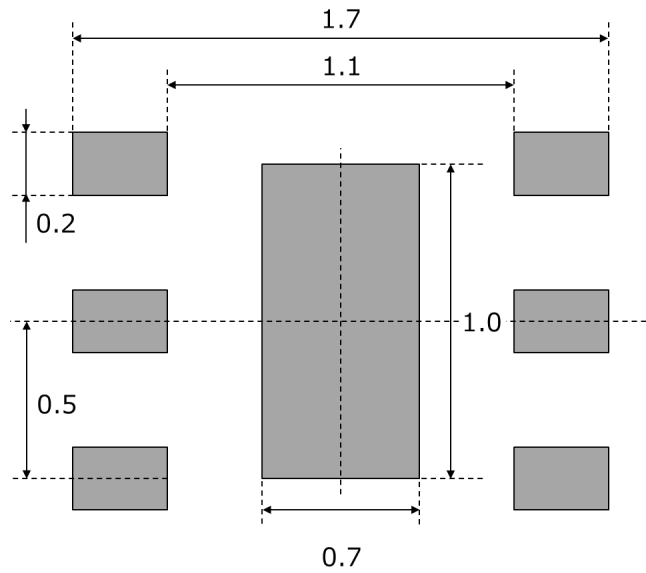
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PCB Layout Footprint

6-pin TSON (Unit: mm)



The PCB Layout Footprint in this document is for reference only.

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[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

[CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

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Version	Change to current version	Page(s)
CDS-0064-01 Nov. 2018	Initial datasheet	N/A