

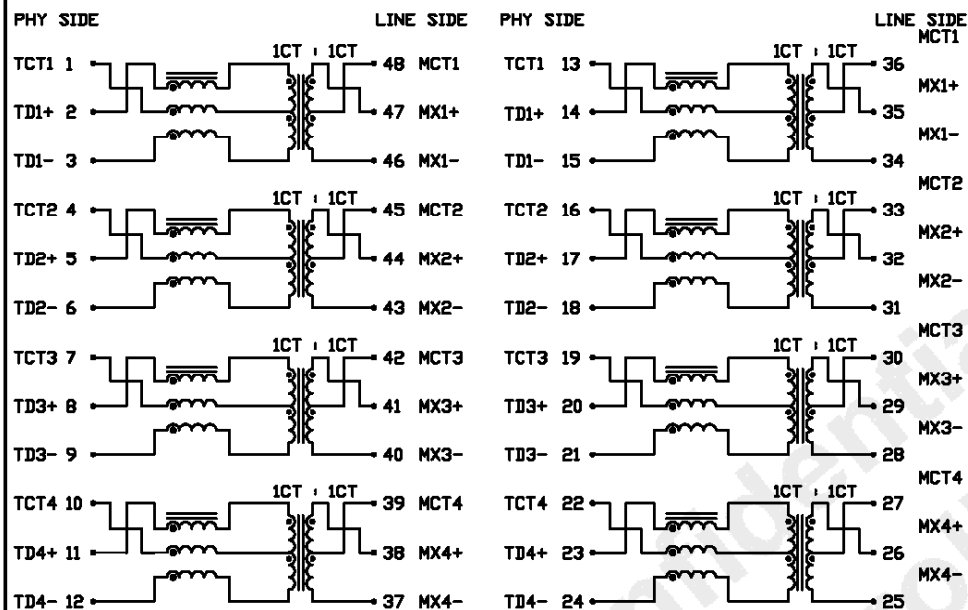
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RoHS



ELECTRICAL CHARACTERISTICS @25°C

SCHEMATIC



DC RESISTANCE

(2-3), (5-6), (8-9), (11-12)	1.00 OHM MAX
(1-3), (4-6), (7-9), (10-12)	0.75 OHM MAX
(47-46), (44-43), (41-40), (38-37)	0.50 OHM MAX
(48-46), (45-43), (42-40), (39-37)	0.30 OHM MAX
(14-15), (17-18), (20-21), (23-24)	1.00 OHM MAX
(13-15), (16-18), (19-21), (22-24)	0.75 OHM MAX
(35-34), (32-31), (29-28), (26-25)	0.50 OHM MAX
(36-34), (33-31), (30-28), (27-25)	0.30 OHM MAX

TURNS RATIO

PHY : LINE

1CT : 1CT ±2%

POLARITY

PER DOT CONVENTION

OCL

100kHz, 100mV
350uH MIN WITH 11mA DC BIAS
120uH MIN WITH 19mA DC BIAS

INTERWINDING CAPACITANCE, C_{ww} @1MHz, 20mV

30pF MAX

INSERTION LOSS

0.3MHz - 1MHz

1.1dB MAX

1MHz - 65MHz

0.8dB MAX

65MHz - 100MHz

1.0dB MAX

100MHz - 125MHz

1.2dB MAX

RETURN LOSS @100OHM

0.5MHz - 40MHz

18dB MIN

40MHz - 100MHz

12-20LOG(f/80MHz)dB MIN

CROSSTALK

0.3MHz - 100MHz

33-20LOG(f/100MHz)dB MIN

CM TO CM REJ

0.3MHz - 100MHz

30dB MIN

CM TO DM REJ

0.3MHz - 100MHz

35dB MIN

BALANCED DC LINE CURRENT

720mA MAX @57VDC CONTINUOUS

1.2A MAX @57VDC FOR 200ms

HIPOT

LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

DESIGNED FOR APPLICATIONS REQUIRING OPERATION FROM -40 TO 85C

REV. : C

PAGE : 2

ORIGINATED BY	DATE
Alice Pang	2020-06-30
DRAWN BY	DATE
ZC Guo	2020-06-30

TITLE
ELECTRICAL SPECIFICATION

PART NO. / DRAWING NO.

S558-5500-77

FILE NAME

S558550077C.DWG

STANDARD DIM. [] METRIC DIM. AS REF.

TOL. IN INCH

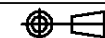
.X

.XX

.XXX

UNIT : INCH [mm]

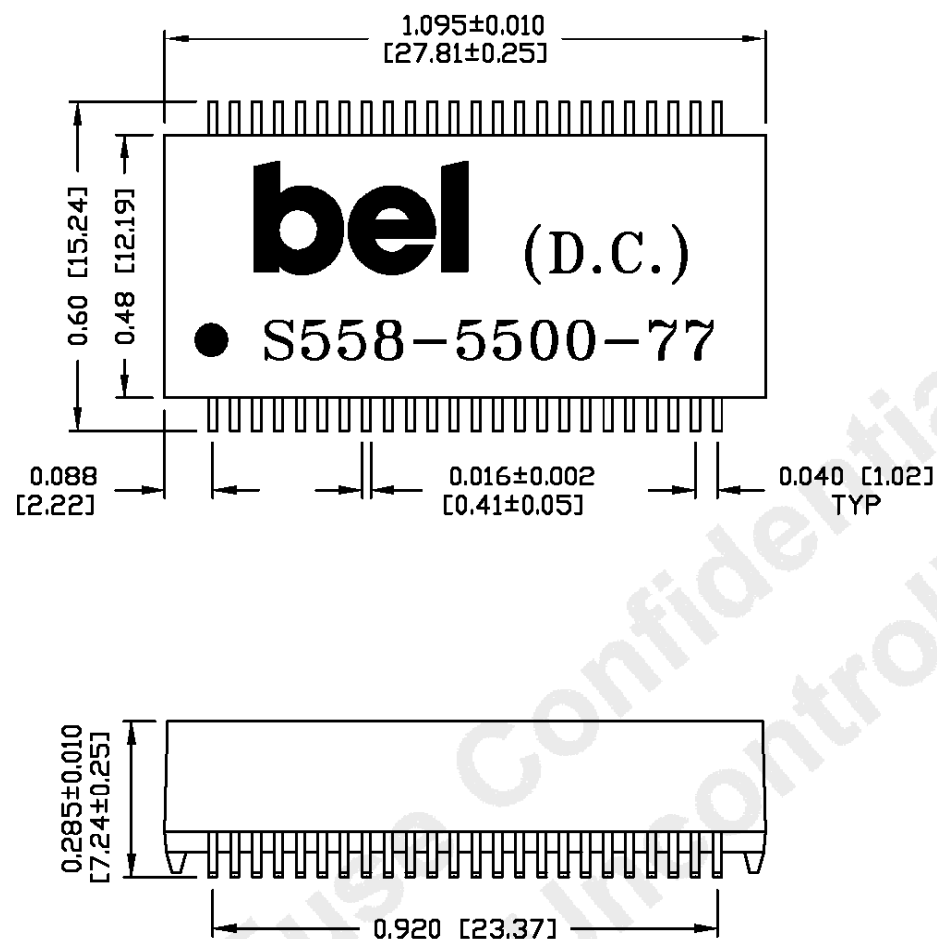
SCALE : N/A



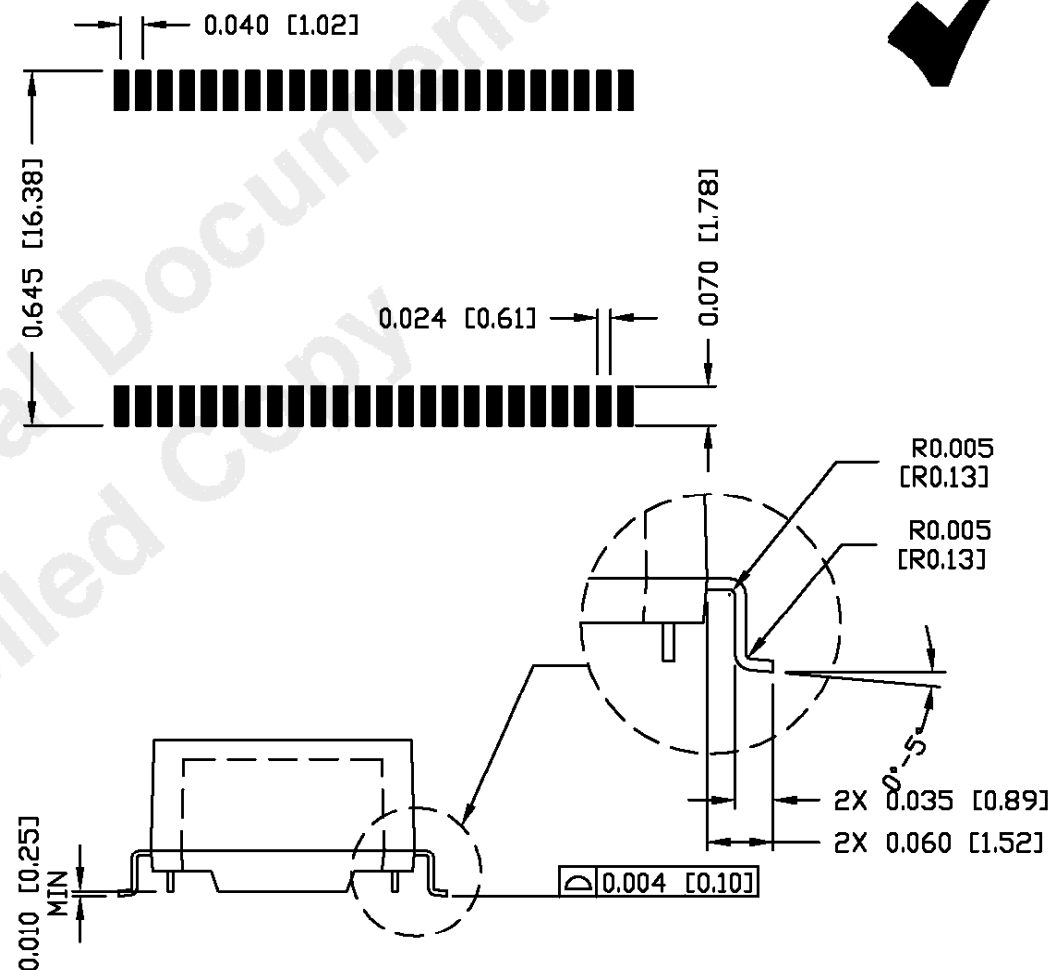
SIZE : A4



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**NOTES:**

1. STANDARD MARKING REFER TO DDC. HAND-WORK-04.
2. PACKAGE CODE: "QVS001".

SUGGESTED PCB PAD LAYOUT**RoHS**

ORIGINATED BY DATE		TITLE MECHANICAL OUTLINE	PART NO. / DRAWING NO.		STANDARD DIM.		[] METRIC DIM. AS REF.		 POWER PROTECT CONNECT
Lawrence Tsang 2019-02-26			S558-5500-77		TOL. IN INCH		UNIT : INCH [mm]		
DRAWN BY DATE			FILE NAME		.X		SCALE : N/A		
ZC Guo 2019-02-26			S558550077A.DWG		.XX ±0.01		⌀		
					.XXX ±0.005		SIZE : A4		