### **Tantalum Ultra Low ESR Capacitor**



LEAD-FREE COMPATIBLE

COMPONENT

SnPb termination option is not

RoHS compliant.

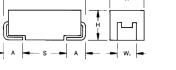


#### **FEATURES**

- Improved Reliability 0.5%/1khrs (Twice Better than Standard)
- DCL Reduced by 25% to 0.0075 CV
- Robust Against Higher Thermo-mechanical Stresses During **Assembly Process**
- Multi-anode Construction
- Super Low ESR
- 100% Surge Current Tested
- CV Range 4.7-1500µF / 2.5-50V
- "Mirror" Construction Used With D case Capacitors Reduces ESL to Half
- · Automotive, Medical, Aerospace, Military and Other Hi-End Applications

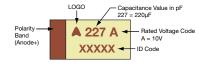
#### **APPLICATIONS**

· Automotive, Avionics and Industrial High Power DC/DC Convertors



### **MARKING**

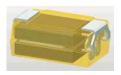
#### D, E, U CASE







#### **MULTIANODE TRM D LOW SELF INDUCTANCE CONSTRUCTION** "MIRROR" DESIGN



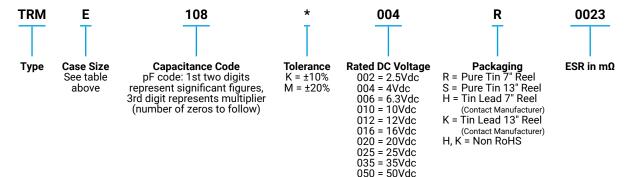
#### **CASE DIMENSIONS:**

### millimeters (inches)

EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W <sub>1</sub> ±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.
2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)
2917	7343-43	7.30 (0.287)	4.30 (0.169)	4.10 (0.162)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)
2924	7361-43	7.30 (0.287)	6.10 (0.240)	4.10 (0.162)	3.10 (0.122)	1.30 (0.051)	4.40 (0.173)
	2917 2917	Code         Metric           2917         7343-31           2917         7343-43	Code         Metric         (0.008)           2917         7343-31         7.30 (0.287)           2917         7343-43         7.30 (0.287)	Code         Metric         (0.008)         -0.10 (0.004)           2917         7343-31         7.30 (0.287)         4.30 (0.169)           2917         7343-43         7.30 (0.287)         4.30 (0.169)	Code         Metric         (0.008)         -0.10 (0.004)         -0.10 (0.004)           2917         7343-31         7.30 (0.287)         4.30 (0.169)         2.90 (0.114)           2917         7343-43         7.30 (0.287)         4.30 (0.169)         4.10 (0.162)	Code         Metric         (0.008)         -0.10 (0.004)         -0.10 (0.004)         (0.008)           2917         7343-31         7.30 (0.287)         4.30 (0.169)         2.90 (0.114)         2.40 (0.094)           2917         7343-43         7.30 (0.287)         4.30 (0.169)         4.10 (0.162)         2.40 (0.094)	Code         Metric         (0.008)         -0.10 (0.004)         -0.10 (0.004)         (0.008)         -0.20 (0.008)           2917         7343-31         7.30 (0.287)         4.30 (0.169)         2.90 (0.114)         2.40 (0.094)         1.30 (0.051)           2917         7343-43         7.30 (0.287)         4.30 (0.169)         4.10 (0.162)         2.40 (0.094)         1.30 (0.051)

W, dimension applies to the termination width for A dimensional area only.

#### **HOW TO ORDER**



#### **TECHNICAL SPECIFICATIONS**

Technical Data:	All technical data relate to an ambient temperature of +25°C											
Capacitance Range:		4.7 μF to 1500 μF										
Capacitance Tolerance:		±10%; ±20%										
Rated Voltage (V <sub>R</sub> )	≤ +85°C:	2.5	4	6.3	10	12	16	20	25	35	50	
Category Voltage (V <sub>c</sub> )	≤ +125°C:	1.7	2.7	4	7	8	10	13	17	23	33	
Surge Voltage (V <sub>s</sub> )	≤ +85°C:	3.3	5.2	8	13	16	20	26	32	46	65	
Surge Voltage (V <sub>s</sub> )	≤ +125°C:	2.2	3.4	5	8	10	13	16	20	28	40	
Temperature Range:		-55°C to -	+125°C									
Reliability:		0.5% per	1000 ho	ours at 8	5°C, V <sub>R</sub> w	ith 0.1Ω/	V series	impedan	ce,			
	60% confidence level											
	Meets requirements of AEC-Q200											





**CAPACITANCE AND RATED VOLTAGE RANGE** (LETTER DENOTES CASE SIZE)

Capac	itance		Rated Voltage DC (V <sub>R</sub> ) to 85°C											
μF	Code	2.5V (e)	4V (G)	6.3V (J)	10V (A)	12V (B)	16V (C)	20V (D)	25V (E)	35V (V)	50V (T)			
4.7	475										D(200)			
6.8	685													
10	106									D(120)				
15	156													
22	226									D(70) E(60,100)				
33	336								D(65)	E(50,65)				
47	476						D(100)	D(55)	E(65)					
68	686													
100	107							E(35,45)						
150	157				D(45)		E(30,40)							
220	227				D(35)	E(35)	U(30,40)							
330	337		D(35)	D(35)	E(35)									
470	477		D(35)	E(30)	U(23,30)									
680	687		E(23)	U(18,23)										
1000	108	D(25)	E(23) U(18,23)											
1500	158	E(18) U(18,23)												

Released ratings, (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. KYOCERA AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.





#### **RATINGS & PART NUMBER REFERENCE**

Part Number	Case	Capacitance	Rated Voltage	Rated Temperature	Category Voltage	Category Temperature	DCL Max.	DF Max.	ESR Max.	100kH	z RMS Cur	rent (A)	MSL
r ai t Number	Size	(μF)	(V)	(°C)	(V)	(°C)	(μA)	(%)	@ 100kHz (mΩ)	25°C	85°C	125°C	IVIGE
					2.5 \	Volt @ 85°C							
TRMD108*002#0025	D	1000	2.5	85	1.7	125	18.8	8	25	3.194	2.874	1.277	3
TRME158*002#0018	Е	1500	2.5	85	1.7	125	28.1	6	18	3.873	3.486	1.549	3
TRMU158*002R0018	U	1500	2.5	85	1.7	125	22.5	6	18	4.048	3.643	1.619	3
TRMU158*002R0023	U	1500	2.5	85	1.7	125	22.5	6	23	3.581	3.223	1.433	3
					4 V	olt @ 85°C							
TRMD337*004#0035	D	330	4	85	2.7	125	9.9	8	35	2.699	2.429	1.080	3
TRMD477*004#0035	D	470	4	85	2.7	125	14.1	8	35	2.699	2.429	1.080	3
TRME687*004#0023	Е	680	4	85	2.7	125	20.4	6	23	3.426	3.084	1.370	3
TRME108*004#0023	Е	1000	4	85	2.7	125	30	6	23	3.426	3.084	1.370	3
TRMU108*004R0018	U	1000	4	85	2.7	125	30	6	18	4.048	3.643	1.619	3
TRMU108*004R0023	U	1000	4	85	2.7	125	30	6	23	3.581	3.223	1.433	3
					6.3	Volt @ 85°C							
TRMD337*006#0035	D	330	6.3	85	4	125	14.9	8	35	2.699	2.429	1.080	3
TRME477*006#0030	E	470	6.3	85	4	125	21.2	6	30	3.000	2.700	1.200	3
TRMU687*006R0018	U	680	6.3	85	4	125	30.6	6	18	4.048	3.643	1.619	3
TRMU687*006R0023	U	680	6.3	85	4	125	30.6	6	23	3.581	3.223	1.433	3
					10 \	/olt @ 85°C							
TRMD157*010#0045	D	150	10	85	7	125	11.3	8	45	2.380	2.142	0.952	3
TRMD227*010#0035	D	220	10	85	7	125	16.5	8	35	2.699	2.429	1.080	3
TRME337*010#0035	Е	330	10	85	7	125	24.8	6	35	2.777	2.500	1.111	3
TRMU477*010R0023	U	470	10	85	7	125	35.3	8	23	3.581	3.223	1.433	3
TRMU477*010R0030	U	470	10	85	7	125	35.3	8	30	3.136	2.822	1.254	3
					12 \	/olt @ 85°C						,	
TRME227*012#0035	Е	220	12	85	8.4	125	19.8	6	35	2.777	2.500	1.111	3
					16 \	/olt @ 85°C							
TRMD476*016#0100	l D	47	16	85	10	125	5.6	8	100	1.597	1.437	0.639	3
TRME157*016#0030	E	150	16	85	10	125	18	6	30	3.000	2.700	1.200	3
TRME157*016#0040	Е	150	16	85	10	125	18	6	40	2.598	2,338	1.039	3
TRMU227*016R0030	U	220	16	85	10	125	26.4	8	30	3.136	2.822	1.254	3
TRMU227*016R0040	U	220	16	85	10	125	26.4	8	40	2.716	2.444	1.086	3
					20 \	/olt @ 85°C							
TRMD476*020#0055	D	47	20	85	13	125	7.1	8	55	2.153	1.938	0.861	3
TRME107*020#0035	E	100	20	85	13	125	15	6	35	2.777	2.500	1.111	3
TRME107*020#0045	Е	100	20	85	13	125	15	6	45	2,449	2.205	0.980	3
						/olt @ 85°C							
TRMD336*025#0065	D	33	25	85	17	125	6.2	8	65	1.981	1.783	0.792	3
TRME476*025#0065	E	47	25	85	17	125	8.8	6	65	2.038	1.834	0.815	3
						/olt @ 85°C							
TRMD106*035#0120	D	10	35	85	23	125	2.6	8	120	1.458	1.312	0.583	3
TRMD226*035#0070	D	22	35	85	23	125	5.8	8	70	1.909	1.718	0.763	3
TRME226*035#0060	E	22	35	85	23	125	5.8	6	60	2.121	1.909	0.849	3
TRME226*035#0100	E	22	35	85	23	125	5.8	6	100	1.643	1.479	0.657	3
TRME336*035#0050	E	33	35	85	23	125	8.7	6	50	2.324	2.091	0.930	3
TRME336*035#0065	E	33	35	85	23	125	8.7	6	65	2.038	1.834	0.815	3
						/olt @ 85°C	0.,		- 00	2.000	1.007	0.0.0	- ŭ
TRMD475*050#0200	D	4.7	50	85	33	125	1.8	8	200	1.129	1.016	0.452	3
						.20				/		002	

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5V RMS with a maximum DC bias of 2.2 volts.

DCL is measured at rated voltage after 5 minutes.

The EIA & CECC standards for low ESR Solid Tantalum Capacitors allow an ESR movement to 1.25 times catalogue limit post mounting.

For typical weight and composition see page 259.

NOTE: KYOCERA AVX reserves the right to supply higher voltage ratings or tighter tolerance part in the same case size, to the same reliability standards.





#### **QUALIFICATION TABLE**

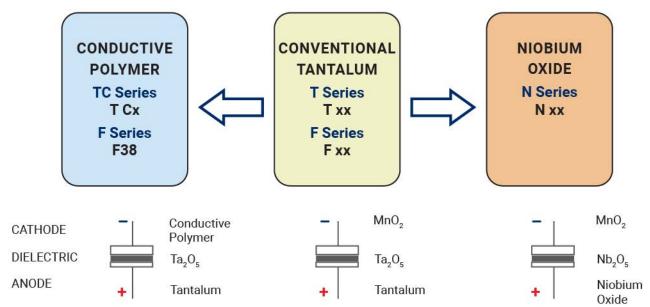
TEST -		0									
	Condition Characteristics										
Endurance				Visual examination	no visib	le damage	9				
	Apply rated voltage	` '		DCL	initial limit						
Endurance	voltage (Uc) at 125		•	ΔC/C	within ±10% of initial value						
	impedance of ≤0.1 for 1-2 hours befor		om temperature	DF	initial lin	nit				-	
	ior 1-2 nours belor	re measuring.		ESR	1.25 x ir	nitial limit					
				Visual examination	no visib	le damage	9			-	
	Store at 125°C, no	voltage applied, fo	or 2000 hours.	DCL	1.25 x initial limit						
Storage Life	Stabilize at room t	•		ΔC/C	initial limit within ±10% of initial value initial limit  1.25 x initial limit  1.25 x initial limit  1.25 x initial limit within ±10% of initial value initial limit  1.25 x initial limit  1.25 x initial limit  1.25 x initial limit  1.25 x initial limit within ±10% of initial value 1.5 x initial limit within ±10% of initial value 1.2 x initial limit 1.25 x initial limit  1.25 x initial limit within ±10% of initial value 1.2 x initial limit within ±10% of initial value 1.2 x initial limit  1.25 x initial limit 1.25 x initial limit  1.25 x initial limit 1.25 x initial limit 1.25 x initial limit 1.25 x initial limit 1.25 x initial limit  1.25 x initial limit 1.25 x initial limit  1.25 x initial limit 1.25 x initial limit 1.25 x initial limit 1.25 x initial limit 1.25 x initial limit within ±5% of initial value initial limit 1.25 x initial limit 1.25 x initial limit 1.25 x initial limit within ±5% of initial value initial limit						
	measuring.	·		DF	initial lin	nit					
				ESR	1						
				Visual examination	n no visible damage  1.5 x initial limit  within ±10% of initial value  1.2 x initial limit  1.25 x initial limit  n ovisible damage  2 x initial limit  within ±10% of initial value  1.2 x initial limit  1.25 x initial limit  +20°C -55°C +20°C +85°C +125°C +20°C						
	Store at 65°C and	95% relative humic	lity for 500 hours	DCL	_						
Humidity	with no applied vo			ΔC/C			tial value			-	
	and humidity for 1	•	•	DF							
	,		J	ESR							
				1.20 1.1111							
	Apply rated voltage	e (Ur) at 85°C 85%	relative humidity	DCL 2 x initial limit						-	
Biased Humidity	for 1000 hours. Sta	. ,	•	ΔC/C							
Diagon Hammarty	humidity for 1-2 ho		•	DF.	1.2 x initial limit						
	,		3	ESR							
	Step	Temperature°C	Duration(min)	10			+20°€	+85°€	+125°C	+20°C	
	1	+20	15	- DCL	_				+		
Temperature	2	-55	15		<del>-</del>				-		
Stability	3	+20	15	ΔC/C	n/a	+0/-10%	±5%	+10/-0%	+12/-0%	±5%	
ļ , , , , , , , , , , , , , , , , , , ,	5	+85 +125	15 15	- DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*	
	6	+20	15	ESR	1.25xIL*	2.5xIL*	1.25xIL*	1.25xIL*	1.25xIL*	1.25xIL*	
				Visual examination	no visible damage						
_	Apply 1.3x categor			DCL							
Surge	cycles of duration	•	•	ΔC/C	within ±	5% of initi	al value				
Voltage	discharge) through	n a charge / discha	rge resistance of	DF	initial lin	initial limit					
	100001			ESR	1.25 x initial limit						
				Visual examination	no visib	le damage	9				
				DCL	initial lin	nit					
Mechanical	MIL-STD-202, Metl	hod 213, Condition	F	ΔC/C	within ±	5% of initi	al value				
Shock	, ,	.,		DF							
				ESR	1.25 x ir						
				Visual examination							
				DCL	_						
Vibration	MIL-STD-202, Metl	hod 204. Condition	D	ΔC/C			al value	10 x IL* 12.5 x IL* IL* +10/-0% +12/-0% ±5% 1.5 x IL* 2 x IL* IL*			
	,o			DF.	initial lin			10 x IL* 12.5 x IL* IL* +10/-0% +12/-0% ±5% 1.5 x IL* 2 x IL* IL*			
				ESR		nitial limit					

<sup>\*</sup>Initial Limit





#### SOLID ELECTROLYTIC CAPACITOR ROADMAP



### **FIVE CAPACITOR CONSTRUCTION STYLES**



#### **SERIES LINE UP: CONVENTIONAL SMD MnO**,

