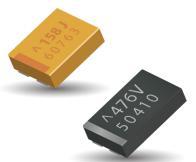
Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors





FEATURES

- · Highest Energy per volume
- Fast DCL drop with Voltage applied after reflow
- Benign failure mode under recommended use conditions
- Low ESR
- Undertab terminations layout:
 - High Volumetric Efficiency
 - Low profile case sizes
 - High capacitance in smaller dimensions
 - Close positioning of several parts for efficient high density PCB layout
- 3x reflow 260°C compatible



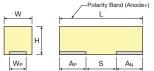


APPLICATIONS

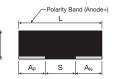
 Power backup for SSDs (MLC, SLC, EFD, PCle), battery-powered portable equipment, industrial alarms, smart power meters, and mobile devices.

J-LEAD A S A A W W

UNDERTAB









CASE DIMENSIONS UNDERTAB: millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H max.	W _P ±0.10 (0.004)	W _N ±0.10 (0.004)	A _P ±0.10 (0.004)	A _N ±0.10 (0.004)	S Min.
L	1210	3528-10	3.50 (0.138)	2.80 (0.110)	1.00 (0.039)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Т	1210	3528-12	3.50 (0.138)	2.80 (0.110)	1.20 (0.047)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Х	2917	7343-15	7.30 (0.287)	4.30 (0.169)	1.50 (0.059)	3.25 (0.128)	3.25 (0.128)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)
4	2924	7361-20	7.30 (0.287)	6.10 (0.240)	2.00 (0.079)	4.75 (0.187)	4.75 (0.187)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)

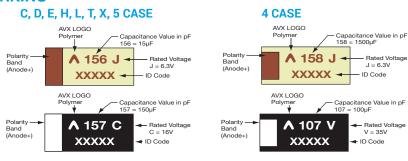
CASE DIMENSIONS J-LEAD: millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W ₁ ±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.
С	2312	6032-28	6.00 (0.236)	3.20 (0.126)	2.60 (0.102)	2.20 (0.087)	1.30 (0.051)	2.90 (0.114)
D	2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)
E	2917	7343-43	7.30 (0.287)	4.30 (0.169)	4.10 (0.162)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)
Н	1210	3528-15	3.50 (0.138)	2.80 (0.110)	1.50 (0.059) max.	2.20 (0.087)	0.80 (0.031)	1.40 (0.055)
5	2917	7343-40	7.30 (0.287)	4.30 (0.169)	3.80 (0.150)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)
			W ₁ dimension a	applies to the termir	nation width for A dir	mensional area onl	y.	

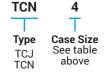
MAXIMUM ENERGY PER CASE SIZE

Case Size	H Max (mm)	Max Energy (mJ)
С	2.8	5.8
D	3.1	21.8
E	4.3	11.9
Н	1.5	3.3
L	1.0	1.8
T	1.2	6.5
Х	1.5	18.2
4	2.0	43.0
5	4.0	46.6

MARKING



HOW TO ORDER



Capacitance Code
pF code: 1st two digits
represent significant figures,
3rd digit represents multiplier

158

M Tolerance M = ±20% Rated DC Voltage 006 = 6.3Vdc 020 = 20Vdc 010 = 10Vdc 025 = 25Vdc 016 = 16Vdc 035 = 35Vdc R T Packaging R = Pure Tin 7" Reel S = Pure Tin 13" Reel

(J-Lead)

0055 ESR in mΩ E T Additional Character E = Black resin

Part Numbers already changed to an "E" suffix will continue to be supplied with only black resin. Those Part Numbers currently produced with gold resin will eventually change to black before July, 2020.



J-CAP[™] Series



Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

TECHNICAL SPECIFICATIONS

Technical Data:		All techr	nical data	relate to	an ambi	ent temp	erature c	of +25°C	
Capacitance Range:		4.7 μF to	ο 1500 μF						
Capacitance Tolerance:		±20%							
Leakage Current DCL:		0.1CV							
Rated Voltage DC (V _R)	≤ +85°C:	6.3	10	16	20	25	35	50	
Surge Voltage (V _s)	≤ +85°C:	8	13	21	26	33	46	65	
Temperature Range:		-55°C up	to +125°	С					
Reliability:			000 hours fidence le		V _R with 0	.1Ω/V ser	es imped	lance	

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.

CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance						Rated \	oltage D	C to 85	°C, [mJ]					
μF	Code	6.3V	(J)	10V	(A)	16V	(C)	20V	(D)	25V	(E)	35V	(V)	50V	(T)
4.7	475											L(300E) T(200E)	[1.8]		
6.8	685													C(200E)	[5.4]
10	106											T(200E)	[3.9]	D(120E)	[8.0]
15	156											C(200E)	[5.8]	E(70E)	[11.9]
22	226									T(200E)	[4.3]	D(100E)	[8.5]		
33	336					H(150E) T(200E)	[3.3]			T(250E)	[6.5]	D(70E)	[12.8]		
47	476			C(100E) H(100E)	[1.7]	T(200) T(150E)	[4.7]			X(100E)	[9.2]	X(150E)	[18.2]		
68	686	H(100E)	[0.8]	D(45E)	[2.5]	D(50E)	[6.7]	D(55E)	[8.4]	D(70E)	[13.3]				
100	107			D(45E)	[3.6]	D(50E)	[9.9]	D(55E)	[12.4]	D(70E) 4(100E)	[19.6]	4(100E)	[38.8]		
150	157	T(200E)	[1.7]	D(45E)	[5.4]	X(100E)	[14.9]			4(70E)	[29.3]				
220	227	H(170E)	[2.6]	D(40E)	[7.9]	D(50E) 4(70E)	[21.8]	4(100E)	[27.2]	4(100E)	[43.0]				
330	337	D(40E)	[3.8]	5(100E)	[11.9]	4(70E) 5(100E)	[32.7]								
470	477	X(50E)	[5.4]			5(100E)	[46.6]								
1000	108	4(55E)	[11.6]												
1500	158	4(55E)	[17.4]												

Note for designers - for the highlighted ratings, higher voltage options are now available in the same case size and are recommended for new designs.

Released ratings, (ESR ratings in mOhms in parentheses) [Energy in mJ]

Note: Voltage ratings are minimum values. AVX reserves the right to supply

higher voltage ratings in the same case size, to the same reliability standards.



J-CAP[™] Series



Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

RATINGS & PART NUMBER REFERENCE

ANDV	0	0	Rated	Maximum	DCL	DF	ESR	1000kHz	Dun dun d			ENERGY	
AVX Part No.	Case Size	Capacitance (μF)	Voltage (V)	Operating Temperature (°C)	Max. (μA)	Max. (%)	Max. @ 100kHz (mΩ)	RMS Current (mA) 45°C	Product Category	MSL	Energy (mJ)	Energy/volume (mJ/cm³)	Energy/area (mJ/cm²)
						6.3 Volt @	85°C						
TCJH686M006#0100E	Н	68	6.3	105	40.8	6	100	1000	3	3	0.8	54	8.0
TCNT157M006#0200E	Т	150	6.3	105	90	10	200	700	3	4	1.7	147	17.7
TCJH227M006#0170E	Н	220	6.3	105	132	10	170	800	3	3	2.6	173	26
TCJD337M006#0040E	D	330	6.3	105	198	6	40	2400	2	3	3.8	42	12.2
TCNX477M006#0050E	Х	470	6.3	85	282	10	50	1900	5	5	5.4	115	17.3
TCN4108M006#0055E	4	1000	6.3	85	600	20	55	1860	5	4	11.6	130	26
TCN4158M006#0055E	4	1500	6.3	85	900	20	55	1860	5	4	17.4	195	39
						10 Volt @	85°C						
TCJH476M010#0100E	Н	47	10	105	47	6	100	1000	3	3	1.7	115	17.3
TCJC476M010#0100E	С	47	10	125	47	6	100	1300	1	3	1.7	34	8.8
TCJD686M010#0045E	D	68	10	105	68	6	45	2200	3	3	2.5	27	7.8
TCJD107M010#0045E	D	100	10	105	100	6	45	2200	3	3	3.6	40	11.5
TCJD157M010#0045E	D	150	10	105	150	6	45	2200	3	3	5.4	59	17.2
TCJD227M010#0040E	D	220	10	105	220	6	40	2400	3	3	7.9	87	25.2
TCJ5337M010#0100E	5	330	10	105	330	10	100	1300	2	3	11.9	100	37.8
						16 Volt @	85°C						
TCJH336M016#0150E	Н	33	16	105	52.8	6	150	800	3	3	3.3	223	33.4
TCNT336M016#0200E	Т	33	16	105	52.8	6	200	700	3	4	3.3	277	33.4
TCNT476M016#0150E	Т	47	16	105	75.2	6	150	800	3	4	4.7	395	47.6
TCNT476M016#0200	T	47	16	105	75.2	6	200	700	3	4	4.7	395	47.6
TCJD686M016#0050E	D	68	16	105	108.8	6	50	2100	2	3	6.7	74	21.5
TCJD107M016#0050E	D	100	16	105	160	6	50	2100	2	3	9.9	109	31.6
TCNX157M016#0100E	X	150	16	105	240	6	100	1300	3	4	14.9	316	47.4
TCJD227M016#0050E	D	220	16	105	352	10	50	2100	2	3	21.8	240	69.5
TCN4227M016#0070E	4	220	16	105	352	20	70	1650	2	4	21.8	245	49
TCN4337M016#0070E	4	330	16	105	528	20	70	1650	3	4	32.7	367	73.5
TCJ5337M016#0100E	5	330	16	105	528	10	100	1300	2	3	32.7	274	104.2
TCJ5477M016#0100E	5	470	16	105	752	10	100	1300	3	3	46.6	391	148.5
	,					20 Volt @							
TCJD686M020#0055E	D	68	20	105	136	6	55	2000	3	3	8.4	92	26.7
TCJD107M020#0055E	D	100	20	105	200	6	55	2000	3	3	12.4	136	39.3
TCN4227M020#0100E	4	220	20	85	440	10	100	1380	5	4	27.2	305	61.1
						25 Volt @							
TCNT226M025#0200E	T	22	25	105	55	6	200	700	3	4	4.3	364	43.9
TCNT336M025#0250E	T	33	25	105	82.5	10	250	600	3	4	6.5	547	65.8
TCNX476M025#0100E	X	47	25	105	117.5	6	100	1300	2	5	9.2	195	29.3
TCJD686M025#0070E	D	68	25	105	170	6	70	1800	2	3	13.3	146	42.3
TCJD107M025#0070E	D	100	25	105	250	6	70	1800	2	3	19.6	215	62.3
TCN4107M025#0100E	4	100	25	105	250	6	100	1380	2	4	19.6	219	43.9
TCN4157M025#0070E	4	150	25	105	375	6	70	1650	2	4	29.3	329	65.9
TCN4227M025#0100E	4	220	25	105	550	10 35 Volt @	100 85°C	1380	3	4	43.0	483	96.7
TCNL475M035#0300E	L	4.7	35	105	16.5	6	300	600	2	5	1.8	186	18.6
TCNT475M035#0200E	Т	4.7	35	105	16.5	10	200	700	3	4	1.8	154	18.6
TCNT106M035#0200E	Т	10	35	105	35	10	200	700	3	4	3.9	328	39.5
TCJC156M035#0200E	С	15	35	105	52.5	6	200	900	3	3	5.8	116	30.3
TCJD226M035#0100E	D	22	35	105	77	6	100	1500	2	3	8.5	94	27.1
TCJD336M035#0070E	D	33	35	105	115.5	6	70	1800	2	3	12.8	141	40.7
TCNX476M035#0150E	Х	47	35	105	165	10	150	1100	3	4	18.2	387	58.0
TCN4107M035#0100E	4	100	35	105	350	10	100	1380	2	3	38.8	435	87.1
						50 Volt @	85°C						
TCJC685M050#0200E	С	6.8	50	105	34	8	200	900	3	3	5.4	108	28.2
TCJD106M050#0120E	D	10	50	105	50	10	120	1400	3	3	8.0	87	25.3
TCJE156M050#0070E	E	15	50	105	75	6	70	1900	3	3	11.9	93	38

Energy is calculated by this formula (consider derating factor):

Energy = $\frac{1}{2}$ C x ((Vr x X)² - Vx²)

where C = Capacitance

Vr = Rated Voltage

X = Recommended derating factor

Vx= 3V (invariable)

Moisture Sensitivity Level (MSL) is defined according to J-STD-020. All technical data relates to an ambient temperature of +25°C. Capacitance is

measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

ESR allowed to move up to 1.25 times catalog limit post mounting.

For typical weight and composition see page 276.

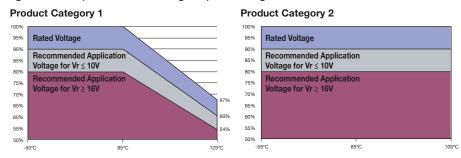
NOTE: AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.



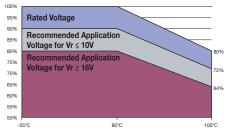
Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

RECOMMENDED DERATING FACTOR

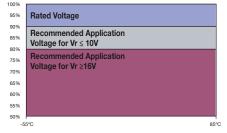
Voltage and temperature derating as percentage of Vr



Product Category 3



Product Category 5





Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

PRODUCT CATEGORY 1 (TEMPERATURE RANGE -55°C TO +125°C)

TEST		Condition	1		С	haracteris	tics					
		Apply rated voltage (Ur) at 85°C and /or 2/3 rated voltage (Ur) at 125°C for 2000 hours through a circuit impedance of s0.1Ω/V. Stabilize at room temperature for 1-2 hours before measuring. Store at 125°C, no voltage applied, for 2000 hours Stabilize at room temperature for 1-2 hours before measuring.		Visual examination	no visibl	e damage						
	Apply rate	ed voltage (Ur) at 85°C	and /or 2/3 rated	DCL	1.25 x in	itial limit						
Endurance Storage Life Humidity	voltage (l	Jr) at 125°C for 2000 h	ours through a	ΔC/C	within ±	within ±20% of initial value						
	temperat	ure for 1-2 hours befor	e measuring.	DF	1.5 x init	1.5 x initial limit						
				ESR	2 x initia	2 x initial limit						
Endurance Storage Life Humidity Temperature Stability Surge Voltage				Visual examination	no visibl	e damage						
	Store at 1	25°C, no voltage appli	ed, for 2000 hours.	DCL	2 x initia	l limit						
Storage Life				ΔC/C	within ±	within ±20% of initial value						
	measurin	g.		DF	1.5 x init	1.5 x initial limit						
Humidity				ESR	2 x initia	2 x initial limit						
				Visual examination	no visib	no visible damage						
	1	55°C and 95% relative I	,	DCL	3 x initia	al limit						
Humidity		th no applied voltage. ure and humidity for 1-		ΔC/C	within +	·30/-20% of	initial va	alue				
Temperature	measurin	,	-2 nours before	DF	1.5 x ini	1.5 x initial limit						
	meadam	·9·		ESR	2 x initia	2 x initial limit						
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+85°C	+125°C	+20°C		
	2	+20 -55	15 15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
	3 4	4 +85 15		ΔC/C	n/a	+0/-20%	±5%	+20/-0%	+30/-0%	±5%		
	5 6	+125 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*		
	1 0	T20	15	Visual examination	no visible damage							
		c 2.3x rated voltage (Ur)		DCL	initial lin	initial limit						
		duration 6 min (30 sec c) through a charge / disc			within +10/-20% of initial value for Vr ≤ 10V							
voitage	10000) through a charge / disc	charge resistance of	ΔC/C	within +:	within +20/-30% of initial value for Vr ≥ 16V						
				DF	1.25 x in	1.25 x initial limit						
				Visual examination	no visib	le damage						
				DCL	initial lii	nit						
	MIL-STD-	202, Method 213, Cond	dition C	ΔC/C	within ±	:5% of initia	ıl value					
Shock				DF	initial lii	mit						
				ESR	initial lii	nit						
				Visual examination	no visib	le damage						
				DCL	initial lii							
Vibration	MIL-STD-	202, Method 204, Cond	dition D	ΔC/C	within ±	5% of initia	ıl value					
		, , , , , , , , , , , , , , , , , , , ,		DF	initial lii	mit						
				ESR	initial li							

^{*}Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.



Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

PRODUCT CATEGORY 2, 3 (TEMPERATURE RANGE -55°C TO +105°C)

TEST		Condition			Cha	aracterist	ics					
				Visual examination	no visibl	e damage						
TEST Endurance Storage Life Humidity Temperature Stability Surge Voltage Mechanical Shock Vibration	through a	ed voltage (Ur) at 85°C circuit impedance of	; for 2000 hours ≤0.1Ω/V (all	DCL	1.25 x in	1.25 x initial limit						
	CATEGOR	RIES). And / or apply ra BV 2) or 0 8x rated vol	ated voltage (Ur)	ΔC/C		within +10/-20% of initial value for Vr ≤ 16V within ±20% of initial value for Vr ≥ 20V						
	impedano	°C for 2000 hours through of ≤0.1Ω/V. Always	stabilize at room	DF	1.5 x init	ial limit						
	temperati	ure for 1-2 hours befor	e measuring.	ESR	2 x initia	l limit						
Endurance Storage Life Humidity Temperature Stability Surge Voltage				Visual examination	no visibl	e damage						
				DCL	1.25 x in	1.25 x initial limit						
	hours. Sta	05°C, no voltage appli abilize at room temper fore measuring.		ΔC/C		10/-20% of 20% of initia		ue for Vr ≤ 1 or Vr ≥ 20V	6V			
	liours bei	ore measuring.		DF	1.5 x init	1.5 x initial limit						
				ESR	2 x initia	l limit						
				Visual examination	no visib	le damage						
	1	55°C and 95% relative I	,	DCL	3 x initia	al limit						
Temperature		th no applied voltage. ure and humidity for 1:		ΔC/C	within +	within +30/-20% of initial value						
	measurin		-2 nours before	DF	1.5 x ini	1.5 x initial limit						
	meadam	9.		ESR	2 x initia	2 x initial limit						
	Step Temperature °C Duration(min) 1 +20 15 2 -55 15			+20°C	-55°C	+20°C	+85°C	+105°C	+20°C			
Temperature	2	-55	15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
		3 +20 15 4 +85 15		ΔC/C	n/a	+0/-20%	±5%	+20/-0%	+30/-0%	±5%		
-	5 6	+105 +20	15 15	DF	IL*	1.5 x IL*	IL*	10 x IL* 12.5 x IL* +20/-0% +30/-0% ±	IL*			
				Visual examination		e damage		1.0				
		k rated voltage (Ur) at 10 KY 2, or apply 1.3x 0.8x ra		DCL	initial lim							
Stability	105°C for min (30 se	CATEGORY 3 for 1000 of charge, 5 min 30 sec	cycles of duration 6 discharge) through	ΔC/C	within +	within +10/-20% of initial value for Vr ≤ 16V within +20/-30% of initial value for Vr ≥ 20V						
	a charge /	discharge resistance of	f 1000Ω	DF	1.25 x in	itial limit						
				Visual examination	no visib	le damage						
				DCL	initial lir	nit						
	MIL-STD-	202, Method 213, Con	dition C	ΔC/C	within ±	5% of initia	al value					
Stability Surge Voltage Mechanical Shock				DF	initial lir	nit						
				ESR	initial lir	nit						
				Visual examination	no visib	le damage						
				DCL	initial lir	nit						
	MIL-STD-	202, Method 204, Con	dition D	ΔC/C	within ±	5% of initia	al value					
				DF	initial lir	nit						
				ESR	initial lir	nit						

^{*}Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.



Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

PRODUCT CATEGORY 5 (TEMPERATURE RANGE -55°C TO +85°C)

TEST		Condition			Chara	acteristics					
				Visual examination	no visible o	lamage					
TEST Endurance Storage Life Humidity Temperature Stability Surge Voltage Mechanical Shock	Apply reted vel	to mo (Ur) at 0E00 :	far 2000 haura	DCL	1.25 x initia	1.25 x initial limit					
	through a circu at room temper	tage (Ur) at 85°C i it impedance of ≤ rature for 1-2 hour	0.1Ω/V. Stabilize s before	ΔC/C		within +10/-20% of initial value for Vr ≤ 16V within ±20% of initial value for Vr ≥ 20V					
	measuring.			DF	1.5 x initial	limit					
				ESR	2 x initial lir	nit					
				Visual examination	no visible o	lamage					
				DCL	1.25 x initia	al limit					
Storage Life		no voltage applied m temperature fo		ΔC/C		/-20% of initia % of initial val					
Humidity	before measuri	ng.		DF	1.5 x initial	limit					
				ESR	2 x initial lir	nit					
				Visual examination	no visible	damage					
		nd 95% relative h		DCL	5 x initial l	imit					
Humidity		applied voltage. S		ΔC/C	within +40	/-20% of init	ial value				
Temperature	measuring.	d humidity for 1-2	nours before	DF	1.5 x initia	1.5 x initial limit					
	incasaring.			ESR	2 x initial I	imit					
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+85°C	+20°0		
	1 2	+20 -55	15 15	DCL	IL*	n/a	IL*	10 x IL*	IL*		
	3 +20 15		ΔC/C	n/a	+0/-20%	±5%	+20/-0%	±5%			
	5	+85 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	IL*		
				Visual examination	no visible d	lamage					
	Apply 1.3x rated	voltage (Ur) at 85°	C for 1000 cycles	DCL	initial limit						
		n (30 sec charge, 5 igh a charge / discl		40/0	within +10/	/-20% of initia	l value for Vi	r ≤ 16V			
voitage	of 1000Ω	igir a criarge / disci	large resistance	ΔC/C	within +20/	/-30% of initia	l value for Vi	r ≥ 20V			
				DF	1.25 x initia	ıl limit					
				Visual examination	no visible	damage					
Maahaniaal				DCL	initial limit	i					
	MIL-STD-202, N	Method 213, Condi	tion C	ΔC/C	within ±5%	of initial va	lue				
Snock				DF	initial limit	i					
				ESR	initial limit	i					
				Visual examination	no visible	damage					
				DCL	initial limit	i					
Vibration	MIL-STD-202, N	Method 204, Condi	tion D	ΔC/C	within ±5%	of initial va	lue				
				DF	initial limit	į					
				ESR	initial limit	i					

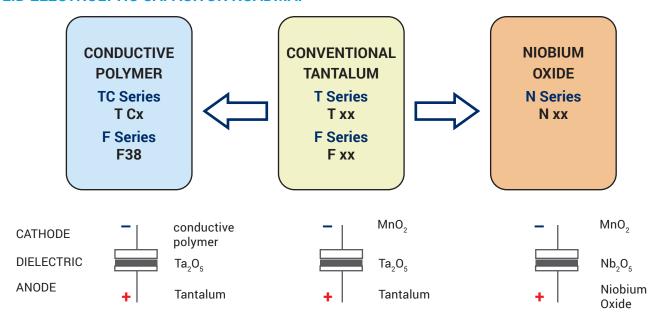
*Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.



Highest Joules/cc Conductive Polymer Solid Electrolytic Chip Capacitors

SOLID ELECTROLYTIC CAPACITOR ROADMAP



FIVE CAPACITOR CONSTRUCTION STYLES

