AES-RZB-V2L-SK-G

RZBoard Development Kit

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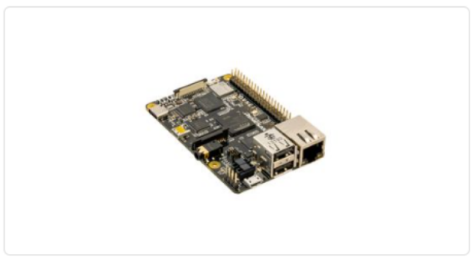

Manufacturer: Avnet Engineering Services

Product Category: Kits & Tools, Evaluation & Development Kits

Avnet Manufacturer Part #: AES-RZB-V2L-SK-G

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Lifecycle

Tool Tip | Print

In Stock : 69

Refresh Stock

Ships in 1 bus. day

Additional inventory

Factory Lead Time: 20 Weeks

Price for: Each **USD $:** **$149.00**

Quantity:

Min: 1 Mult: 1

1

1+ $149.00

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SubTotal: **$149.00**

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Description | Subs/Alternatives(2) | Documents(4)

RZBoard V2L is a power efficient, vision-AI accelerated development board in a popular single board computer format with well supported expansion interfaces. Based on the Renesas RZ/V2L processor, this platform is ideal for development of cost-efficient vision-AI and a range of energy-efficient edge AI applications. It's RZ/V2L processor has two 1.2GHz Arm® Cortex®-A55 cores plus a 200MHz Cortex-M33 core, a MALI 3D GPU and Image Scaling Unit. This processor SoC further differentiates itself with an on-chip DRP-AI accelerator plus H.264 video (1920 x 1080) encode/decode function, making it ideal for implementing cost-effective embedded-vision applications.

RZBoard V2L is engineered in a compact Raspberry Pi form-factor with a versatile set of expansion interfaces, including Gigabit Ethernet, 801.11ac Wi-Fi and Bluetooth 5, two USB 2.0 hosts and a USB 2.0 OTG interface, MIPI DSI and CSI camera interfaces, CANFD interface, Pi-HAT compatible 40-pin expansion header and Click Shuttle expansion header.

The board supports analog audio applications via it's audio codec and stereo headphone jack. It also provides five 12bit ADC inputs for interfacing with analog sensors. 5V input power is sourced via a USB-C connector and managed via a single-chip Renesas RAA215300 PMIC device.

Onboard memory includes 2GB DDR4, 32GB eMMC and 16MB QSPI flash memory, plus microSD slot for removable media.

Software enablement includes CIP Kernel based Linux BSP (maintained for 10 years+) plus reference designs that highlight efficient vision AI implementations using the DRP-AI core. Onboard 10-pin JTAG/SWD mini-header and 4-pin UART header enable the use of an external debugger and USB-serial cable.

Available accessory options include a MIPI 7-inch display, MIPI CSI camera and 5V/3A USB Type C power supply.

ECCN / UNSPSC / COO

DESCRIPTION	VALUE
Country of Origin:	China
ECCN:	3A991.A.2
HTSN:	8471500150
Schedule B:	8471500150

FEEDBACK