

ELECTRONIC PRODUCT/PROCESS CHANGE

Refer to the checked box below to identify the type of notification you are receiving. One box must be checked. Only one box may be checked.

<input checked="" type="checkbox"/>	PCN = Product/Process Change Notification
<input type="checkbox"/>	PCI = Product/Process Change Input Request
<input type="checkbox"/>	PCA = Product/Process Change Approval Request

Refer to the section below to identify relevant information regarding this notification.

PCN/PCI/PCA #:	PCN-C1346
Date:	26-02-18
Customer:	All
Amphenol Originator Name:	Anders Ohlsson
Originator Phone:	+46733327032
Originator e-Mail:	anderso@amphenol-highspeed.com

Change Type (check one)

<input type="checkbox"/>	Dimensions and Tolerances	<input type="checkbox"/>	Material (Not Specified)	<input type="checkbox"/>	Color (Tint)
<input type="checkbox"/>	Delivery Packaging	<input type="checkbox"/>	Performance	<input type="checkbox"/>	Environmental Impact
<input type="checkbox"/>	Outwardly Visible Part Shape	<input type="checkbox"/>	Part Marking	<input checked="" type="checkbox"/>	Other
<input type="checkbox"/>	Material (Specified)	<input type="checkbox"/>	Color (Base)		Manufacturing location change

Description of the Change

All Highspeed I/O connector manufacturing in the current factory in Shenzhen within the ASCA campus will be moving to a new building 100m across the street
 Current address: AM1/AM2 Tangwei Industrial District, Shenzhen New Address: BM2/BM3 Tangwei Industrial District, Shenzhen
 Supply Chain, production tools, processes, factory management and line workers all remain the same

Reason for the Change

Due to rapid growth and high demand for HSIO connector products, the move will allow to expand our production space for current and new products

Part Number(s) Affected

All Highspeed I/O connector parts that we currently produce in Shenzhen
 QSFP (U90) , Mini SAS (FSX & FS1), OSFP (UE62A, UE62B &UE62M), MiniSAS HD (U92), XFP (U79 & UE7X), CXP (U91),SMP9 (U11R & U11P), RCX (UE12), INFINIBAND (U65),
 SFP (U77, UE75, UE78 & UE86), SFP+ (U77, UE76 & UE86), CXP (U91), ULTRAPORT QSFP+ (FS1Z, U95 & U95Z), QSFP+ (FS1, FS1M)
 ULTRAPORT SFP+ (U77, UE76 &UE86), Xcede I/O (UE86G)

This change will affect not only today's part numbers, but any part number(s) utilizing these components in the future.

Refer to the checked box below for specific instructions regarding this notification. Unchecked boxes are not applicable.
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PCN = Product/Process Change Notification	PCI = Product/Process Change Input Request	PCA = Product/Process Change Approval Request
This change was qualified, validated, and has been or will be implemented pursuant to Amphenol internal specification(s). This notification extends to all of your domestic and international facilities. This change has or will become effective as of: 16-03-18 you have questions or require additional information, please contact the Amphenol Originator listed above.	Amphenol is seeking customer input regarding this proposed change in order to understand its potential impact on your process. To provide input, please contact the Amphenol Originator listed above. Please provide your input within fifteen (15) days of the date listed above. Amphenol's planned implementation date for this change is on or about: _____, 200 ____ . In the absence of any customer response whatsoever, this change will be implemented by default. This extends to all of your	Amphenol is seeking customer approval of this proposed change. Amphenol's planned implementation date for this change is on or about: _____, 200 ____ . In the absence of any customer response whatsoever, this change will be implemented by default. This extends to all of your domestic and international facilities. Should you have questions or require additional information, please contact the Amphenol Originator listed above. This change was qualified, validated, and will be implemented

domestic and international facilities. Should you have questions or require additional information, please contact the Amphenol Originator listed above. This change was qualified, validated, and has been or will be implemented pursuant to Amphenol internal specification(s). Please attach customer input to this form and return input to the Amphenol Originator listed above.

pursuant to Amphenol internal specification(s). Please sign below and return to the Amphenol Originator listed above.

Customer's Authorized Representative

Signature

Name: _____

Title: _____

Date: _____

QAF-1011A / REVISION "-"

Moving to new factory building plan		Mar-18					Apr-18					test item1	test item2	test item3	test item4	test item5	
Sl. No	Activities	Week 9	Week 10	week 11	week 12	week 13	week 14	week 15	week 16	week 17	week 18	Solderability	Press-Fit	Salt Spray	Footprint Gauge	Mating Gap	
"11"	Moving to new factory building Schedule LE.			█	█	█	█	█	█	█							
	a. QSFP Schedule LE.			█	█								Y	Y	Y		
	b. MINI-SAS Schedule LE.			█	█							Y		Y		Y	
	c. QSFP Schedule Act.			█	█								Y	Y	Y	Y	
	d. MINI-SAS HD Schedule LE.			█	█									Y	Y	Y	
	e. XFP. CXP. SMP9. RCX. INFINIBAND. Schedule LE.			█	█									Y	Y	Y	
	f. SFP Schedule LE.				█	█	█	█						Y	Y	Y	
	g. SFP+ Schedule LE.					█	█	█	█					Y	Y	Y	Y
	h. CFP Schedule LE.						█	█						Y	Y	Y	
	i. ULTRAPORT QSFP+ Schedule LE.								█	█				Y	Y	Y	
	i. QSFP+ Schedule LE.								█	█			Y	Y	Y	Y	Y
	k. ULTRAPORT SFP+ Schedule LE.								█	█				Y	Y	Y	Y
	l. XCEDE IO Schedule LE.										█	█	Y		Y	Y	Y

test item6
Coplanarity
Y
Y
Y

Moving to new factory building plan		Mar-18					Apr-18			
Sl.No	Activities	Week 9	Week 10	week 11	week 12	week 13	week 14	week 15	week 16	week 17
"11"	Moving to new factory building Schedule LE.	■	■	■	■	■	■	■	■	
	a. QSFP Schedule LE.	■		■						
	b. MINI-SAS Schedule LE.	■		■						
	c. OSFP Schedule Act.	■		■						
	d. MINI-SAS HD Schedule LE.	■		■						
	f. XFP. CXP. SMP9. RCX. INFINIBAND. Schedule LE.	■		■						
	g. SFP Schedule LE.		■	■	■	■				
	h. SFP+ Schedule LE.			■	■		■			
	i. CFP Schedule LE.				■		■			
	i. ULTRAPORT QSFP+ Schedule LE.						■		■	
	k. QSFP+ Schedule LE.						■		■	
	l. ULTRAPORT SFP+ Schedule LE.						■		■	
	m. XCEDE IO Schedule LE.							■		■