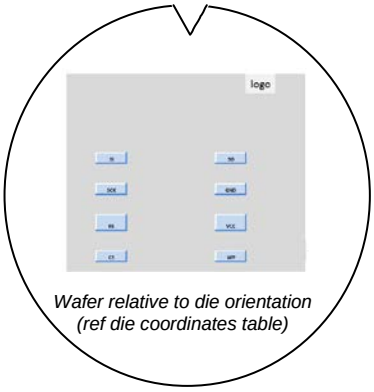
WAFER PRODUCT DATASHEET (ADDENDUM)

Product	AT45DB081E-DWF
Description	8 Mbit, DataFlash, 1.7V – 3.6V VCC
Die Map	 Image reflects relative pad positioning only; not representative of actual die size. Reference coordinates table for actual measurements and pad position  *Wafer relative to die orientation (ref die coordinates table)*
Die Size & Pad Coordinates	

	X (µm)	Y (µm)
Die Size	1570	2282
Scribe Line Width	80	80
Die Step	1650	2362
Pad Opening	65	65
SI	-711	-261.26
SCK	-711	-501.26
RSB	-711	-741.26
CSB	-711	-1021.26
WPB	711	-971.69
VCC	711	-691.69
GND	711	-451.69
SO	711	-211.69

Adesto Technologies Corporation
3600 Peterson Way, Santa Clara, CA 95054 | phone: (408) 400-0578 fax: (408) 400-0721 | www.adestotech.com

Technical Details	
Adesto Product Family	DataFlash
Density	8 Mbit
Operating Vcc	1.7V - 3.6V
ESD	>2kV JESD22-A114
Delivery Option	Wafer- unsawn
Wafer Size (mm)	200
Process Geometry (nm)	110
Die ID	637F6
Wafer Map	Electronic- text file
Manufacturing Facility	UMC
Wafer Thickness (µm) Maximum	725
Back Grind Options	None / Contact Adesto
Back Plane Connection	Floating / Not Required
Backside preparation / metallization	None
Bond wire qualified	AU ☒ CU ☐ AG ☐
Passivation Material	HDP oxide + SiON
Passivation Thickness (Å)	21000
Bond Pad Material	Ti/TiN/AlCu
Bond Pad Thickness (Å)	6000
Good Die per Wafer	Contact Adesto [1]
Active Circuits underneath the bond pad	Yes

[1] Average value; subject to change without notice.

Part Number Ordering Code[2]	Operating Temperature Range	Functional Specification
AT45DB081E-DWF	-40°C to 85°C	http://www.adestotech.com/wp-content/uploads/DS-45DB081E_028.pdf

[2] Handle product in accordance with UV and ESD precautions.

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Revision Level – Release Date	History
A – August 2015	Initial release.
B – November 2015	Updated wafer orientation in die map image. Added footnote on handling.
C – August 2017	Updated address and contact information.

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